## 1. General description

Ultra low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode designed to protect one signal line from the damage caused by ESD and other transients. The device is housed in a SOD882D leadless ultra small Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

### 2. Features and benefits

- · Bidirectional ESD protection of one line
- Ultra small SMD plastic package
- Solderable side pads
- Package height typ. 0.37 mm
- Ultra low diode capacitance C<sub>d</sub> = 2.9 pF
- ESD protection up to 10 kV
- IEC 61000-4-2; level 4 (ESD)
- Ultra low leakage current: I<sub>RM</sub> = 5 nA

## 3. Applications

- · Computers and peripherals
- Audio and video equipment
- · Cellular handsets and accessories
- · High-speed data lines
- Communication systems
- Portable electronics

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	2.9	3.5	pF



## 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	Transparent top view  DFN1006D-2 (SOD882D)	K1 K2

## 6. Ordering information

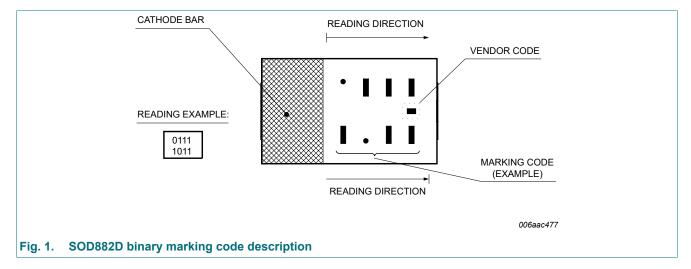
### **Table 3. Ordering information**

Type number Package			
	Name	Description	Version
PESD5V0U1BLD		leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.4 mm body	SOD882D

## 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD5V0U1BLD	0001
	0000



## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maxim	um ratings		'			
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge), T <sub>amb</sub> = 25 °C	[1] [2]	-	10	kV
		IEC 61000-4-2 (air discharge), T <sub>amb</sub> = 25 °C		-	15	kV
		machine model, T <sub>amb</sub> = 25 °C		-	400	V
		MIL-STD-883 (human body model), T <sub>amb</sub> = 25 °C		-	10	kV

- [1] Device stressed with ten non-repetitive ESD pulses.
- [2] Measured from pin 1 to pin 2.

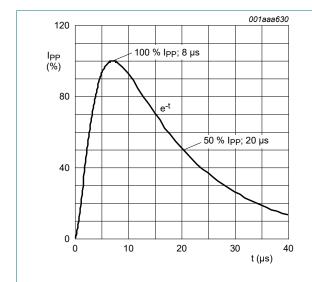


Fig. 2. 8/20 µs pulse waveform according to IEC 61000-4-5

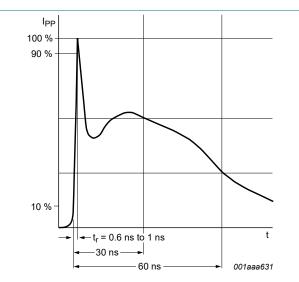


Fig. 3. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	5	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C		5.5	7	9.5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5 V; T <sub>amb</sub> = 25 °C		-	5	100	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	2.9	3.5	pF
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[1]	-	0.8	-	Ω

[1] Non-repetitive current pulse, Transmission Line Pulse (TLP) tp = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.

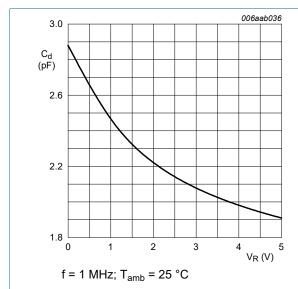


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

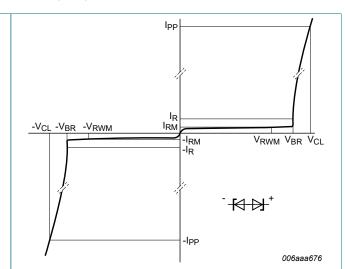
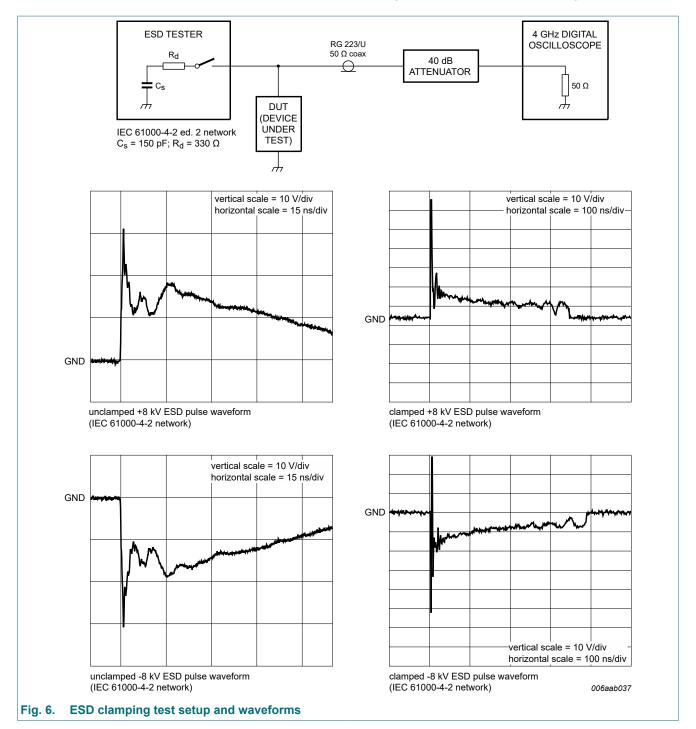
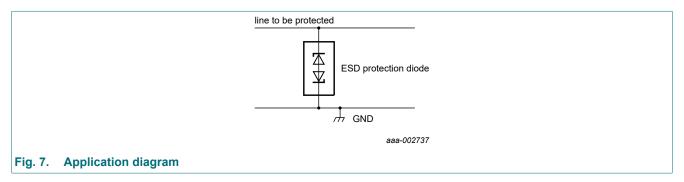


Fig. 5. V-I characteristics for a bidirectional ESD protection diode



## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

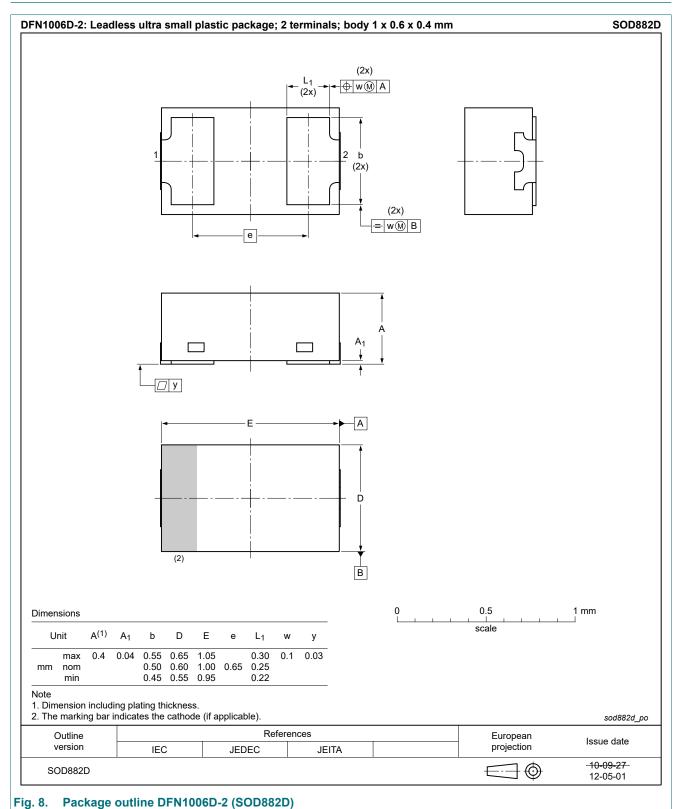


#### Circuit board layout and protection device placement

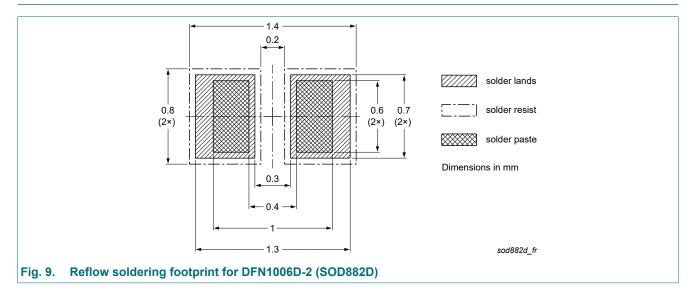
Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## 11. Package outline



# 12. Soldering



# 13. Revision history

## Table 7. Revision history

Table 1. Revision mistor	y y			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0U1BLD v.3	20230401	Product data sheet	-	PESD5V0U1BLD v.2
Modifications:	Product changed to i (-Q) product alternation	eria.com for automotive		
PESD5V0U1BLD v.2	20110727	Product data sheet	-	PESD5V0U1BLD v.1
PESD5V0U1BLD v.1	20110504	Product data sheet	-	-

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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