

PESD2IVN27-T

ESD protection for In-vehicle networks

6 March 2018

Product data sheet

1. General description

ESD protection device in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package designed to protect two automotive In-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

2. Features and benefits

- Reverse stand-off voltage: V_{RWM} = 27 V
- Low clamping voltage: V_{CL}= 36 V at I_{PP} = 3 A
- Typ. diode capacitance matching: ∆C_d/C_d = 0.1 %
- ESD protection up to 30 kV (IEC 61000-4-2)
- ESD protection up to 30 kV (ISO 10605; C = 330 pF, R = 330 Ω)
- ISO 7637-3: Pulse a: V_S = -150 V / Pulse b: V_S= +100 V
- Ultra low leakage current: I_{RM} < 1 nA
- · Qualified according to AEC-Q101 / Automotive grade

3. Applications

ESD protection for In-vehicle network lines in automotive environments

- CAN
- LIN
- FlexRay
- SENT

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	27	V
I _{РРМ}	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	-	3	Α
V _{CL}	clamping voltage	$I_{PPM} = 3 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[1] [2]	-	36	45	V

- [1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	3	K1 4 N
2	K2	cathode (diode 2)		CC CC
3	CC	common cathode		K2 K1 D]
			1 2	006aaa155
			TO-236AB (SOT23)	

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PESD2IVN27-T	TO-236AB	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PESD2IVN27-T	Т3%

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	3	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

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Symbol	Parameter	Conditions		Min	Max	Unit		
ESD maximum	ESD maximum ratings							
V _{ESD}		IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV		
		ISO 10605; contact discharge; C = 330 pF, R = 330 Ω	[2] [3]	-	30	kV		
		ISO 10605; contact discharge; C = 150 pF, R = 330 Ω	[2] [3]	-	30	kV		

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

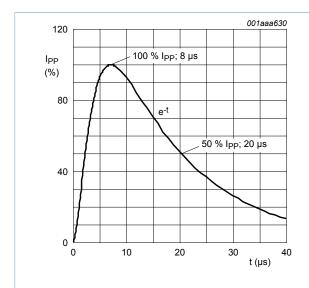


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

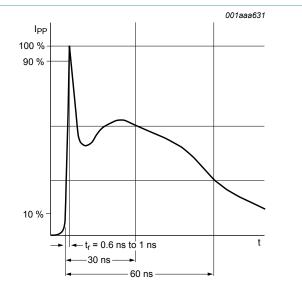


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

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9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	27	V
V_{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	28	33	38	V
I _{RM}	reverse leakage current	V _{RWM} = 27 V; T _{amb} = 25 °C	[1]	-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	13	17	pF
$\Delta C_d/C_d$	diode capacitance		[2]	-	0.1	-	%
	matching	$f = 1 \text{ MHz}; V_R = 2.5 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$	[2]	-	0.1	-	%
V_{CL}	clamping voltage	I_{PPM} = 1 A; t_p = 8/20 µs; T_{amb} = 25 °C	[3] [1]	-	34	43	V
		$I_{PPM} = 3 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[3] [1]	-	36	45	V
		I_{PP} = 16 A; t_p = TLP; T_{amb} = 25 °C	[4] [1]	-	35	-	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[4] [1]	-	0.25	-	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- [2] ΔC_d is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

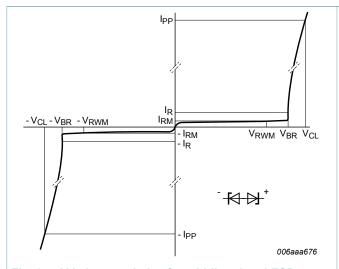


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

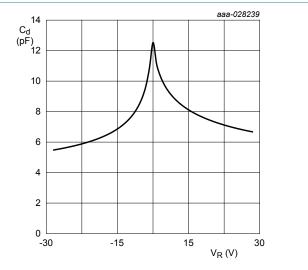


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

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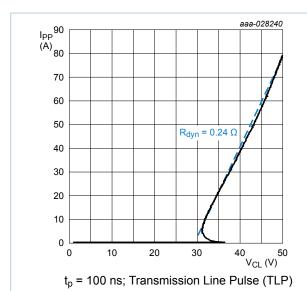
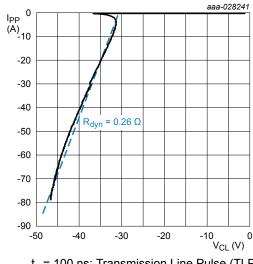
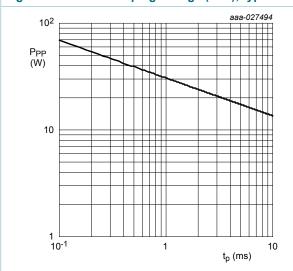


Fig. 5. Positive clamping voltage (TLP); typical values



t_p = 100 ns; Transmission Line Pulse (TLP)



Peak pulse power as a function of exponential Fig. 7. pulse duration; typical values

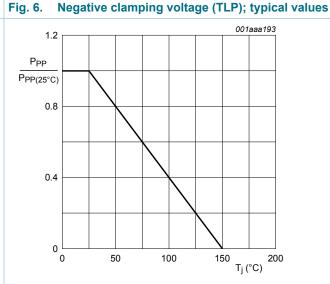
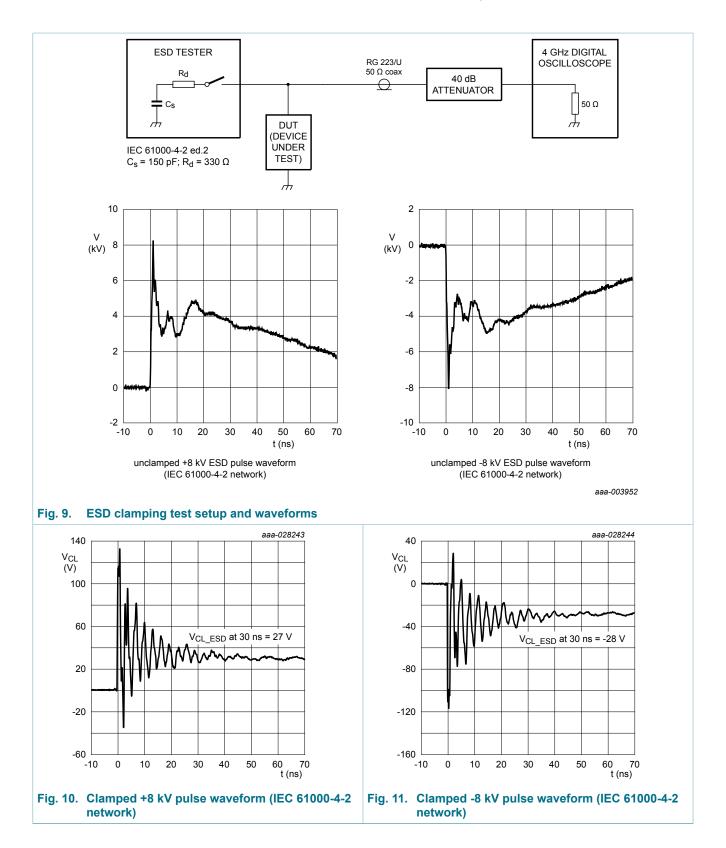


Fig. 8. Relative variation of peak pulse power as a function of junction temperature; typical values

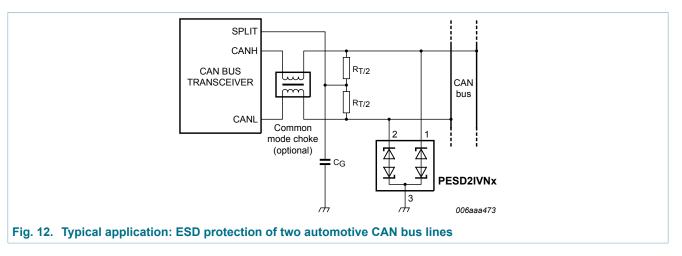
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10. Application information

The device is designed for the protection of two automotive IVN bus line from the damage caused by ESD and surge pulses.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

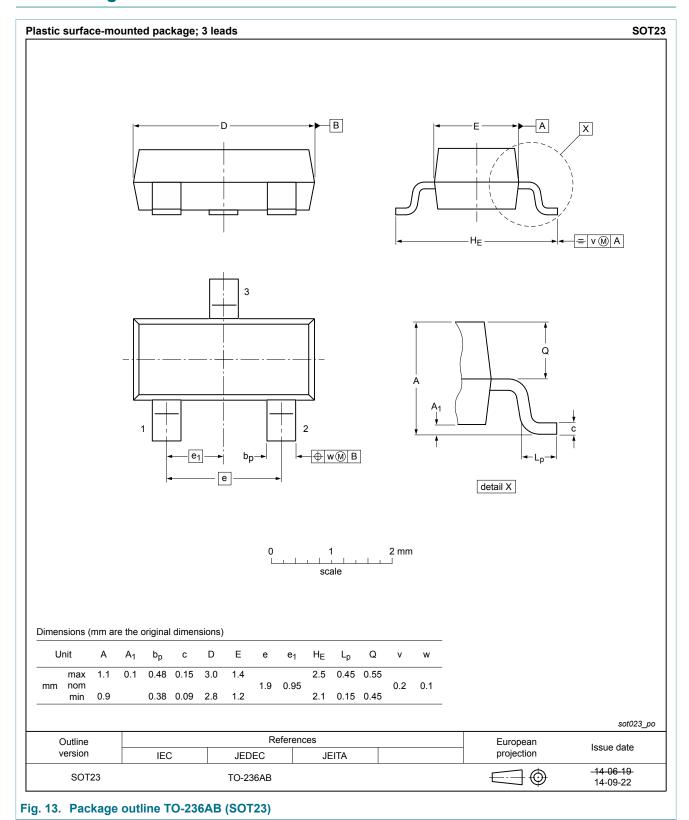
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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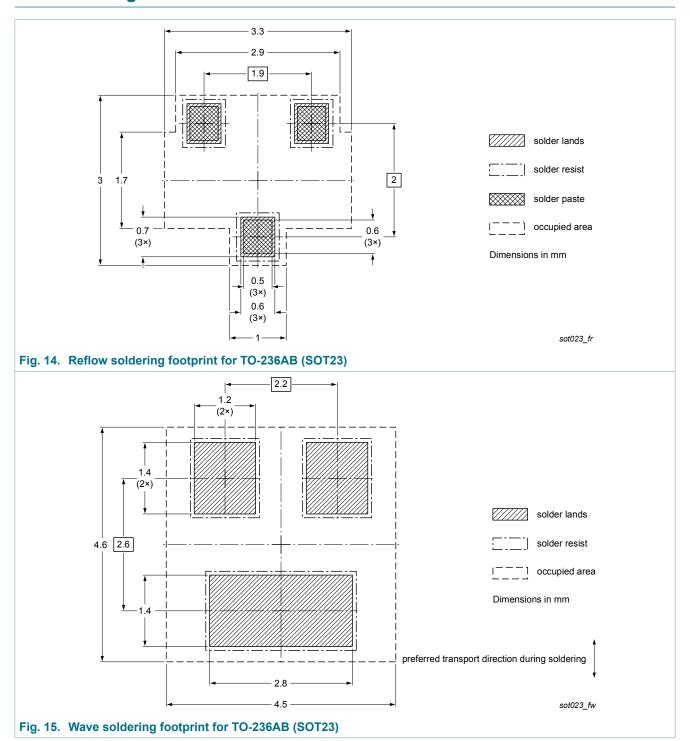
12. Package outline



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13. Soldering



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14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD2IVN27-T v.1	20180306	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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