

# PESD2CANFD60LT-Q

# ESD protection for in-vehicle networks

23 January 2025

**Product data sheet** 

### 1. General description

ESD protection device in a small SOT23 Surface-Mounted Device (SMD) plastic package, designed to protect two automotive in-vehicle network bus lines from the damage caused by ElectroStatic Discharge (ESD) and other transients.

### 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 60 V
- Low capacitance: C<sub>d</sub> = 6 pF
- ESD protection up to 24 kV (contact discharge; C = 150 pF, R = 330 Ω)
- ISO 7637-2 pulse 3a/3b = -300 V / +300 V
- ISO 7637-2 pulse 2a = +95 V
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

ESD protection for in-vehicle network lines in automotive environments

- · Designed for use in electric and hybrid vehicles with 48V board net
- CAN / CAN-FD
- FlexRay
- SENT

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	60	V
I <sub>PPM</sub>	rated peak pulse current	$t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1] [2]	-	-	4	А
V <sub>CL</sub>	clamping voltage	$I_{PP} = 4 \text{ A}; t_p = 8/20  \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[2] [1]	-	77	-	V

- [1] Device stressed with 8/20  $\mu$ s exponential decay waveform according to IEC 61000-4-5.
- [2] Measured from pin 1 to 3 or 2 to 3.



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	3	
2	K2	cathode (diode 2)		к1 — Ы
3	CC	common cathode	SOT23	CC CC 006aaa155

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PESD2CANFD60LT-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

# 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PESD2CANFD60LT-Q	%QE

[1] % = placeholder for manufacturing site code

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1] [2]	-	4	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maxim	um ratings					
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2; contact discharge; T <sub>amb</sub> = 25 °C	[2] [3]	-	24	kV
		ISO 10605; contact discharge; C = 330 pF, R = 330 Ω; T <sub>amb</sub> = 25 °C	[2] [3]	-	24	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 Ω; T <sub>amb</sub> = 25 °C	[2] [3]	-	24	kV

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- 2] Measured from pin 1 to 3 or 2 to 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

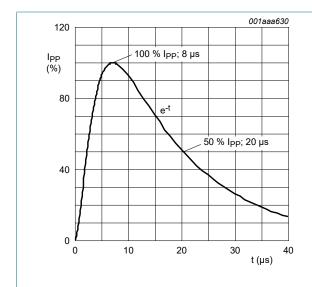


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

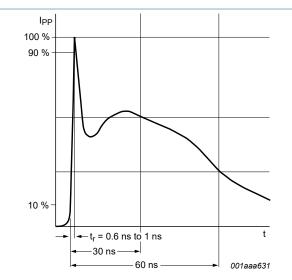


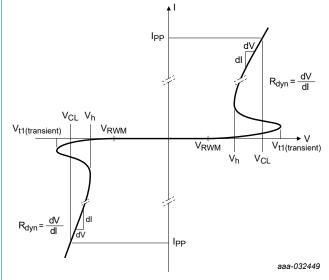
Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### 9. Characteristics

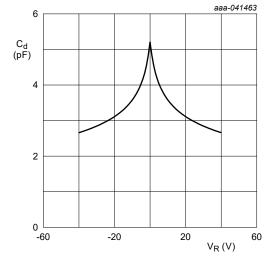
**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	60	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C	[1]	62	68	82	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 60 V; T <sub>amb</sub> = 25 °C	[1]	-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	[1]	-	5.2	6	pF
$\Delta C_d/C_d$	diode capacitance		[2]	-	0.5	-	%
	matching	f = 1 MHz; V <sub>R</sub> = 2.5 V; T <sub>amb</sub> = 25 °C	[2]	-	0.5	-	%
		f = 1 MHz; V <sub>R</sub> = -2.5 V; T <sub>amb</sub> = 25 °C	[2]	-	0.5	-	%
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1] [3]	-	67	-	V
		I <sub>PP</sub> = 4 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1] [3]	-	77	-	V
		I <sub>PP</sub> = 16 A; t <sub>p</sub> = 100 ns; T <sub>amb</sub> = 25 °C	[1] [4]	-	79	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; t <sub>p</sub> = 100 ns; T <sub>amb</sub> = 25 °C	[1] [4]	-	1	-	Ω

- [1] Measured from pin 1 to 3 or 2 to 3.
- [2]  $\Delta C_d$  is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.





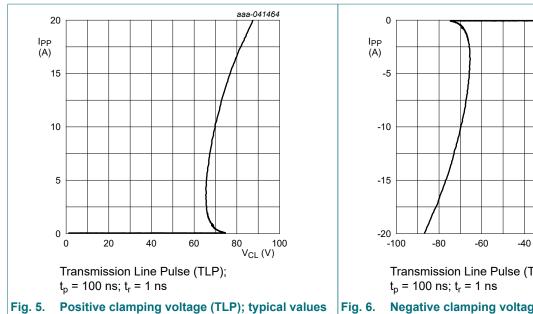


 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^{\circ}\text{C}$ 

Diode capacitance as a function of reverse voltage; typical values

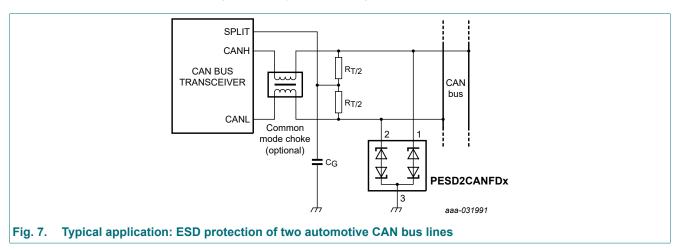
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### **ESD** protection for in-vehicle networks



## 10. Application information

The device is designed for the protection of two automotive in-vehicle bus lines, e.g. CAN (FD), from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### 11. Test information

### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline

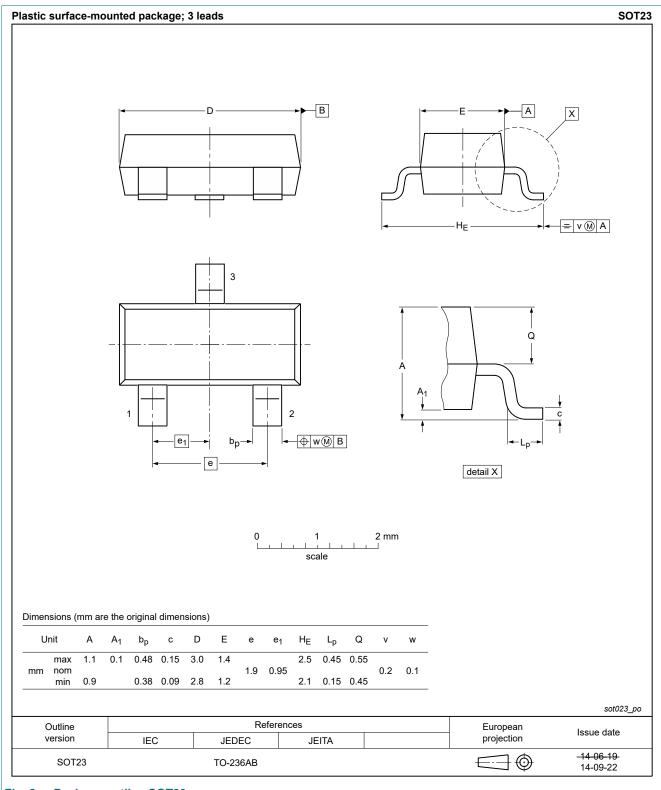
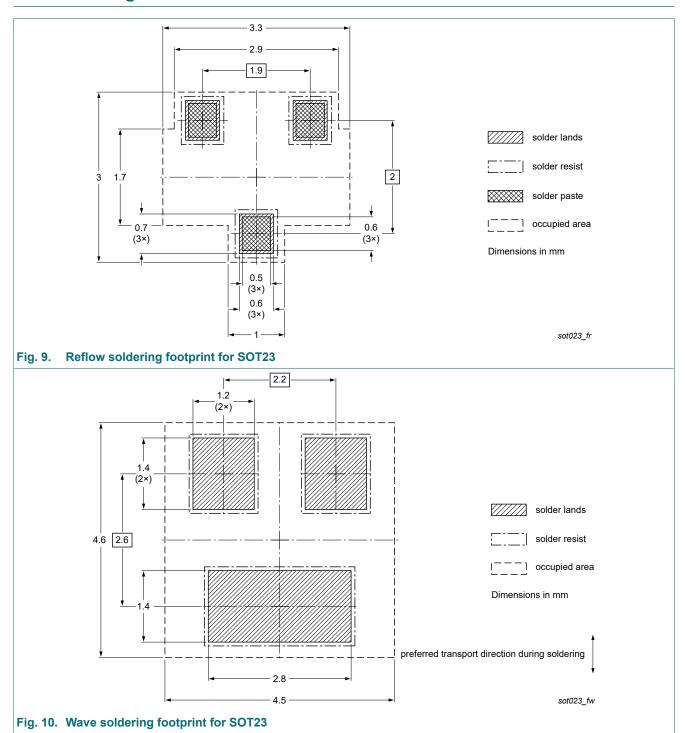


Fig. 8. Package outline SOT23

# 13. Soldering



# 14. Revision history

### **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD2CANFD60LT-Q	20250123	Product data sheet	-	-
v.1				

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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