

Dual Enhancement Mode Power MOSFET (N- and P- Channel)

DESCRIPTION

The PED2312A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

- P-Channel

$V_{DS} = -20V, I_D = -3A$

$R_{DS(ON)} = 110m\Omega @ V_{GS}=-2.5V$

$R_{DS(ON)} = 85 m\Omega @ V_{GS}=-4.5V$

- N-Channel

$V_{DS} = 20V, I_D = 3A$

$R_{DS(ON)} = 65m\Omega @ V_{GS}=2.5V$

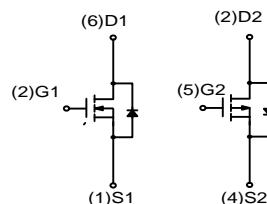
$R_{DS(ON)} = 50 m\Omega @ V_{GS}=4.5V$

Application

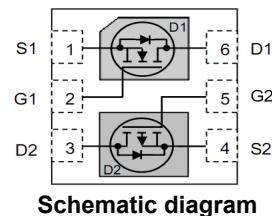
- PWM applications

- Load switch

- Power management



N-Channel MOSFET P-Channel MOSFET



Schematic diagram



DFN2X2-6L top view

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	20	-20	V
Gate-Source Voltage	V_{GS}	± 10	± 12	V
Drain Current-Continuous	I_D	3.5	-3	A
Drain Current -Pulsed (Note 1)	I_{DM}	10	-10	A
Maximum Power Dissipation	P_D	0.8	0.8	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150		°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	100	°C/W
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N-Channel**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V_{DSS}	$V_{GS}=0V, I_D=250\mu A$	20		-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.7	1.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=2.5V, I_D=2.5A$	-	65	90	$m\Omega$
		$V_{GS}=4.5V, I_D=2.9A$	-	50	65	$m\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=2.9A$	-	8	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=10V, V_{GS}=0V, F=1.0MHz$	-	300	-	PF
Output Capacitance	C_{oss}		-	120	-	PF
Reverse Transfer Capacitance	C_{rss}		-	80	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=2.9A, V_{GS}=4.5V, R_{GEN}=6\Omega$	-	10	15	nS
Turn-on Rise Time	t_r		-	50	85	nS
Turn-Off Delay Time	$t_{d(off)}$		-	17	45	nS
Turn-Off Fall Time	t_f		-	10	20	nS
Total Gate Charge	Q_g	$V_{DS}=10V, I_D=2.9A, V_{GS}=4.5V$	-	4.0	10	nC
Gate-Source Charge	Q_{gs}		-	0.65	-	nC
Gate-Drain Charge	Q_{gd}		-	1.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_s=2.9A$	-	0.75	1.2	V
Diode Forward Current (Note 2)	I_s		-	-	2.9	A

P-Channel**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-20		-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-20V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.45	-0.7	-1	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-4.5V, I_D=-2.5A$	-	85	110	$m\Omega$
		$V_{GS}=-2.5V, I_D=-2A$	-	110	140	$m\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-2.8A$	-	9.5	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=-10V, V_{GS}=0V, F=1.0MHz$	-	405	-	PF
Output Capacitance	C_{oss}		-	75	-	PF
Reverse Transfer Capacitance	C_{rss}		-	55	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-10V, I_D=-1A$ $V_{GS}=-4.5V, R_{GEN}=10\Omega$	-	11	-	nS
Turn-on Rise Time	t_r		-	35	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	30	-	nS
Turn-Off Fall Time	t_f		-	10	-	nS
Total Gate Charge	Q_g	$V_{DS}=-10V, I_D=-3A, V_{GS}=-2.5V$	-	3.3	12	nC
Gate-Source Charge	Q_{gs}		-	0.7	-	nC
Gate-Drain Charge	Q_{gd}		-	1.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=1.3A$	-	-	-1.2	V
Diode Forward Current (Note 2)	I_S		-	-	-1.3	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

N-CHANNEL TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

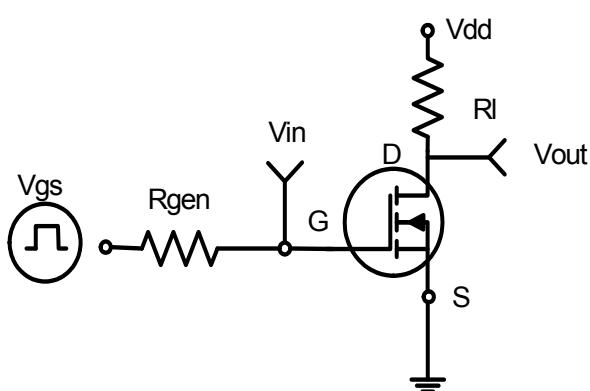


Figure 1:Switching Test Circuit

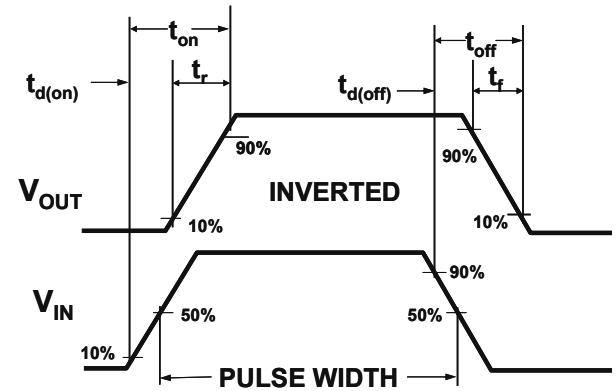


Figure 2:Switching Waveforms

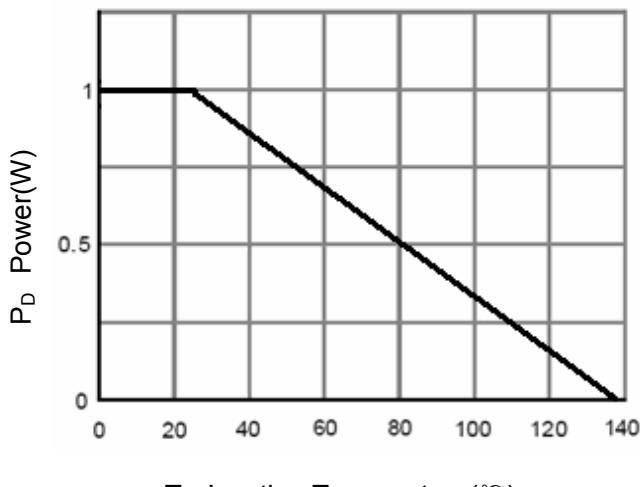


Figure 3 Power Dissipation

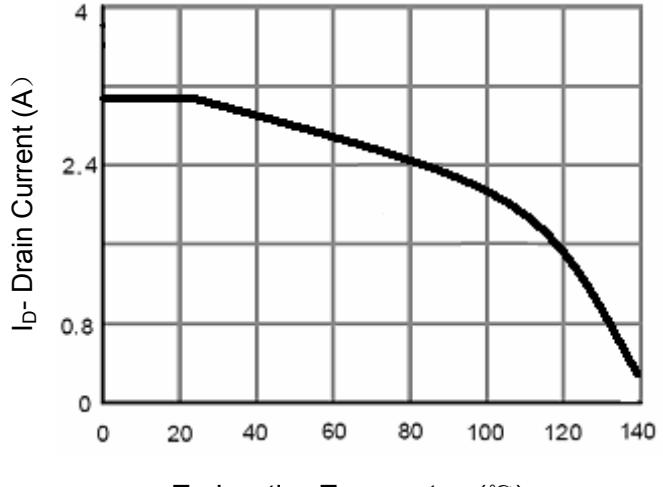


Figure 4 Drain Current

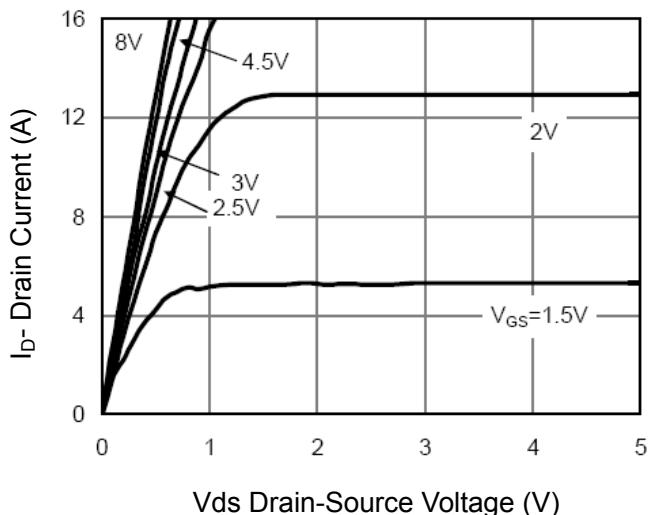


Figure 5 Output CHARACTERISTICS

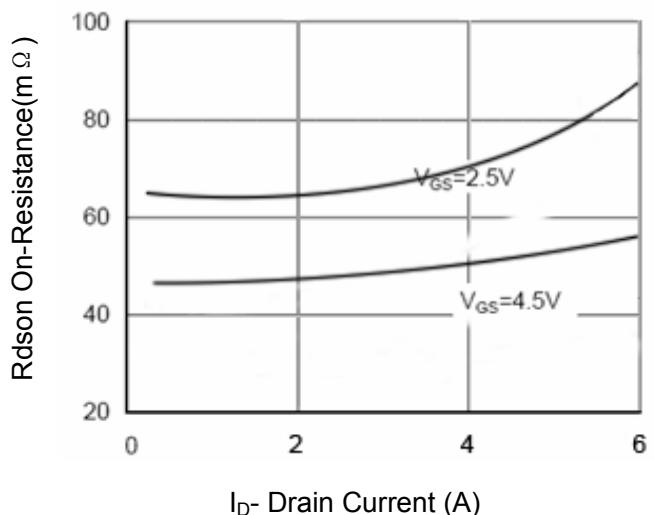
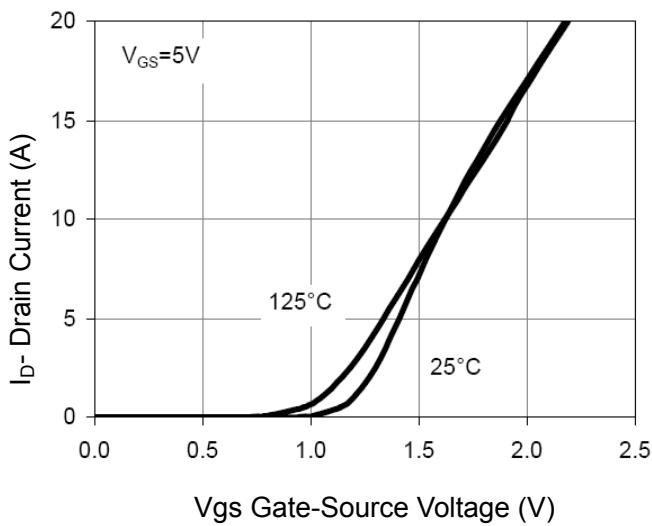
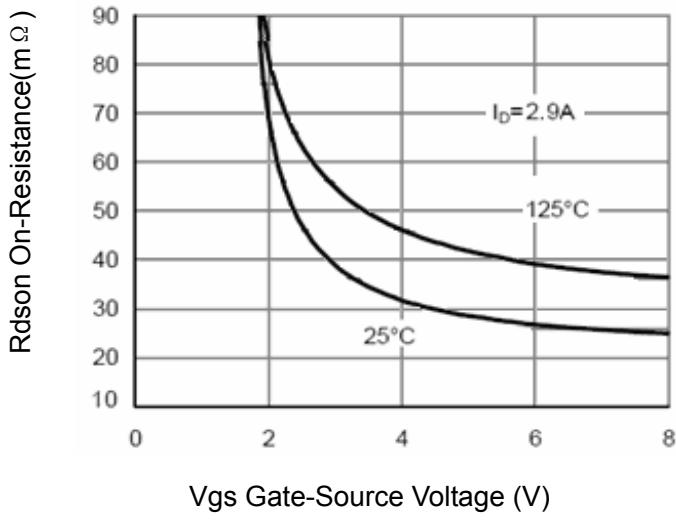
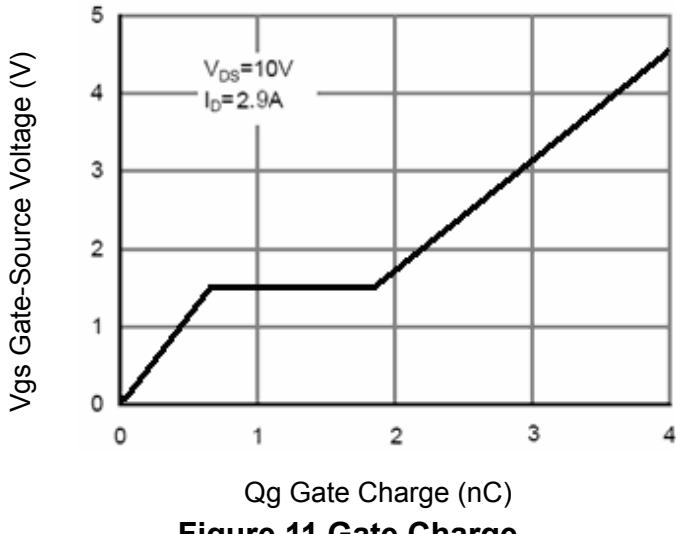
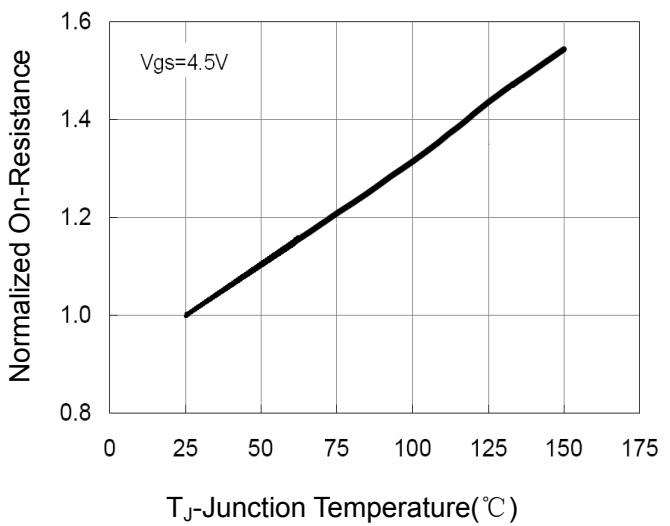
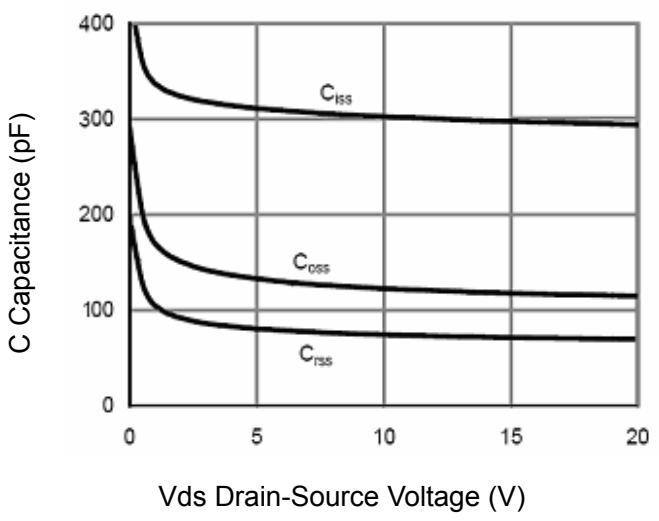
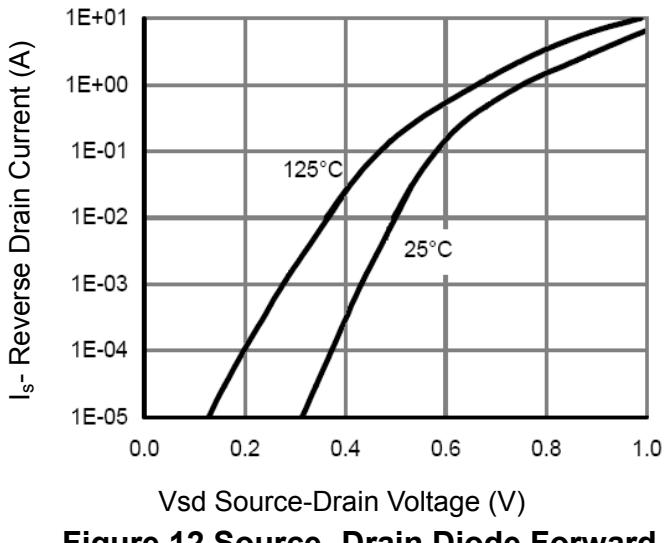


Figure 6 Drain-Source On-Resistance

**Figure 7 Transfer Characteristics****Figure 9 R_{DSON} vs V_{GS}** **Figure 11 Gate Charge****Figure 8 Drain-Source On-Resistance****Figure 10 Capacitance vs V_{DS}** **Figure 12 Source-Drain Diode Forward**

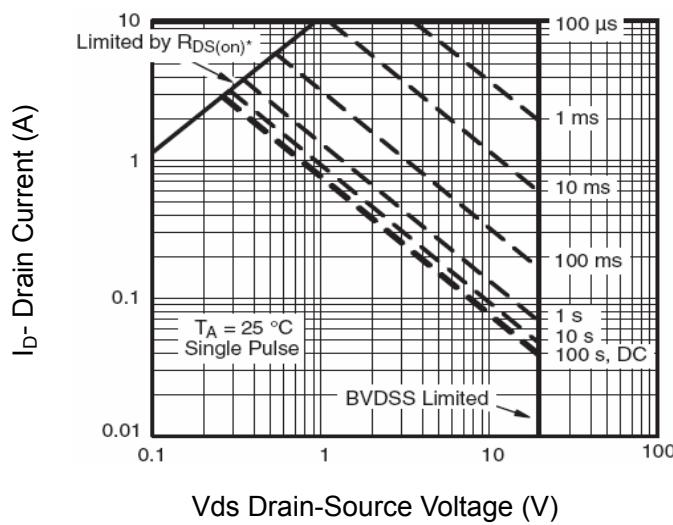


Figure 13 Safe Operation Area

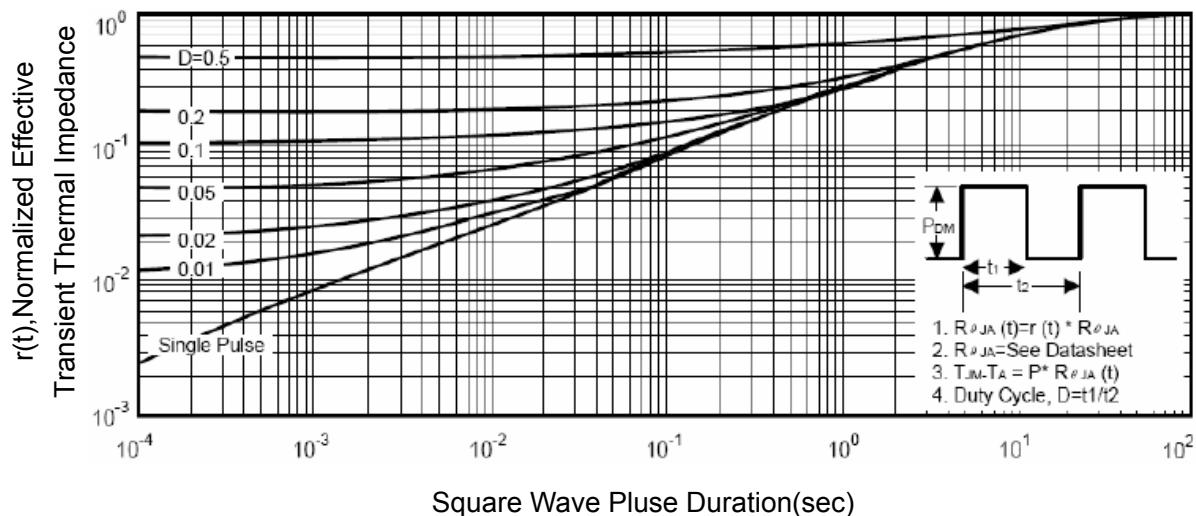


Figure 14 Normalized Maximum Transient Thermal Impedance

P-CHANNEL TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

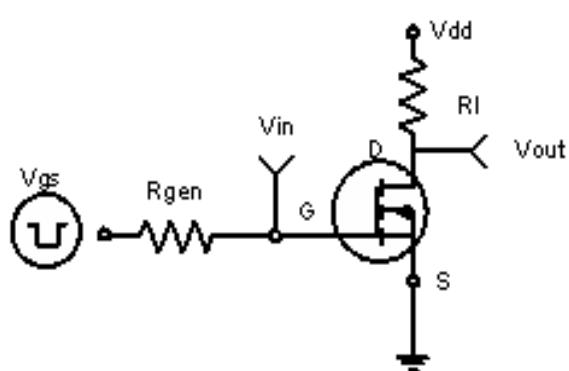


Figure 1:Switching Test Circuit

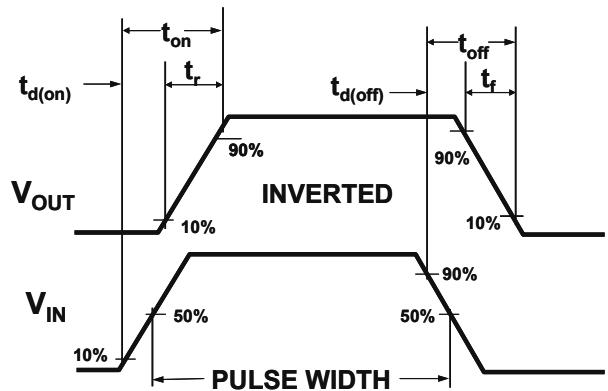


Figure 2:Switching Waveforms

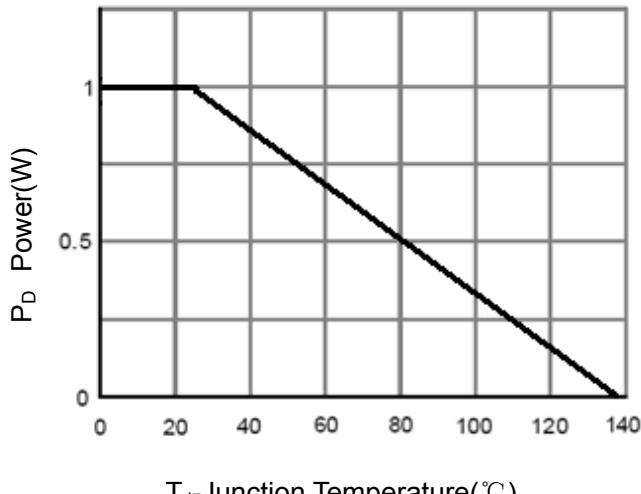


Figure 3 Power Dissipation

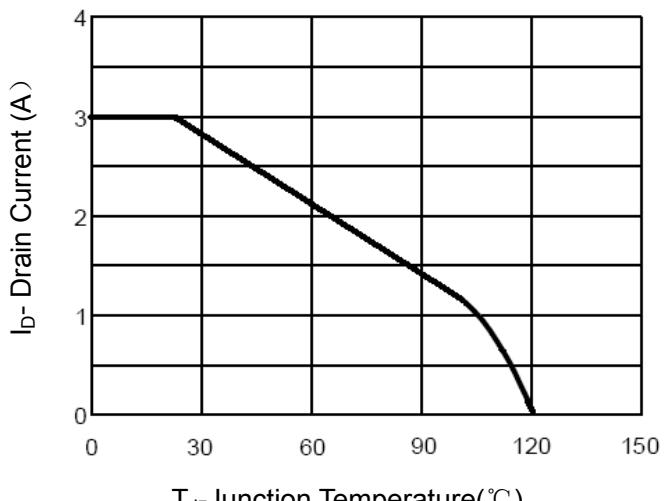


Figure 4 Drain Current

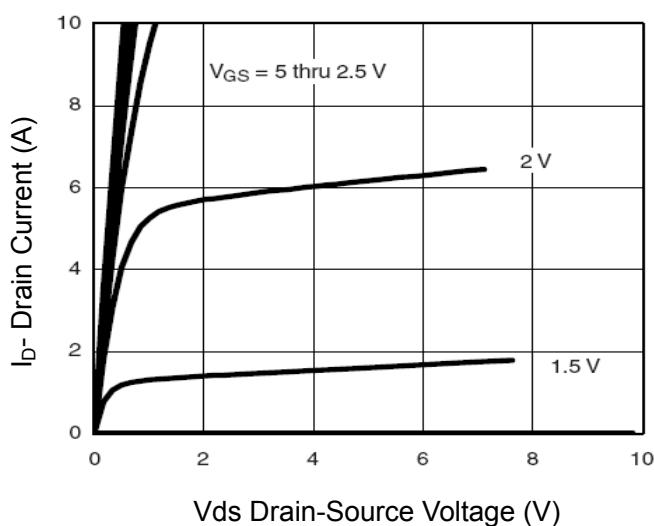


Figure 5 Output CHARACTERISTICS

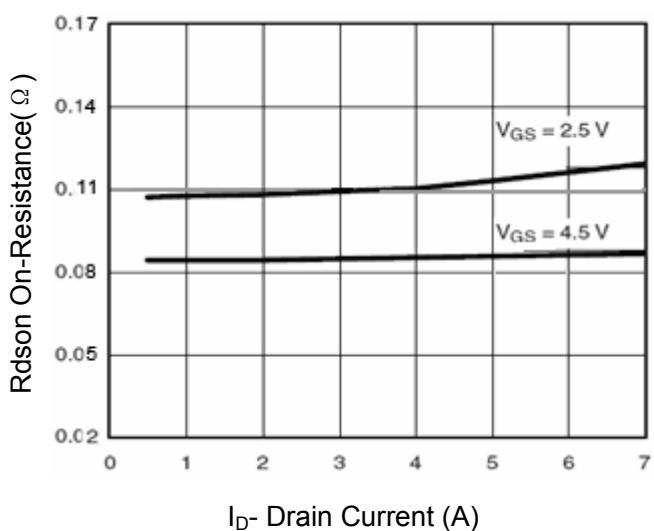
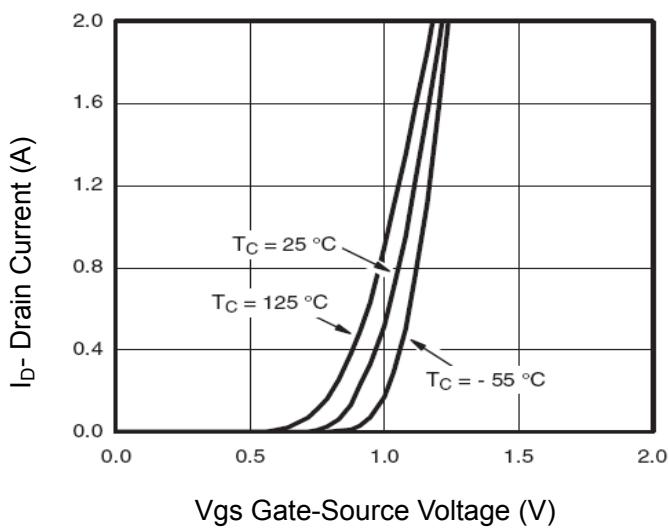
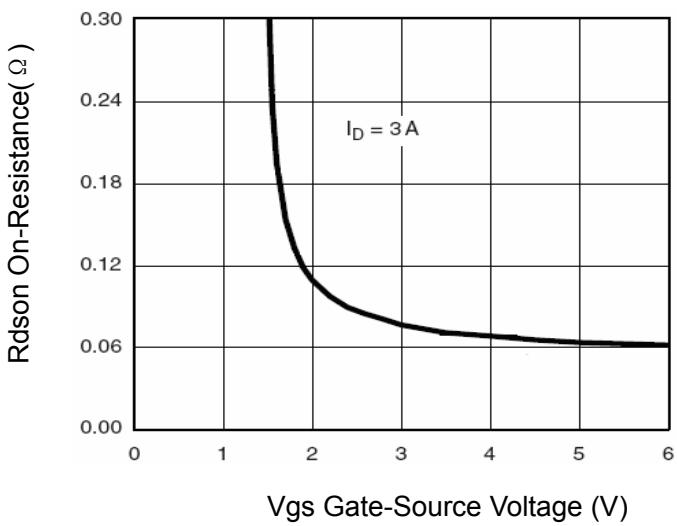
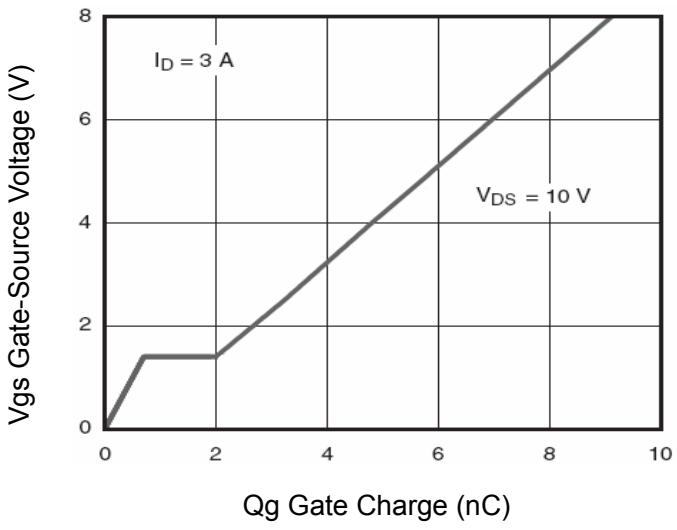
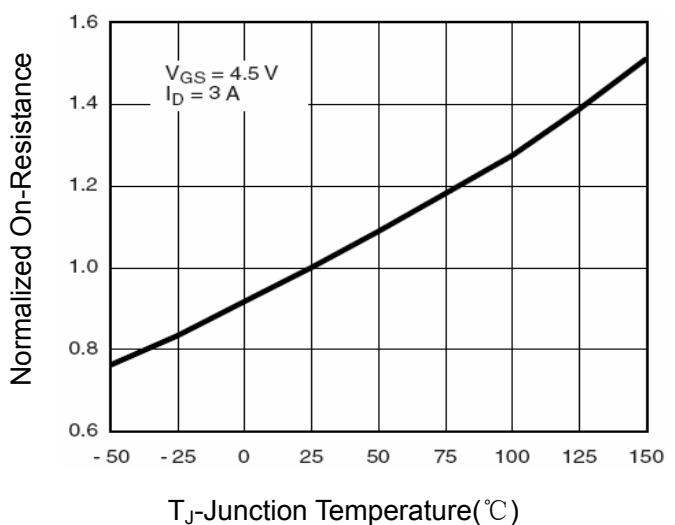
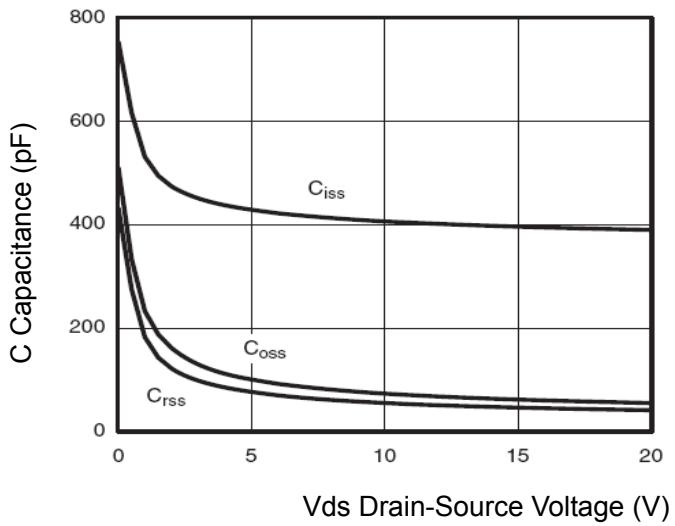
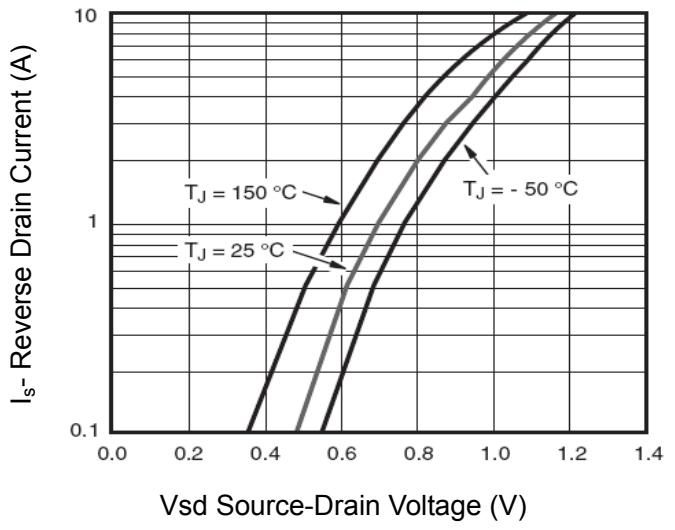
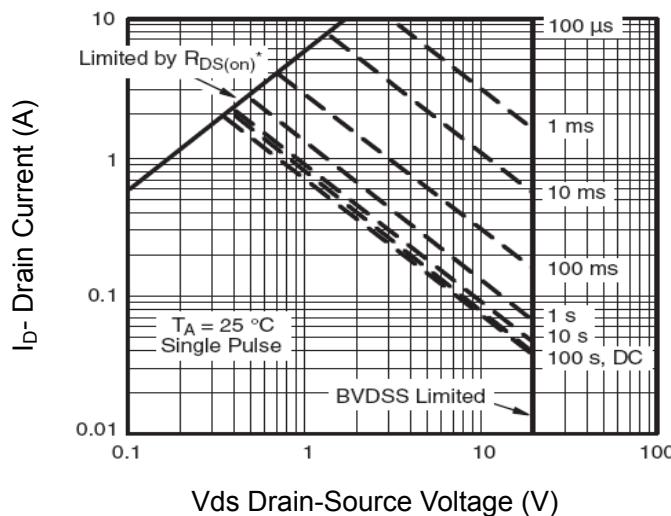
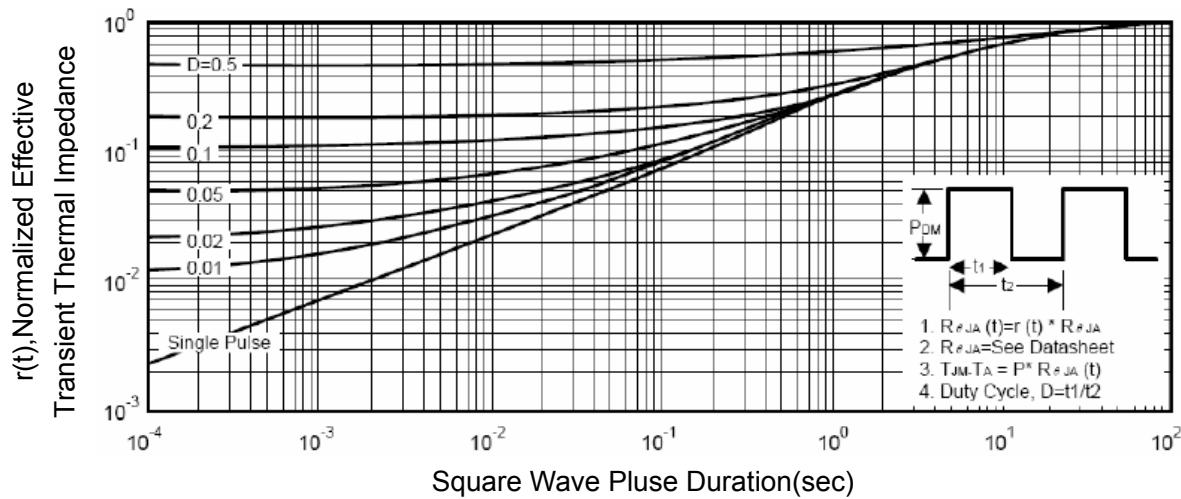
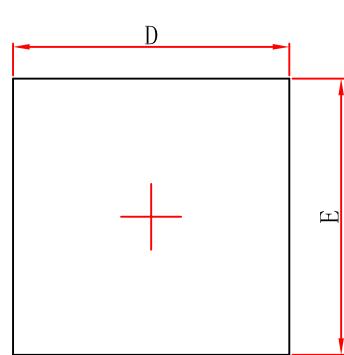


Figure 6 Drain-Source On-Resistance

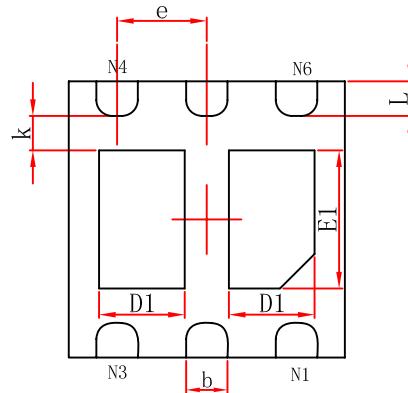
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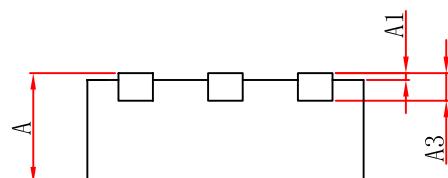
DFN2X2-6L PACKAGE INFORMATION



Top View



Bottom View



Side View

Symbol	Dimension in Millimeters	
	Min.	Max.
A	0.700	0.800
A1	0.000	0.050
A3	0.203REF	
D	1.900	2.100
E	1.900	2.100
E1	0.750	0.850
D1	0.600	0.700
k	0.200MIN	
b	0.250	0.350
e	0.650TYP	
L	0.250	0.350