

NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****PE6W2EA**

PDFN 3x3P

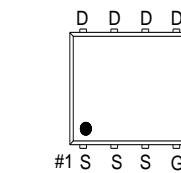
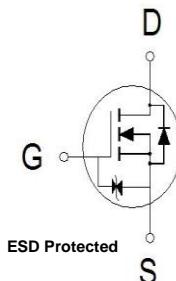
Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(on)}$	I_D
30V	18mΩ	24A

**Features**

- Pb-Free, Halogen Free and RoHS compliant.
- Low $R_{DS(on)}$ to Minimize Conduction Losses.
- Ohmic Region Good $R_{DS(on)}$ Ratio.
- Optimized Gate Charge to Minimize Switching Losses.
- Products Integrated ESD diode with ESD Protected up to 2KV.



G. GATE
D. DRAIN
S. SOURCE

100% UIS Tested
100% Rg Tested

Applications

- Protection Circuits Applications.
- Computer for DC to DC Converters Applications.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ⁴	$T_C = 25^\circ C$	I_D	24	A
	$T_C = 100^\circ C$		15	
Pulsed Drain Current ¹		I_{DM}	36	A
Continuous Drain Current	$T_A = 25^\circ C$	I_D	7.8	
	$T_A = 70^\circ C$		6.2	
Avalanche Current ⁵		I_{AS}	21.4	
Avalanche Energy ⁵	$L = 0.02mH$	E_{AS}	4.6	mJ
Power Dissipation	$T_C = 25^\circ C$	P_D	19	W
	$T_C = 100^\circ C$		7.7	
Power Dissipation ³	$T_A = 25^\circ C$	P_D	2	W
	$T_A = 70^\circ C$		1.3	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****PE6W2EA**

PDFN 3x3P

Halogen-Free & Lead-Free

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10s$	$R_{\theta JA}$		62	°C / W
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$		96	
Junction-to-Case	Steady-State	$R_{\theta JC}$		6.5	

¹Pulse width limited by maximum junction temperature.²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ C$.³The Power dissipation is based on $R_{\theta JA} t \leq 10s$ value.⁴The maximum current rating is package limited.⁵ $V_{DD} = 50V$, $V_{GS} = 10V$, starting $T_J = 25^\circ C$.**ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ C$, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.75	2.2	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 16V$			± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			1	μA
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ C$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 6A$		18	27	$m\Omega$
		$V_{GS} = 10V, I_D = 7A$		13	18	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 7A$		37		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		376		pF
Output Capacitance	C_{oss}			76		
Reverse Transfer Capacitance	C_{rss}			48		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		3.1		Ω
Total Gate Charge ²	Q_g	$V_{DS} = 15V, V_{GS} = 10V, I_D = 7A$		7.6		nC
				4.2		
Gate-Source Charge ²	Q_{gs}			1		
Gate-Drain Charge ²	Q_{gd}			1.9		
Turn-On Delay Time ²	$t_{d(on)}$			5.5		
Rise Time ²	t_r			35		
Turn-Off Delay Time ²	$t_{d(off)}$	$I_D \geq 7A, V_{GS} = 10V, R_{GEN} = 6\Omega$		15		nS
Fall Time ²	t_f			46		

NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****PE6W2EA**

PDFN 3x3P

Halogen-Free & Lead-Free

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)

Continuous Current ³	I _S				17	A
Forward Voltage ¹	V _{SD}	I _F = 7A, V _{GS} = 0V			1.1	V
Reverse Recovery Time	t _{rr}	I _F = 7A, dI _F /dt = 100A / μS		4.7		nS
Reverse Recovery Charge	Q _{rr}			0.9		nC

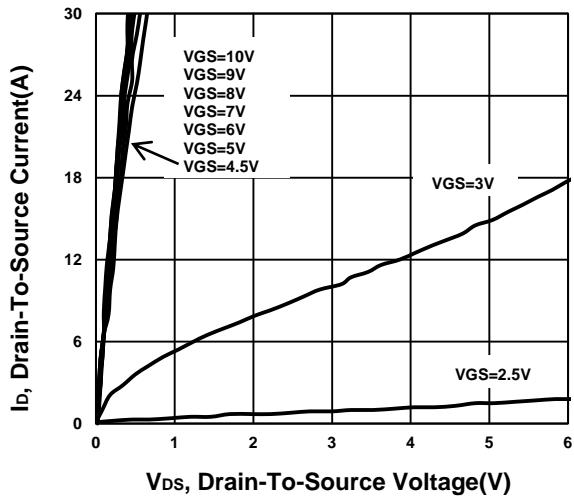
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.²Independent of operating temperature.³The maximum current rating is package limited.

NIKO-SEM

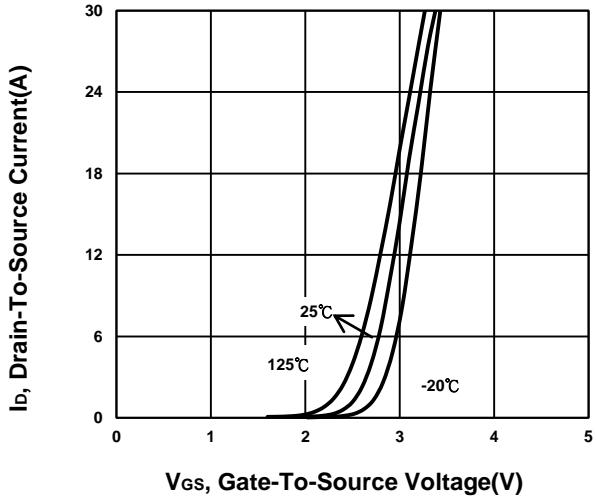
**N-Channel Enhancement Mode
Field Effect Transistor**

PE6W2EA
PDFN 3x3P
Halogen-Free & Lead-Free

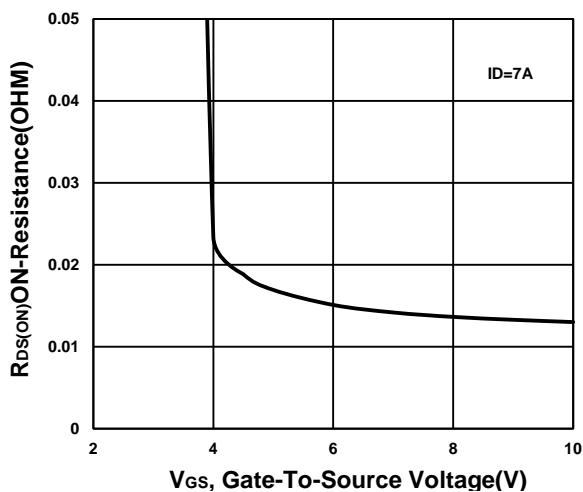
Output Characteristics



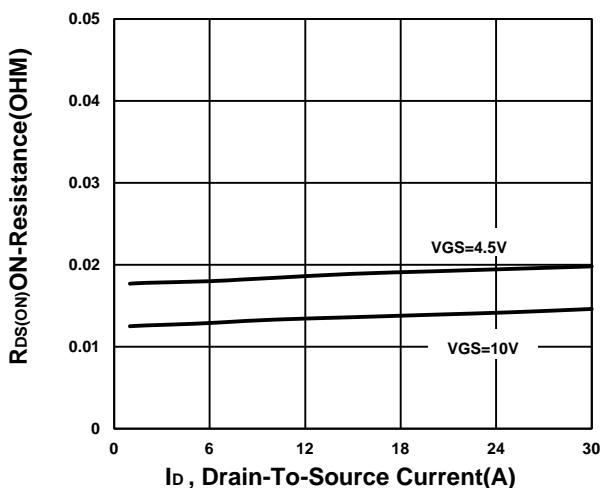
Transfer Characteristics



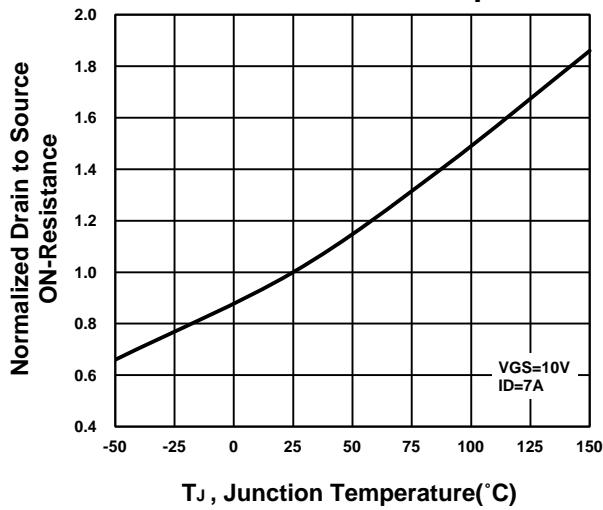
On-Resistance VS Gate-to-Source Voltage



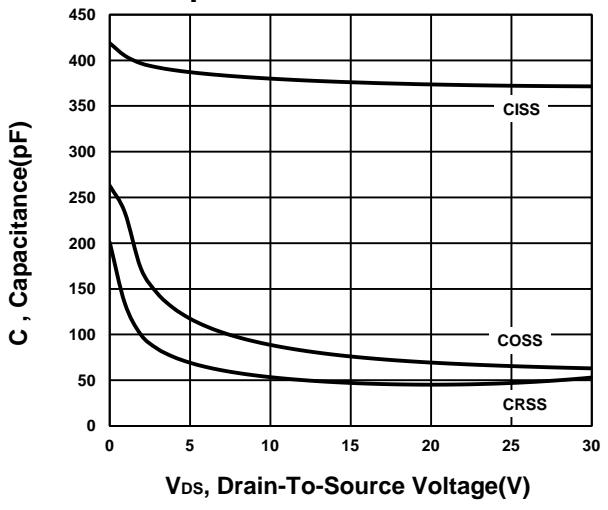
On-Resistance VS Drain Current

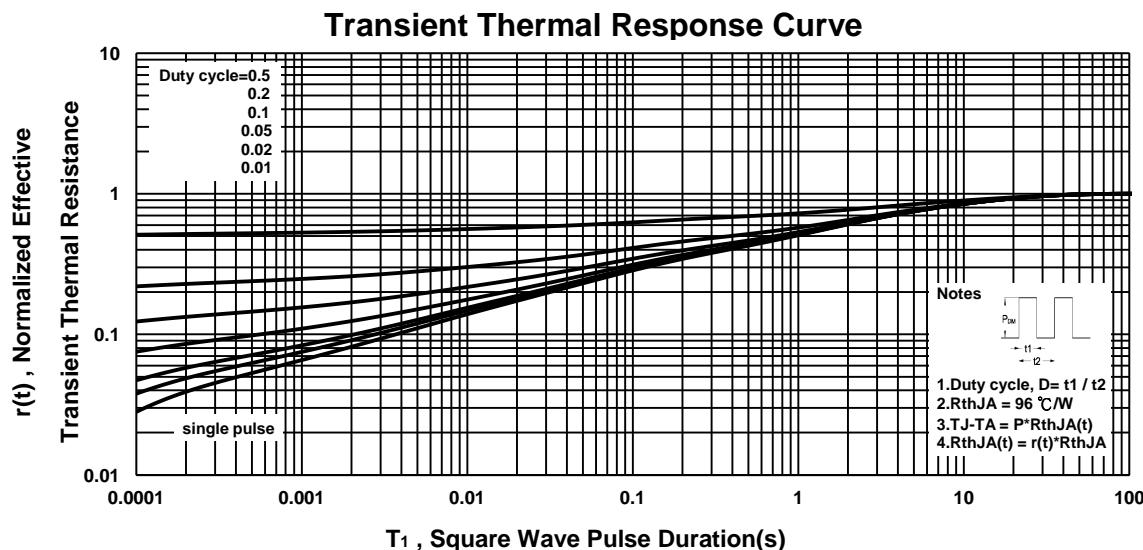
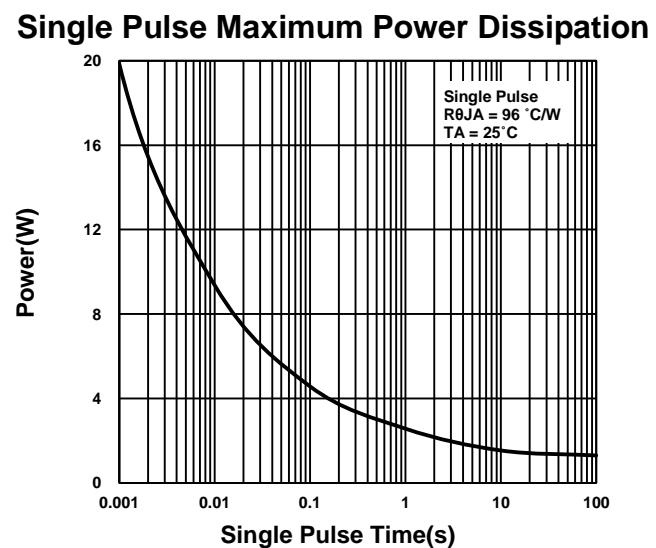
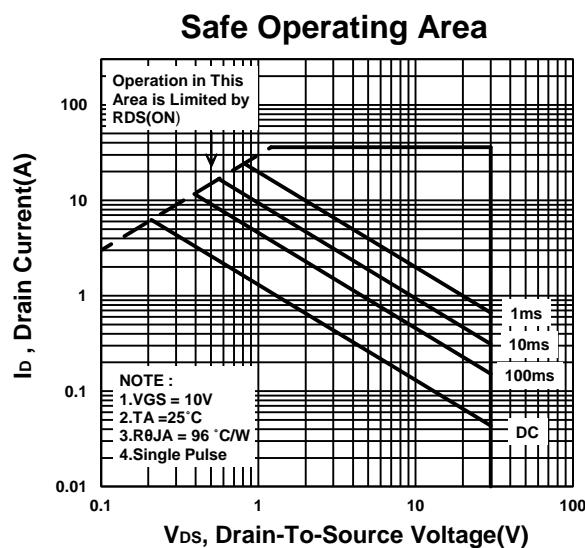
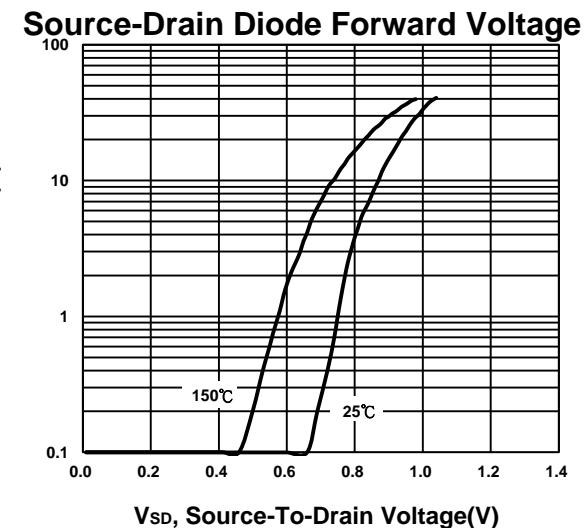
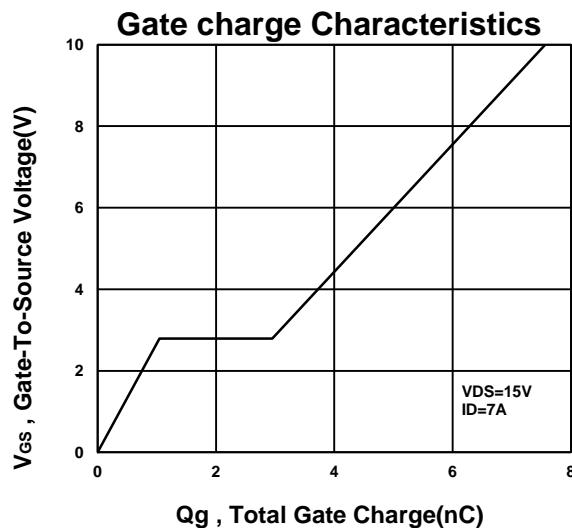


On-Resistance VS Temperature



Capacitance Characteristic



NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****PE6W2EA
PDFN 3x3P
Halogen-Free & Lead-Free**

NIKO-SEM

**N-Channel Enhancement Mode
Field Effect Transistor**

PE6W2EA
PDFN 3x3P
Halogen-Free & Lead-Free

