

N-Channel Enhancement Mode Power MOSFET

Description

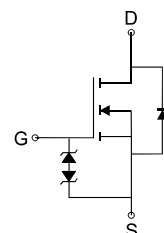
The PE2308A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a battery protection or in other switching application.

General Features

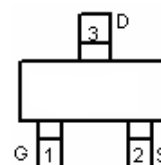
- $V_{DS} = 20V, I_D = 6A$
 $R_{DS(ON)} = 16m\Omega @ V_{GS}=4.5V$
 $R_{DS(ON)} = 21m\Omega @ V_{GS}=2.5V$
- High power and current handling capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	± 12	V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	6.0	A
	$T_A = 70^\circ\text{C}$		3.6	
Drain Current-Pulsed (Note 1)		I_{DM}	15.0	A
Maximum Power Dissipation		P_D	1.25	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 To 150	$^\circ\text{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	100	$^\circ\text{C}/\text{W}$
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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	20	22	-	V

Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V, V _{DS} =0V	-	-	±10	uA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5	0.7	1.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4.5A	-	16	22	mΩ
		V _{GS} =2.5V, I _D =4.0A	-	21	28	mΩ
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =4A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{iss}	V _{DS} =8V, V _{GS} =0V, F=1.0MHz	-	500	-	PF
Output Capacitance	C _{Oss}		-	300	-	PF
Reverse Transfer Capacitance	C _{rss}		-	140	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =10V, I _D =1A V _{GS} =4.5V, R _{GEN} =6Ω	-	20	40	nS
Turn-on Rise Time	t _r		-	18	40	nS
Turn-Off Delay Time	t _{d(off)}		-	60	108	nS
Turn-Off Fall Time	t _f		-	28	56	nS
Total Gate Charge	Q _g	V _{DS} =10V, I _D =3A, V _{GS} =4.5V	-	10	15	nC
Gate-Source Charge	Q _{gs}		-	2.3	-	nC
Gate-Drain Charge	Q _{gd}		-	2.9	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =1A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	6.0	A

Notes:

1. Repetitive rating: pulse width limited by maximum junction temperature.
2. Surface mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

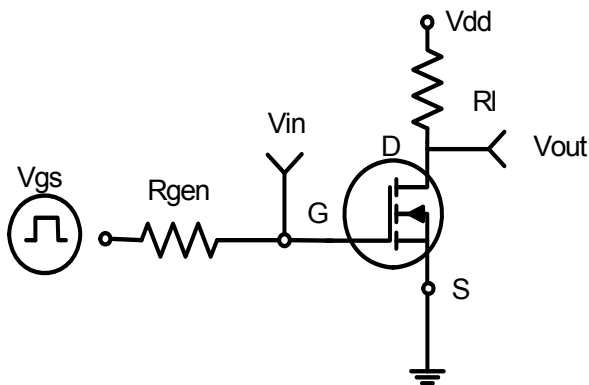


Figure 1: Switching Test Circuit

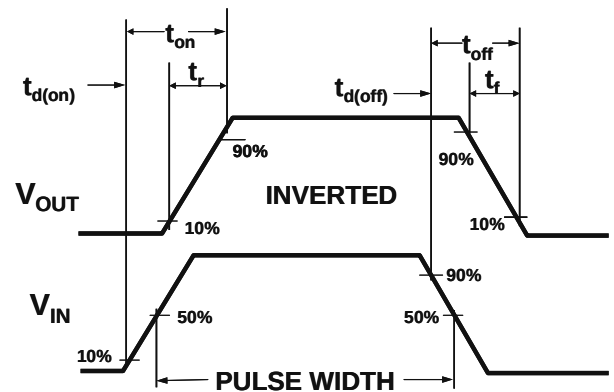


Figure 2: Switching Waveforms

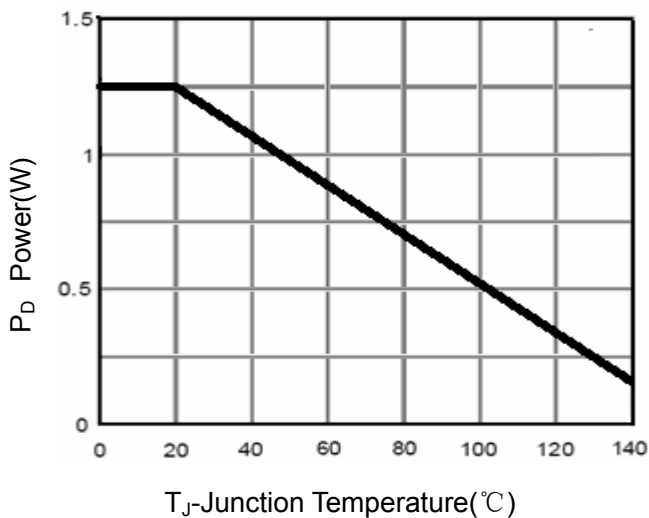


Figure 3 Power Dissipation

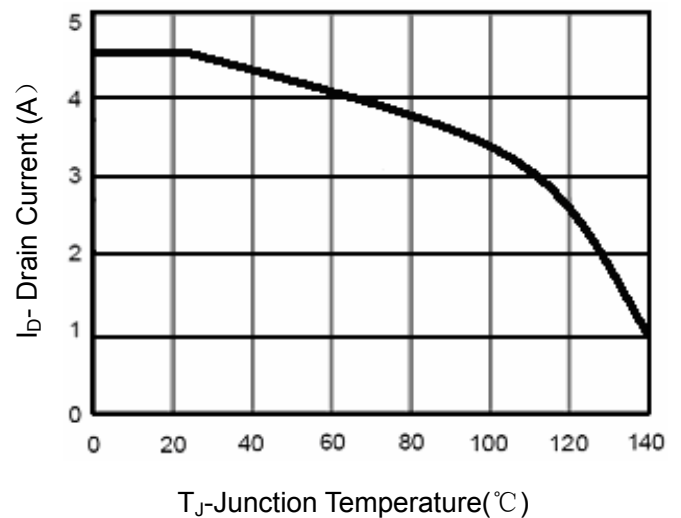


Figure 4 Drain Current

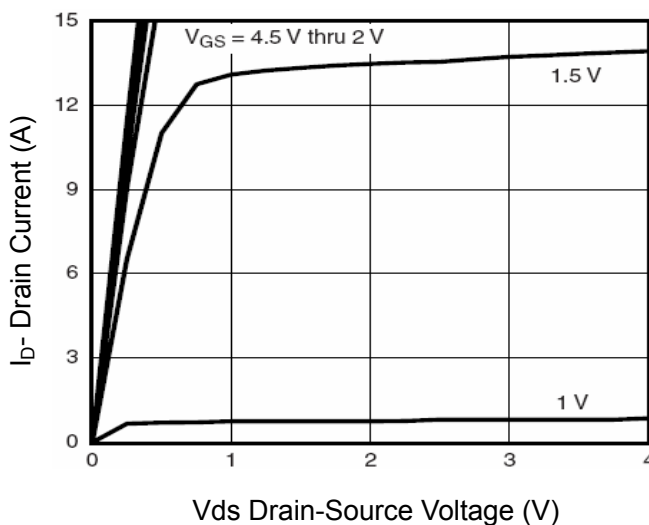


Figure 5 Output Characteristics

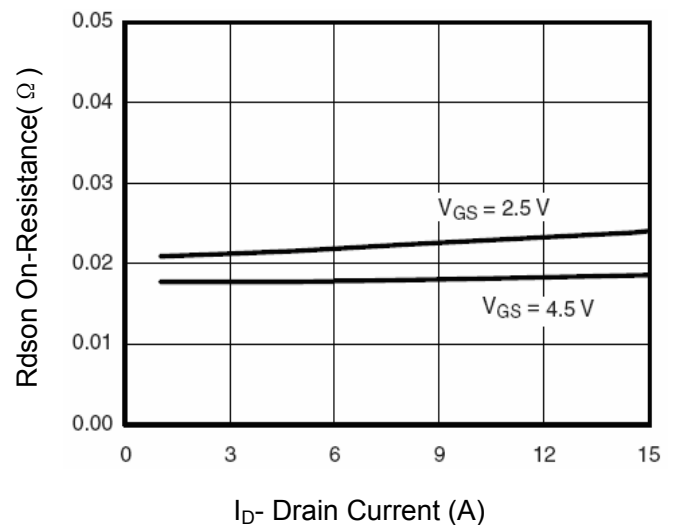


Figure 6 Drain-Source On-Resistance

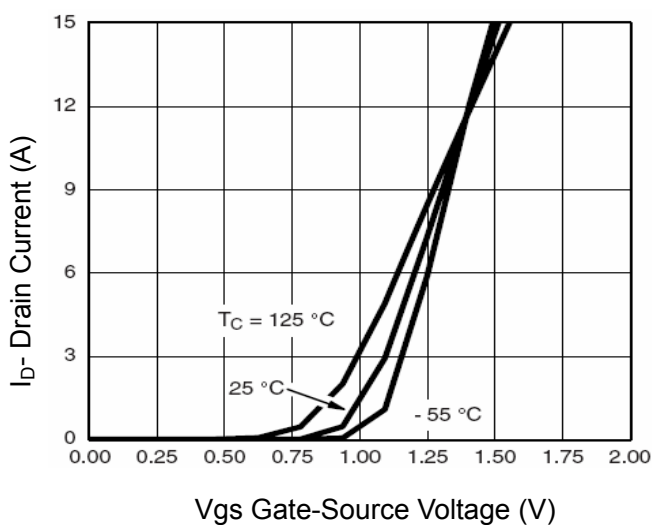


Figure 7 Transfer Characteristics

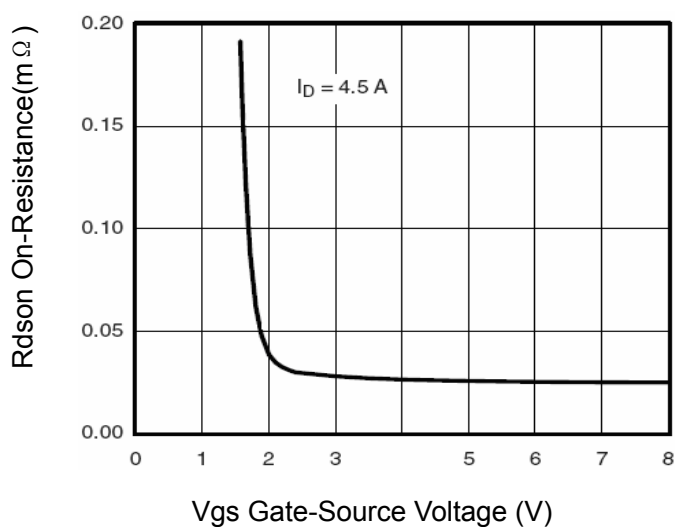


Figure 9 $R_{DS(on)}$ vs. V_{GS}

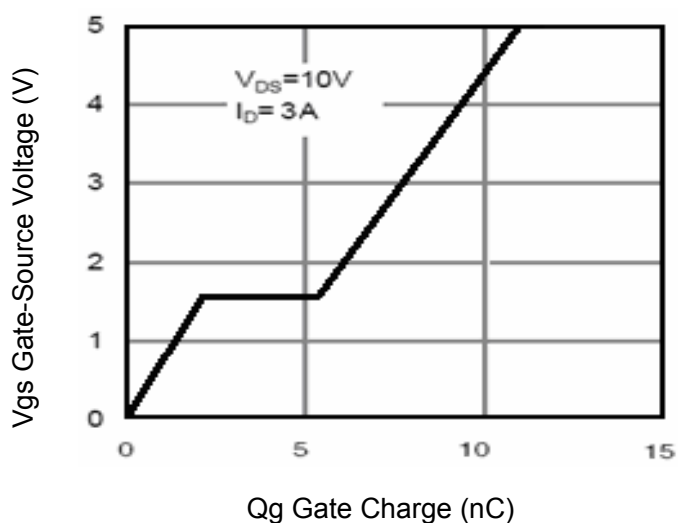


Figure 11 Gate Charge

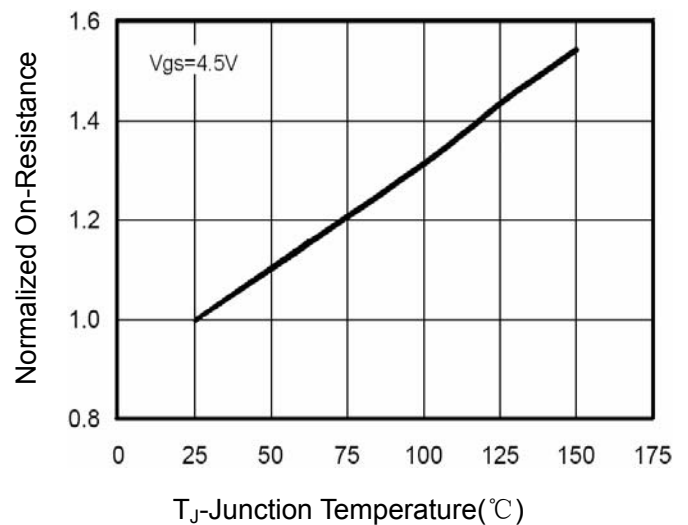


Figure 8 Drain-Source On-Resistance

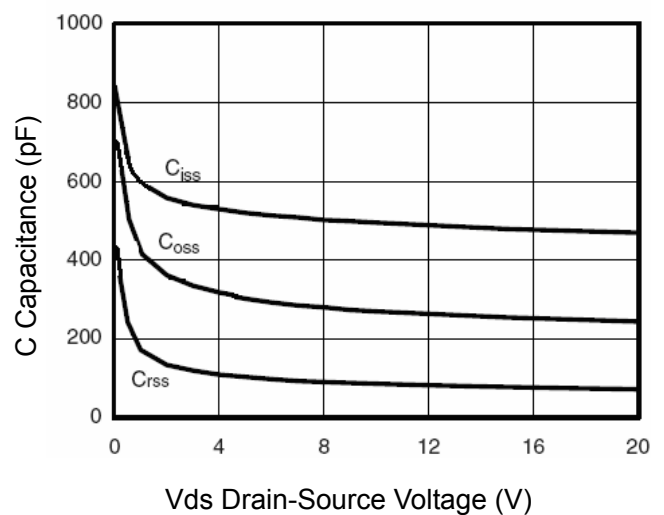


Figure 10 Capacitance vs V_{DS}

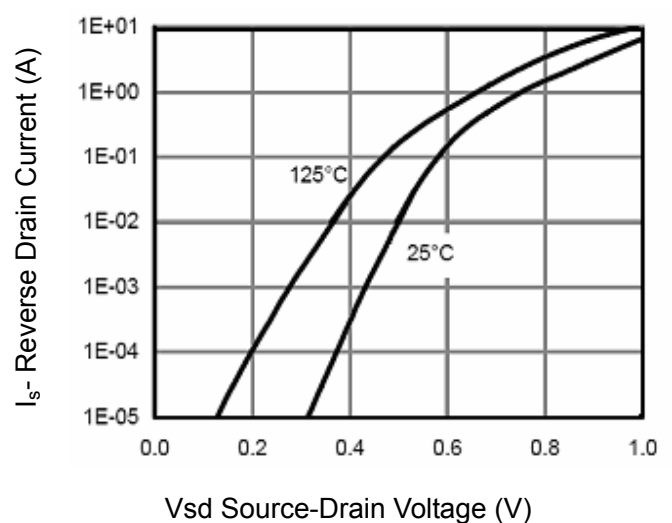


Figure 12 Source- Drain Diode Forward

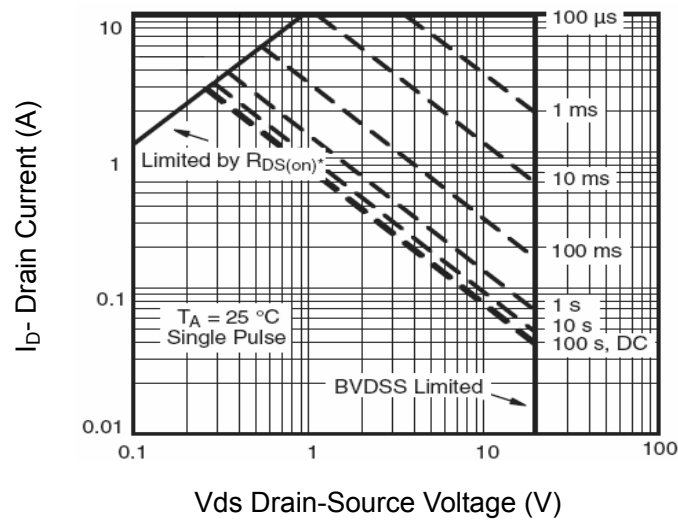


Figure 13 Safe Operation Area

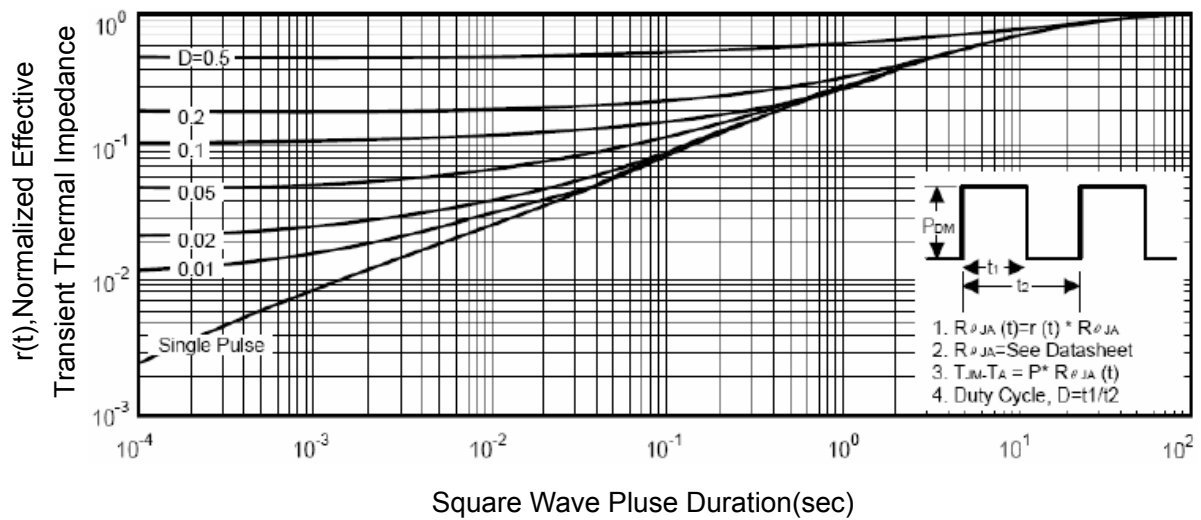
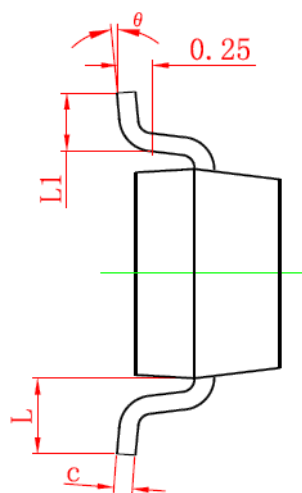
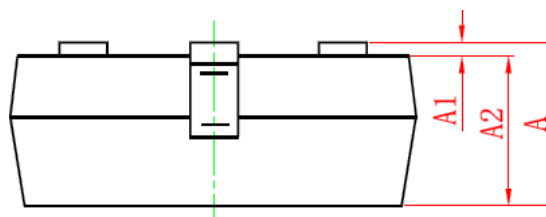
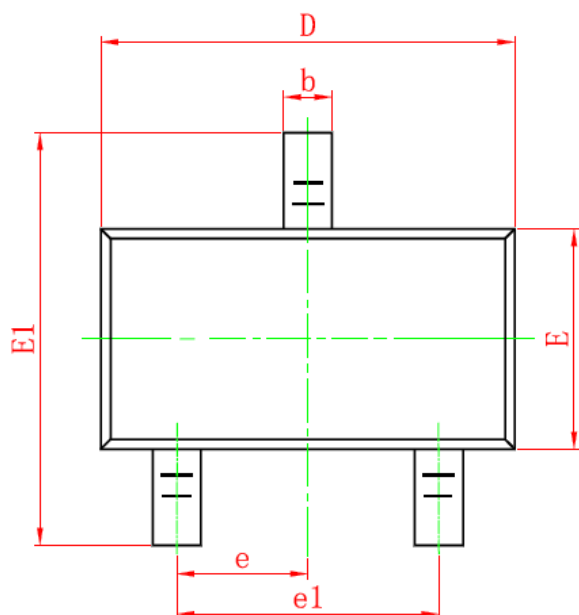


Figure 14 Normalized Maximum Transient Thermal Impedance

SOT-23 Package Information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

Notes

1. All dimensions are in millimeters.
2. Tolerance $\pm 0.10\text{mm}$ (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.