

## P-Channel Enhancement Mode Power MOSFET

### DESCRIPTION

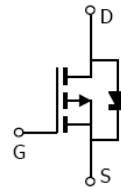
The PE1011E uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a Battery protection or in other Switching application.

### GENERAL FEATURES

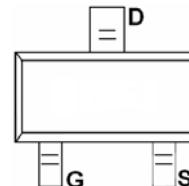
- $V_{DS} = -20V, I_D = -0.6A$
- $R_{DS(ON)} < 700m\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON)} < 860m\Omega @ V_{GS} = -2.5V$
- High Power and current handing capability
- Lead free product is acquired
- Gate-Source ESD Protection

### Application

- Battery operated Systems
- Load/ power Switching Cell Phones, Pagers
- Power Supply Converter Circuits



Schematic diagram



Marking and pin Assignment



SOT-523 top view

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	V
Drain Current-Continuous	$I_D$	-0.6	A
Drain Current-Pulsed (Note 1)	$I_{DM}$	-1	A
Maximum Power Dissipation	$P_D$	150	mW
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	125	°C/W
---	-----------------	-----	------

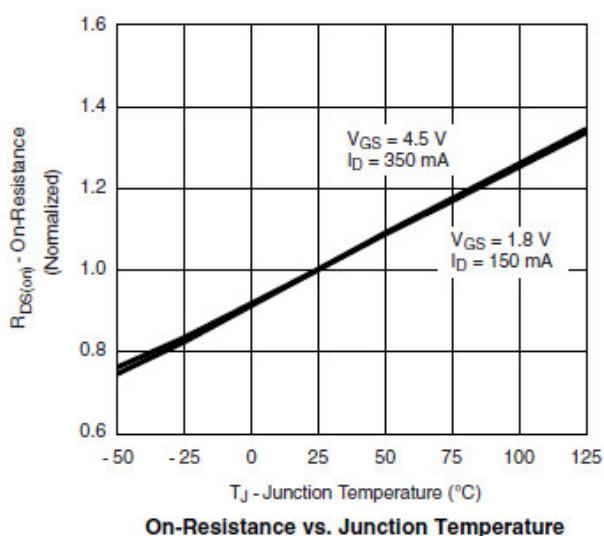
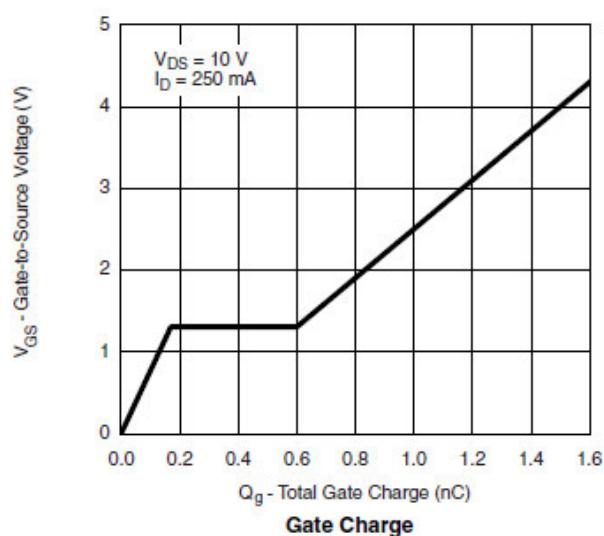
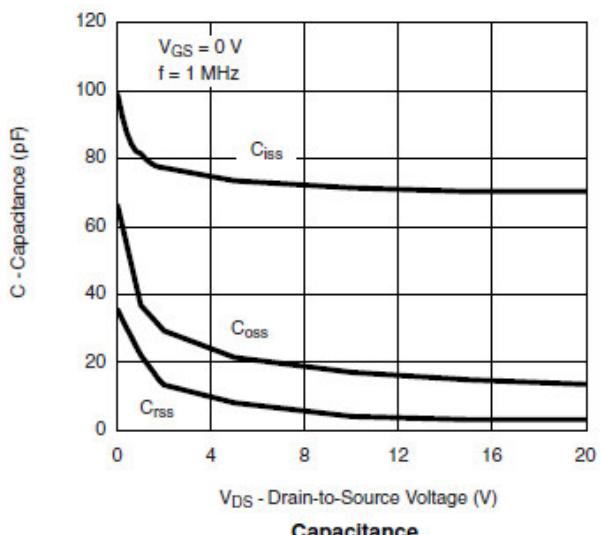
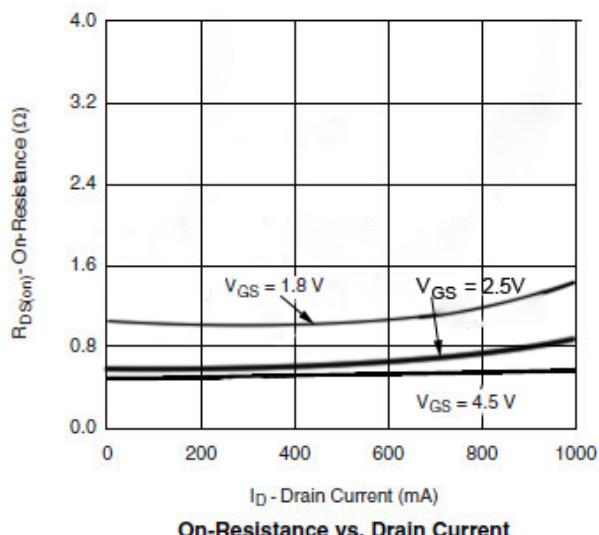
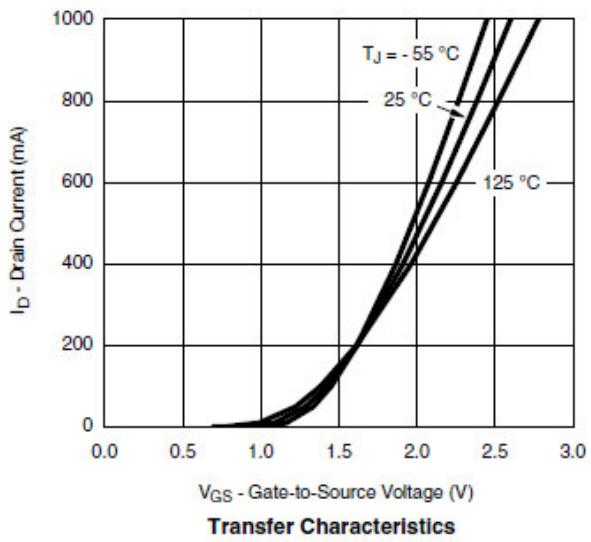
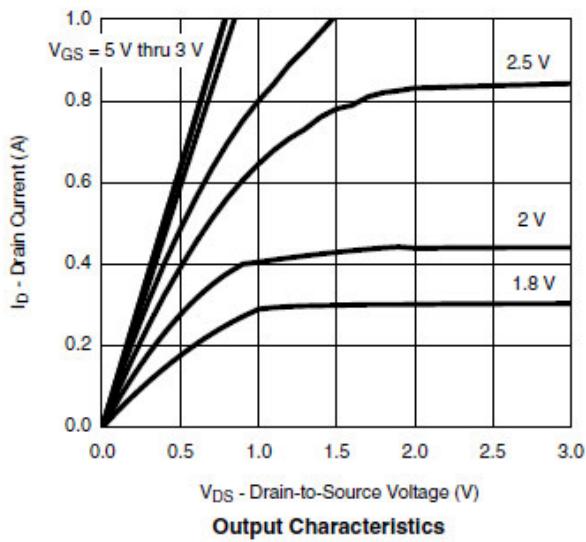
**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-20			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.4		-1.0	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 12V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$ $V_{DS}=-20V, V_{GS}=0V$ $T_J=85^\circ C$			-1 -5	uA
On-State Drain Current	$I_{D(on)}$	$V_{DS}\geq 5V, V_{GS}=4.5V$	0.7			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-0.6A$		600	700	mΩ
		$V_{GS}=-2.5V, I_D=-0.5A$		700	860	
		$V_{GS}=-1.8V, I_D=-0.4A$		1000	1450	
Forward Transconductance	$g_{FS}$	$V_{DS}=-10V, I_D=-0.4A$		1		S
Diode Forward Voltage	$V_{SD}$	$I_S=-0.15A, V_{GS}=0V$		0.65	1.2	V
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=-10V, V_{GS}=0V$ $f=1MHz$		70	100	pF
Output Capacitance	$C_{oss}$			20		
Reverse Transfer Capacitance	$C_{rss}$			10		
Total Gate Charge	$Q_g$	$V_{DS}=-10V, V_{GS}=-4.5V$ $I_D=-0.25A$		1.0	1.3	nC
Gate-Source Charge	$Q_{gs}$			0.1		
Gate-Drain Charge	$Q_{gd}$			0.3		
Turn-On Time	$t_{d(on)}$	$V_{DD}=-10V, R_L=30\Omega$ $I_D=-0.2A, V_{GEN}=-4.5V$ $R_G=10\Omega$		10	15	ns
	$t_r$			10	15	
Turn-Off Time	$t_{d(off)}$			40	60	
	$t_f$			30	50	

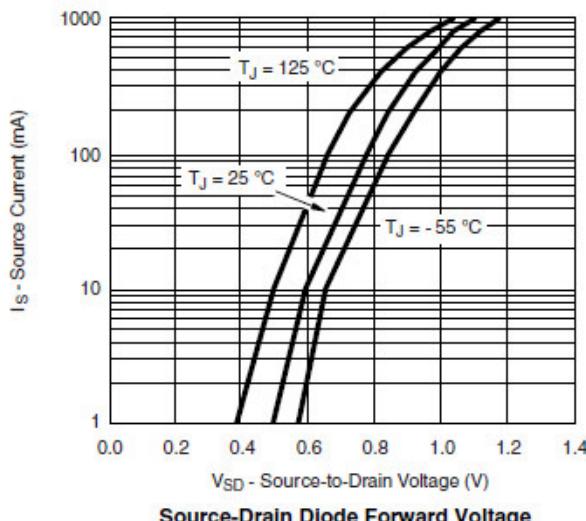
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

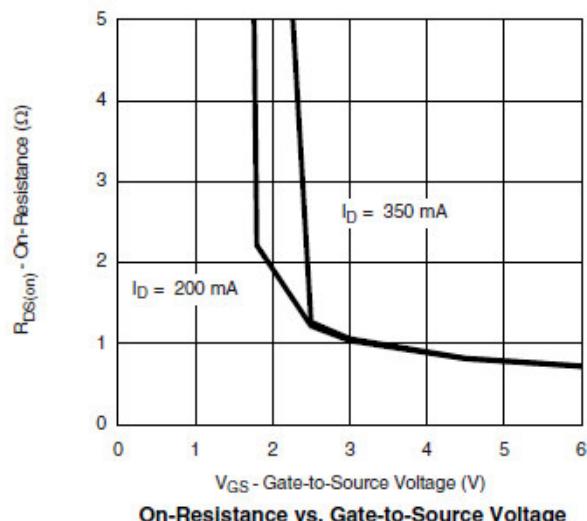
## Typical Characteristics



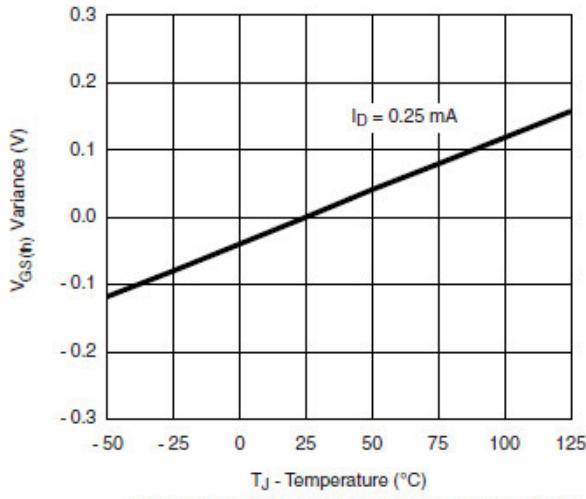
## Typical Characteristics



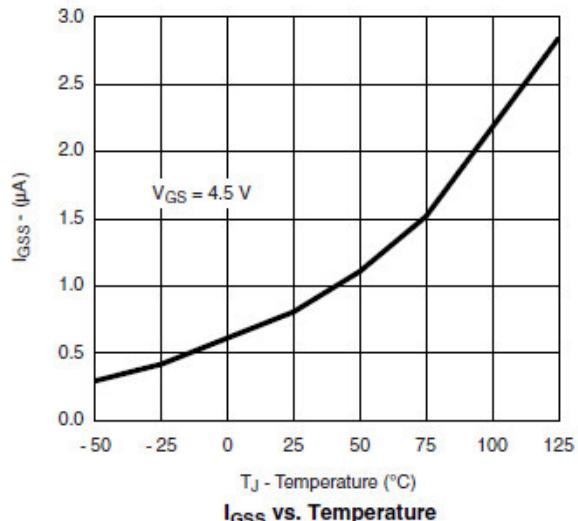
Source-Drain Diode Forward Voltage



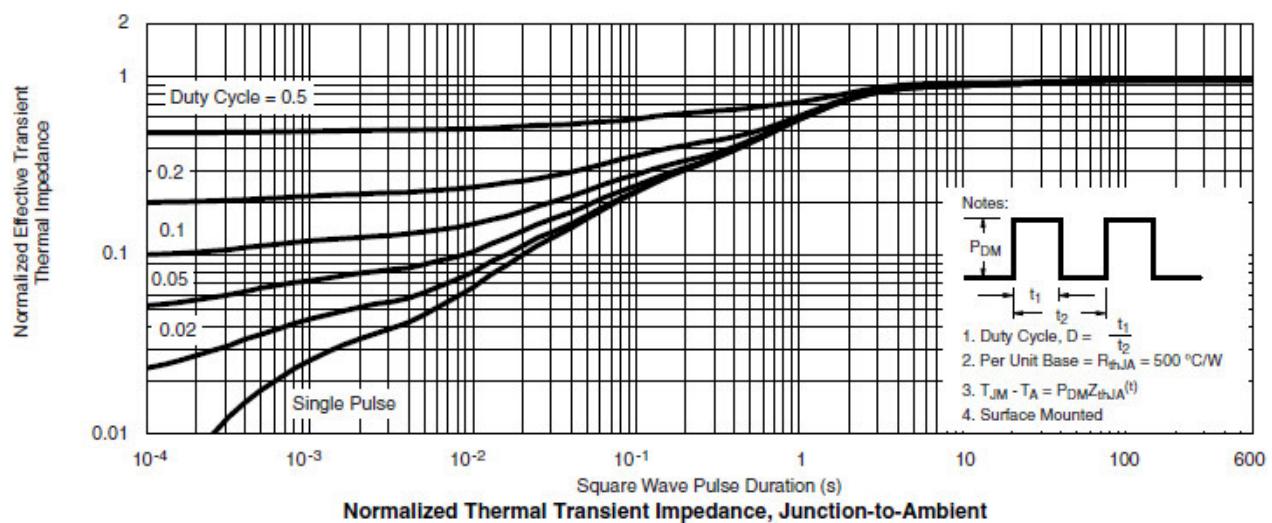
On-Resistance vs. Gate-to-Source Voltage



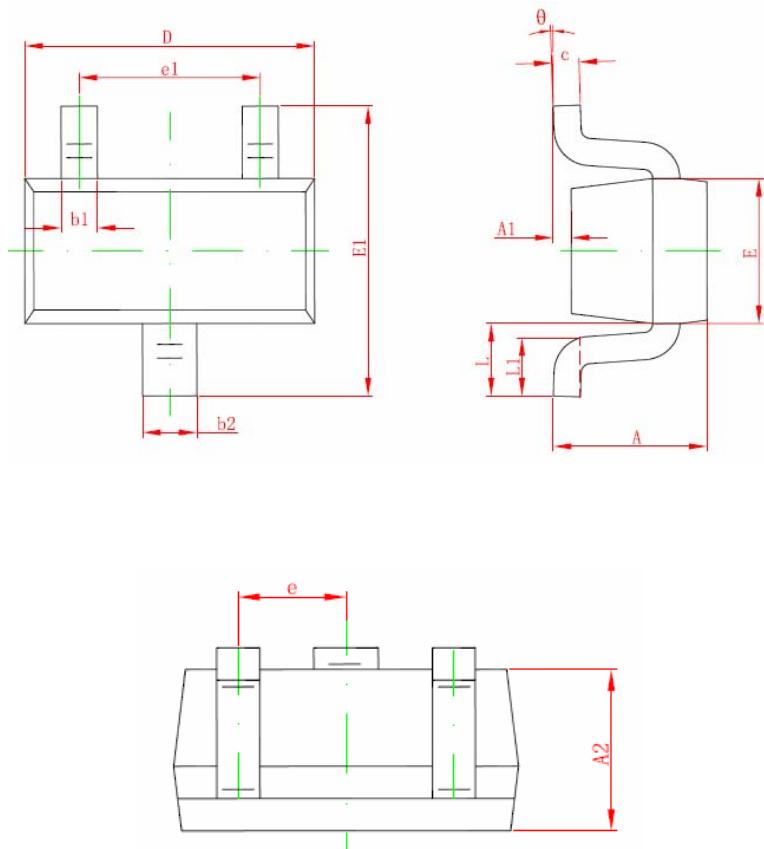
Threshold Voltage Variance vs. Temperature



$I_{GSS}$  vs. Temperature



## SOT-523 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b1	0.150	0.250	0.006	0.010
b2	0.250	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	1.500	1.700	0.059	0.067
E	0.700	0.900	0.028	0.035
E1	1.450	1.750	0.057	0.069
e	0.500 TYP.		0.020 TYP.	
e1	0.900	1.100	0.035	0.043
L	0.400 REF.		0.016 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

### NOTES

- All dimensions are in millimeters.
- Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
- Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- Dimension L is measured in gauge plane.
- Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.