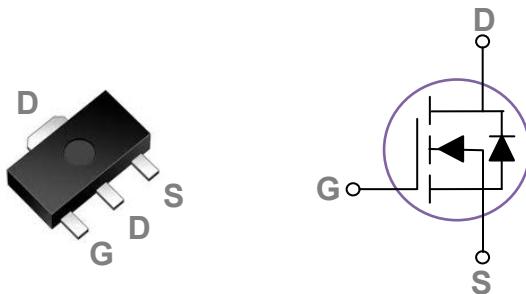


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT89 Pin Configuration



BVDSS	RDS(ON)	ID
60V	75mΩ	5A

### Features

- 60V,5A,  $RDS(ON) = 75m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	5	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	3.2	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	20	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	25	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	7	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	1.79	W
	Power Dissipation – Derate above 25°C	0.014	W/°C
$T_{STG}$	Storage Temperature Range	-50 to 150	°C
$T_J$	Operating Junction Temperature Range	-50 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	70	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	30	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=250\mu\text{A}$	60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $\text{I}_D=1\text{mA}$	---	0.05	---	$\text{V}/^\circ\text{C}$
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=60\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$\text{V}_{\text{DS}}=48\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

**On Characteristics**

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=5\text{A}$	---	60	75	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$ , $\text{I}_D=3\text{A}$	---	70	90	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_D=250\mu\text{A}$	1.2	1.8	2.5	V
			---	-5	---	$\text{mV}/^\circ\text{C}$
$\text{gfs}$	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}$ , $\text{I}_D=3\text{A}$	---	7	---	S

**Dynamic and switching Characteristics**

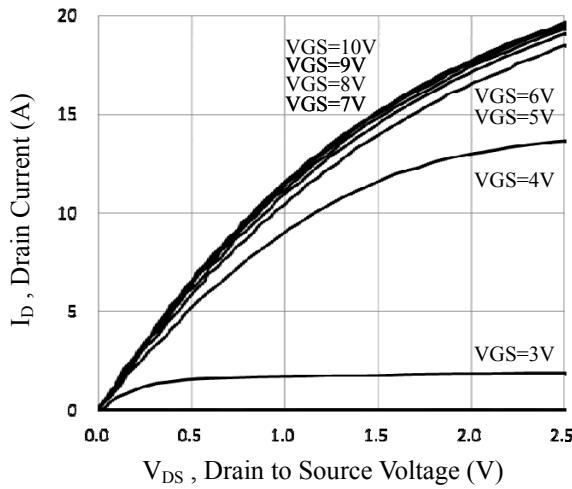
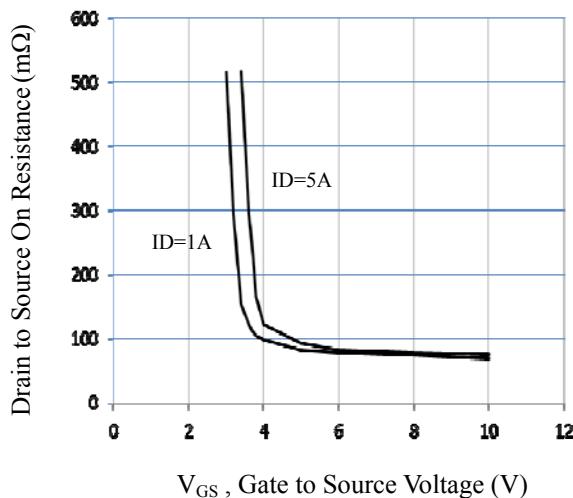
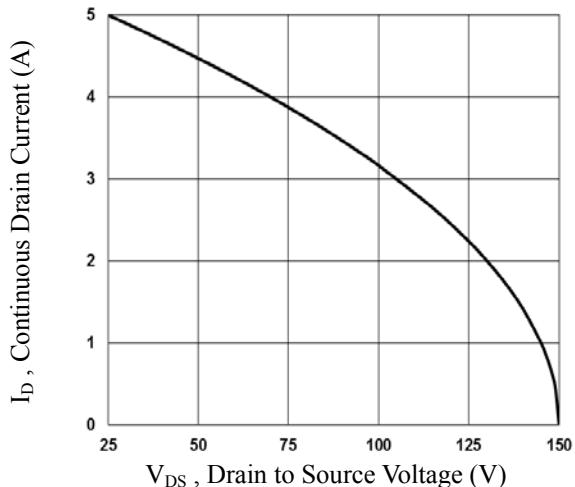
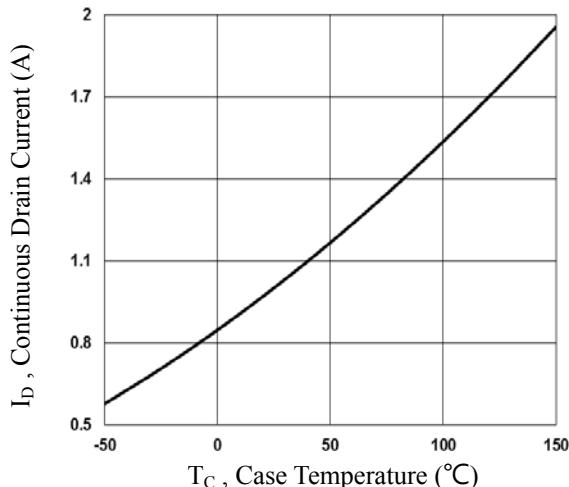
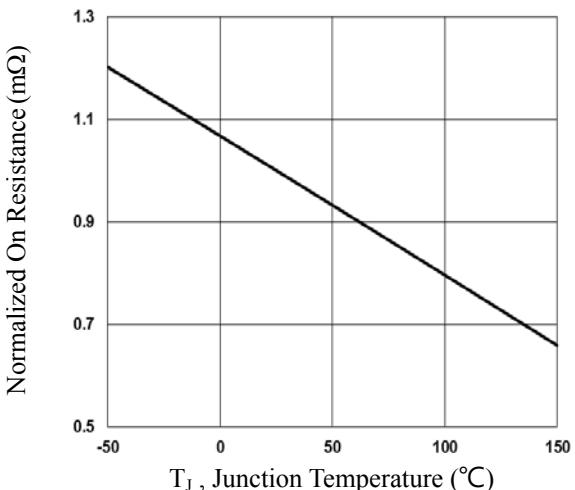
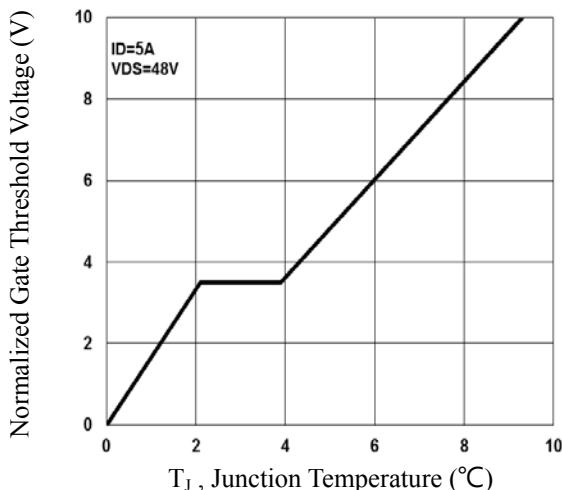
$\text{Q}_g$	Total Gate Charge <sup>2,3</sup>	$\text{V}_{\text{DS}}=48\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=5\text{A}$	---	9.3	14	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	2.1	4	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	1.8	4	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time <sup>2,3</sup>	$\text{V}_{\text{DD}}=30\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{R}_G=3.3\Omega$ $\text{I}_D=1\text{A}$	---	2.9	6	ns
$\text{T}_r$	Rise Time <sup>2,3</sup>		---	9.5	18	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time <sup>2,3</sup>		---	18.4	35	
$\text{T}_f$	Fall Time <sup>2,3</sup>		---	5.3	10	
$\text{C}_{\text{iss}}$	Input Capacitance		---	500	725	pF
$\text{C}_{\text{oss}}$	Output Capacitance	$\text{V}_{\text{DS}}=15\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{F}=1\text{MHz}$	---	45	65	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	16	30	
$\text{R}_g$	Gate resistance	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$ , $\text{F}=1\text{MHz}$	---	2	4	$\Omega$

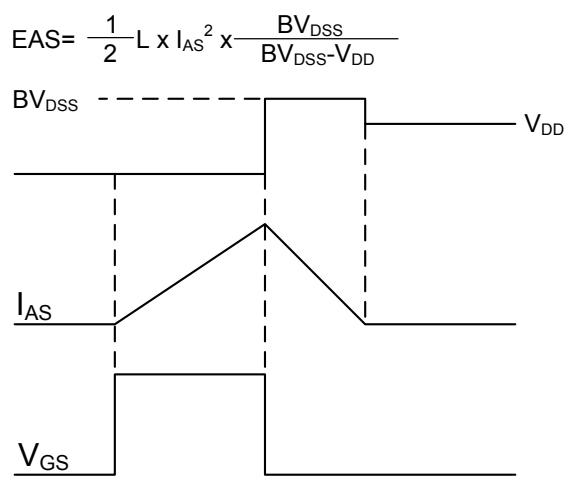
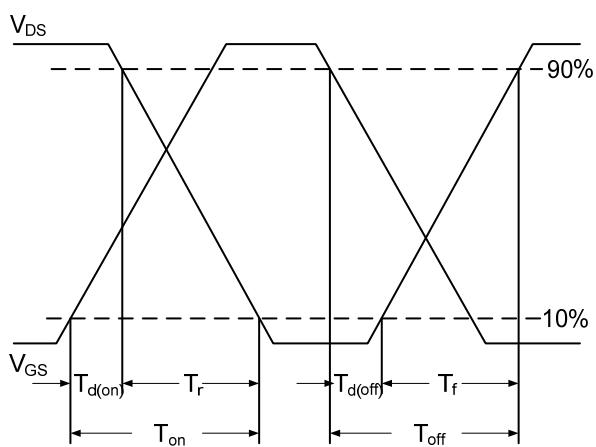
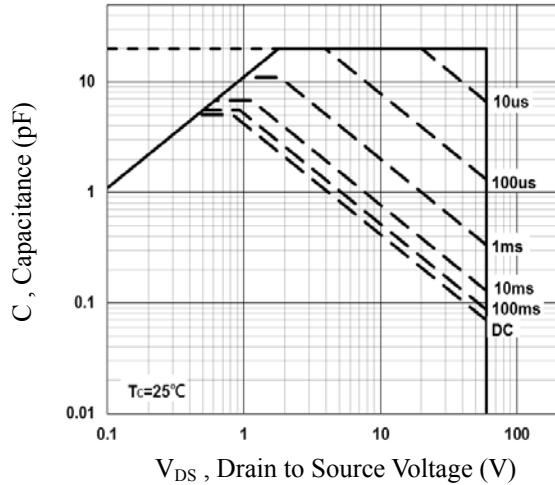
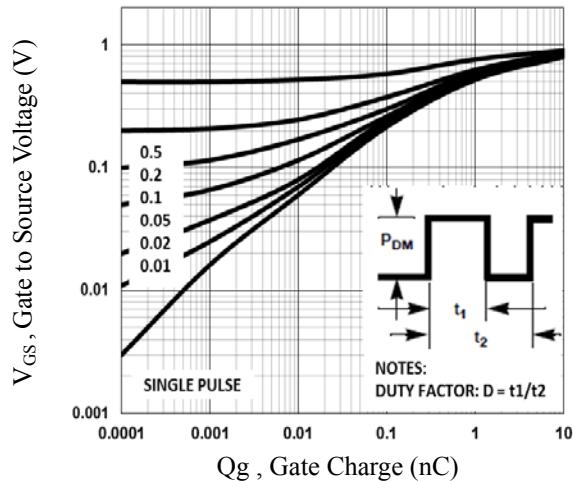
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_s$	Continuous Source Current	$\text{V}_G=\text{V}_D=0\text{V}$ , Force Current	---	---	5	A
$\text{I}_{\text{SM}}$	Pulsed Source Current		---	---	20	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$\text{t}_{\text{rr}}$	Reverse Recovery Time <sup>2</sup>	$\text{V}_{\text{GS}}=30\text{V}$ , $\text{I}_s=1\text{A}$ , $d\text{I}/dt=100\text{A}/\mu\text{s}$ $T_J=25^\circ\text{C}$	---	23.2	---	ns
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge <sup>2</sup>		---	14.3	---	nC

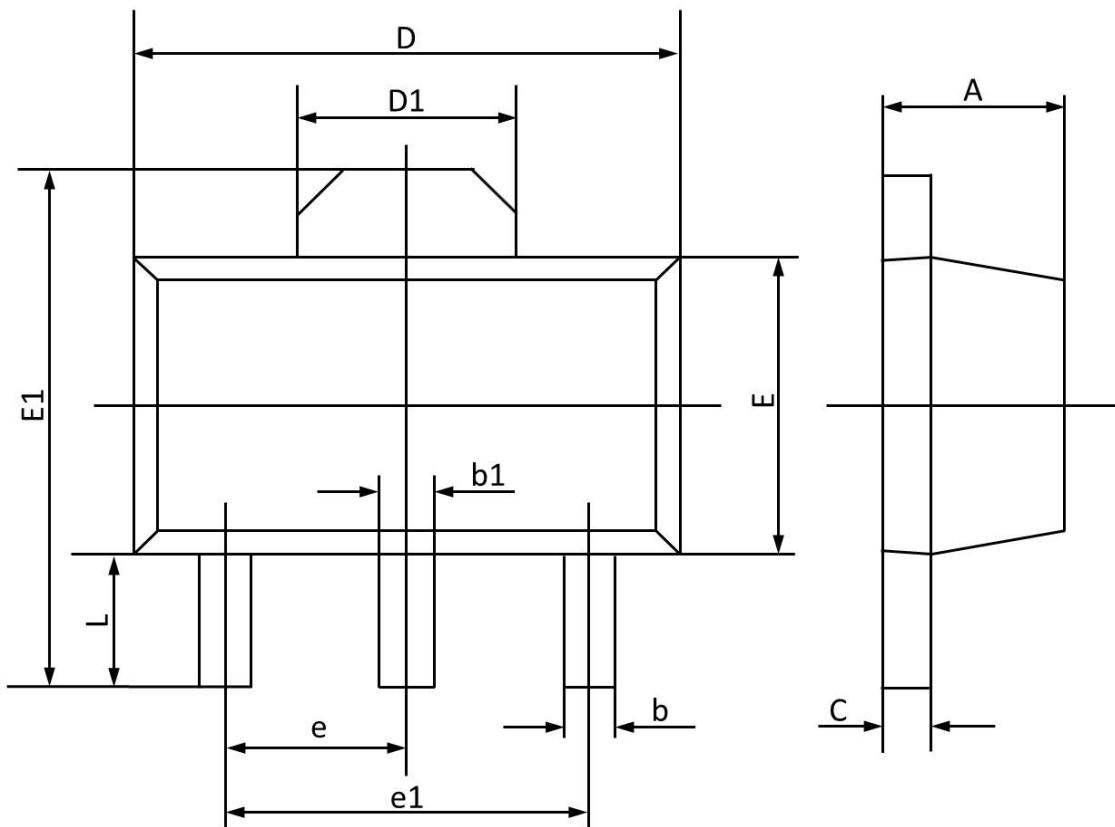
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $\text{V}_{\text{DD}}=25\text{V}$ ,  $\text{V}_{\text{GS}}=10\text{V}$ ,  $L=1\text{mH}$ ,  $\text{I}_{\text{AS}}=7\text{A}$ ,  $\text{R}_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$
3. The data tested by pulsed, pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1** Typical Output Characteristics

**Fig.2** RDSON vs. Gate Voltage

**Fig.3** Output Characteristics

**Fig.4** Continuous Drain Current vs.  $T_C$ 

**Fig.5** Normalized RDSON vs.  $T_J$ 

**Fig.6** Normalized  $V_{th}$  vs.  $T_J$



## SOT89 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP		0.118 TYP	
L	0.900	1.200	0.035	0.047