PBSS4540Z

40 V low VCEsat NPN transistor

26 June 2025 **Product data sheet**

1. General description

NPN low V_{CEsat} transistor in a small SOT223 Surface Mounted Device (SMD) plastic package.

PNP complement: PBSS5540Z

2. Features and benefits

- Low collector-emitter saturation voltage
- High current capabilities
- Improved device reliability due to reduced heat generation.
- Qualified AEC-Q101

3. Applications

- Supply line switching circuits
- Battery management applications
- DC/DC converter applications
- Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers)
- MOSFET driver applications

4. Quick reference data

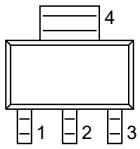
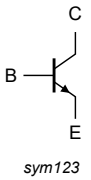
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	40	V
I_C	collector current			-	-	5	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-	10	A
R_{CEsat}	collector-emitter saturation resistance	I_C = 5 A; I_B = 500 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	42	71	mΩ

nexperia

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-73 (SOT223)	 sym123
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4540Z	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4540Z	PB4540

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	40	V
V_{CEO}	collector-emitter voltage	open base		-	40	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I_C	collector current			-	5	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	10	A
I_{BM}	peak base current			-	2	A
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	1.35	W
			[2]	-	2	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm^2.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.

9. Thermal characteristics

Table 6. Thermal characteristics

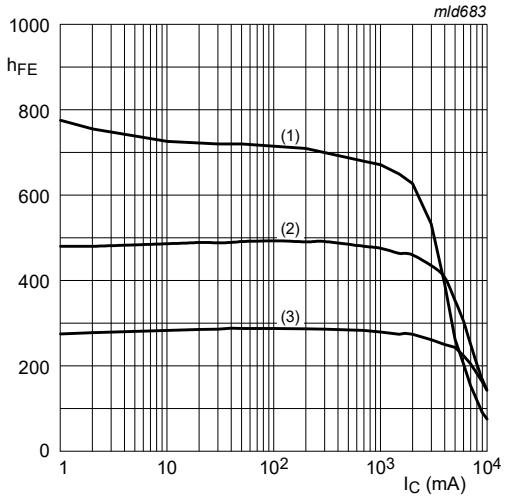
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	92	K/W
			[2]	-	-	62.5	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm^2.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.

10. Characteristics

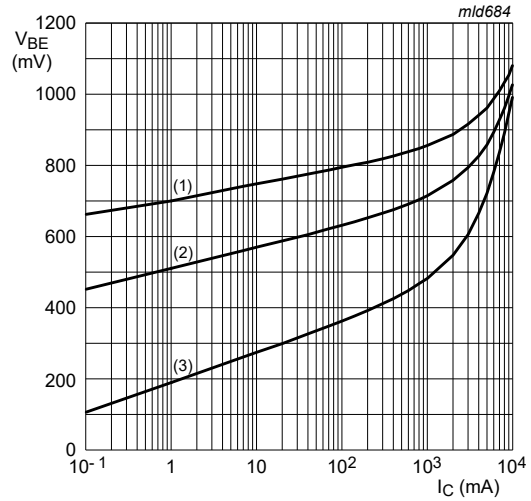
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = 30 V; I_E = 0 A; T_{amb} = 25 °C		-	-	100	nA
		V_{CB} = 30 V; I_E = 0 A; T_j = 150 °C		-	-	50	µA
I_{EBO}	emitter-base cut-off current	V_{EB} = 5 V; I_C = 0 A; T_{amb} = 25 °C		-	-	100	nA
h_{FE}	DC current gain	V_{CE} = 2 V; I_C = 500 mA; T_{amb} = 25 °C		300	500	-	
		V_{CE} = 2 V; I_C = 1 A; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		300	500	-	
		V_{CE} = 2 V; I_C = 2 A; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		250	450	-	
		V_{CE} = 2 V; I_C = 5 A; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		100	300	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = 500 mA; I_B = 5 mA; T_{amb} = 25 °C		-	50	90	mV
		I_C = 1 A; I_B = 10 mA; T_{amb} = 25 °C		-	75	120	mV
		I_C = 2 A; I_B = 200 mA; T_{amb} = 25 °C		-	90	150	mV
		I_C = 5 A; I_B = 500 mA; T_{amb} = 25 °C		-	210	355	mV
R_{CEsat}	collector-emitter saturation resistance	I_C = 5 A; I_B = 500 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	42	71	mΩ
V_{BEsat}	base-emitter saturation voltage	I_C = 5 A; I_B = 500 mA; T_{amb} = 25 °C		-	1.1	1.3	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I_C = 2 A; T_{amb} = 25 °C		-	0.8	1.1	V
f_T	transition frequency	V_{CE} = 10 V; I_C = 100 mA; f = 100 MHz; T_{amb} = 25 °C		70	130	-	MHz
C_c	collector capacitance	V_{CB} = 10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	60	75	pF



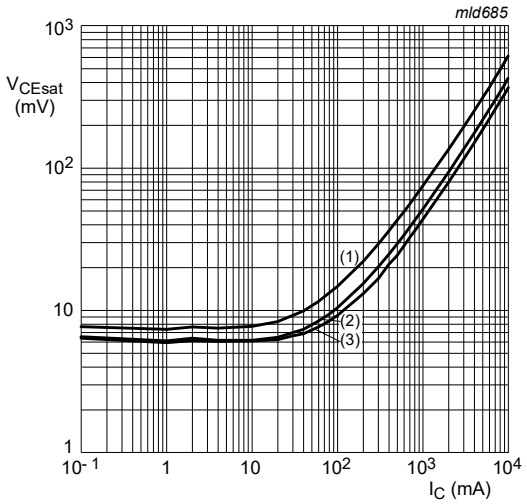
V_{CE} = 2 V
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 1. DC current gain as a function of collector current; typical values



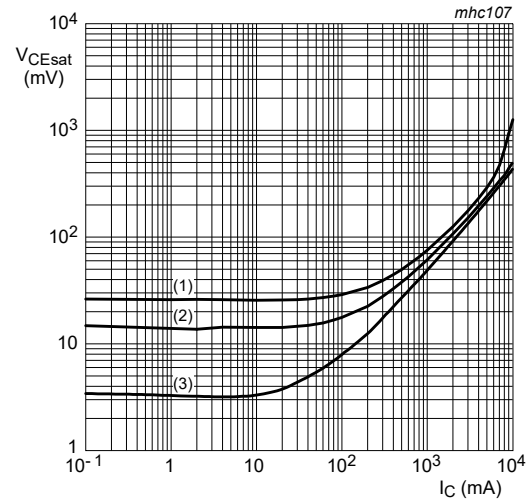
V_{CE} = 2 V
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 2. Base-emitter voltage as a function of collector current; typical values



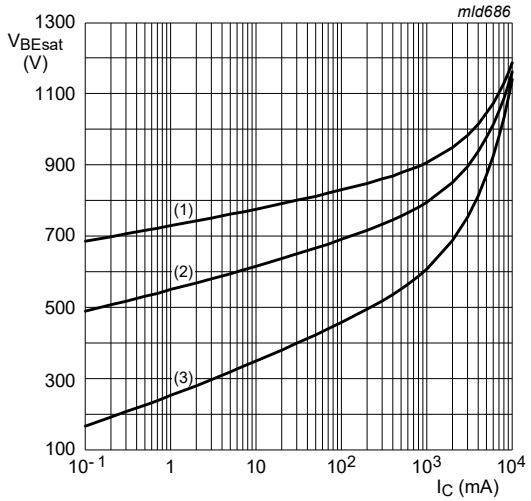
I_C/I_B = 20
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 3. Collector-emitter saturation voltage as a function of collector current; typical values



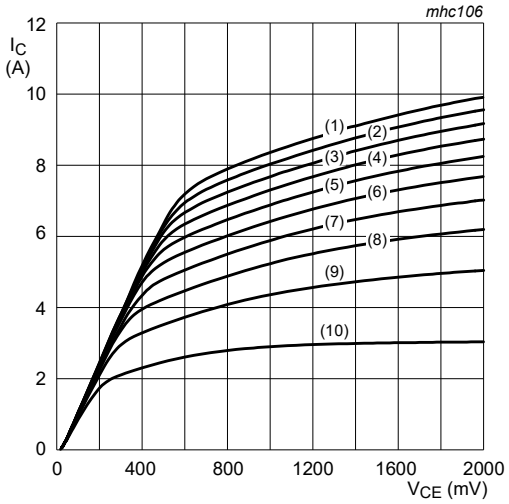
T_{amb} = 25 °C
(1) I_C/I_B = 100
(2) I_C/I_B = 50
(3) I_C/I_B = 10

Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values



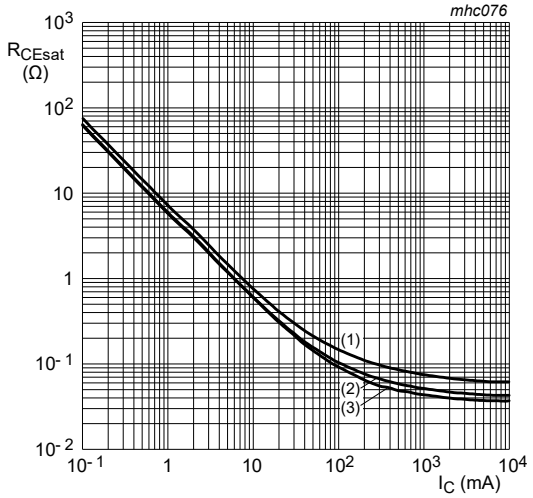
I_C/I_B = 20
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values



(1) I_B = 70 mA
(2) I_B = 63 mA
(3) I_B = 56 mA
(4) I_B = 49 mA
(5) I_B = 42 mA
(6) I_B = 35 mA
(7) I_B = 28 mA
(8) I_B = 21 mA
(9) I_B = 14 mA
(10) I_B = 7 mA

Fig. 6. Collector current as a function of collector-emitter voltage; typical values



I_C/I_B = 20
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 7. Collector-emitter equivalent on-resistance as a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard*Q101 -Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

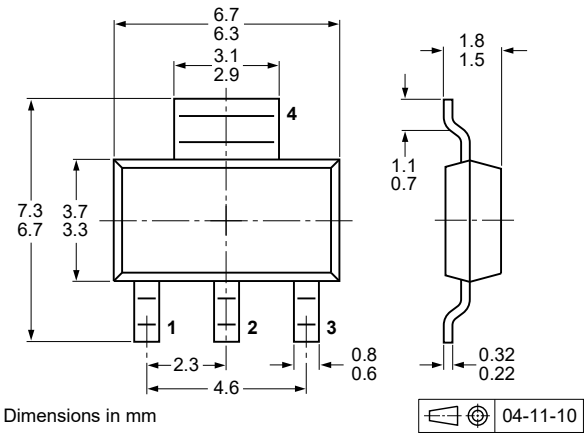


Fig. 8. Package outline SC-73 (SOT223)

13. Soldering

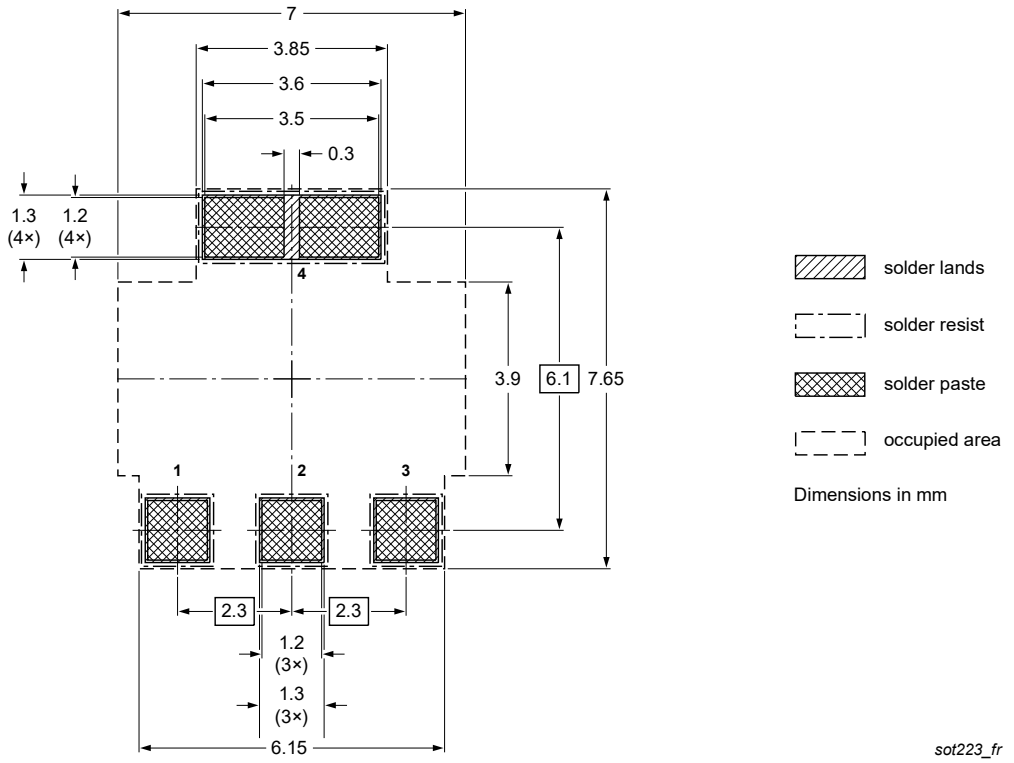


Fig. 9. Reflow soldering footprint for SC-73 (SOT223)

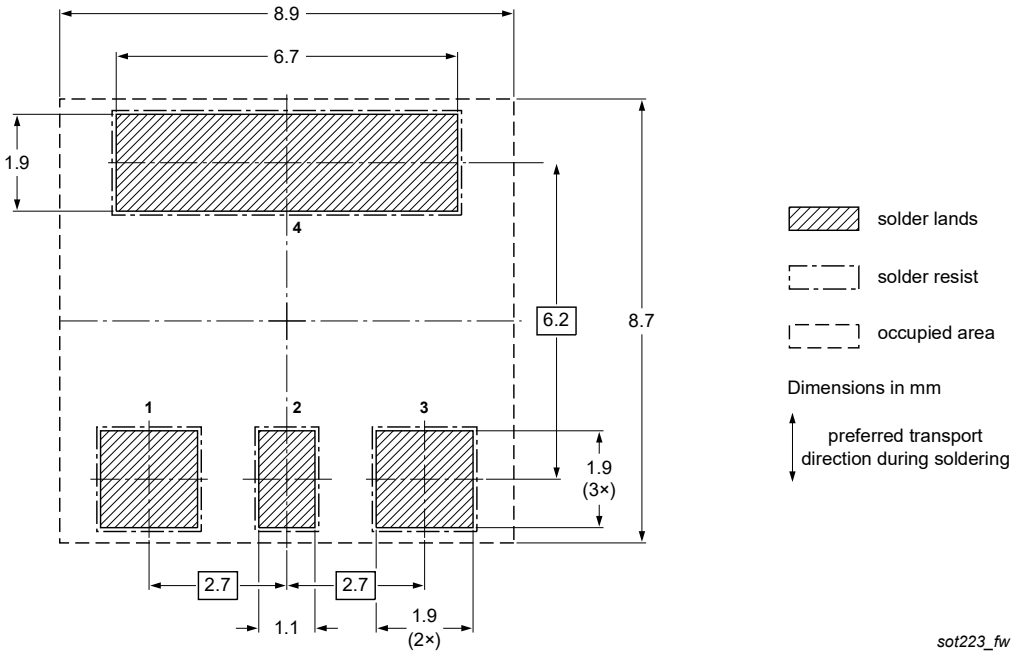


Fig. 10. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4540Z v.2	20250626	Product data sheet	-	PBSS4540Z v.1
Modifications	• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate.			
PBSS4540Z v.1	20011114	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 26 June 2025