



# PBSS4032PZ

30 V, 4.4 A PNP low  $V_{CEsat}$  (BISS) transistor

Rev. 01 — 31 March 2010

Product data sheet

## 1. Product profile

### 1.1 General description

PNP low  $V_{CEsat}$  Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4032NZ.

### 1.2 Features and benefits

- Low collector-emitter saturation voltage  $V_{CEsat}$
- Optimized switching time
- High collector current capability  $I_C$  and  $I_{CM}$
- High collector current gain ( $h_{FE}$ ) at high  $I_C$
- High energy efficiency due to less heat generation
- AEC-Q101 qualified
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

### 1.3 Applications

- DC-to-DC conversion
- Battery-driven devices
- Power management
- Charging circuits

### 1.4 Quick reference data

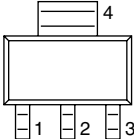
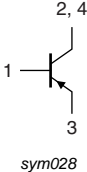
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-30	V
$I_C$	collector current		-	-	-4.4	A
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	-10	A
$R_{CEsat}$	collector-emitter saturation resistance	$I_C = -4$ A; $I_B = -400$ mA	<a href="#">[1]</a> -	58	86	m $\Omega$

[1] Pulse test:  $t_p \leq 300$   $\mu$ s;  $\delta \leq 0.02$ .

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	base		
2	collector		
3	emitter		
4	collector		

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4032PZ	SC-73	plastic surface-mounted package with increased heat sink; 4 leads	SOT223

4. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4032PZ	PB4032PZ

5. Limiting values

Table 5. Limiting values

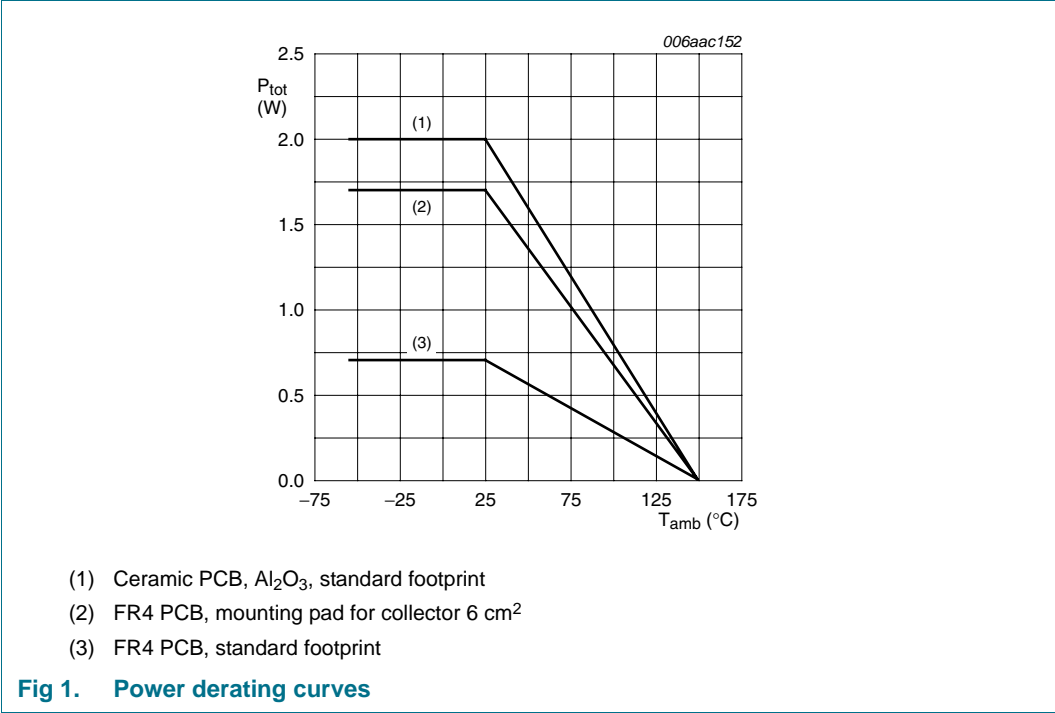
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	-	-30	V
$V_{CEO}$	collector-emitter voltage	open base	-	-30	V
$V_{EBO}$	emitter-base voltage	open collector	-	-5	V
$I_C$	collector current		-	-4.4	A
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	-10	A
$I_B$	base current		-	-1	A

**Table 5.** Limiting values ...continued  
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ }^{\circ}\text{C}$	[1] -	700	mW
			[2] -	1700	mW
			[3] -	2000	mW
$T_j$	junction temperature		-	150	$^{\circ}\text{C}$
$T_{amb}$	ambient temperature		-55	+150	$^{\circ}\text{C}$
$T_{stg}$	storage temperature		-65	+150	$^{\circ}\text{C}$

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

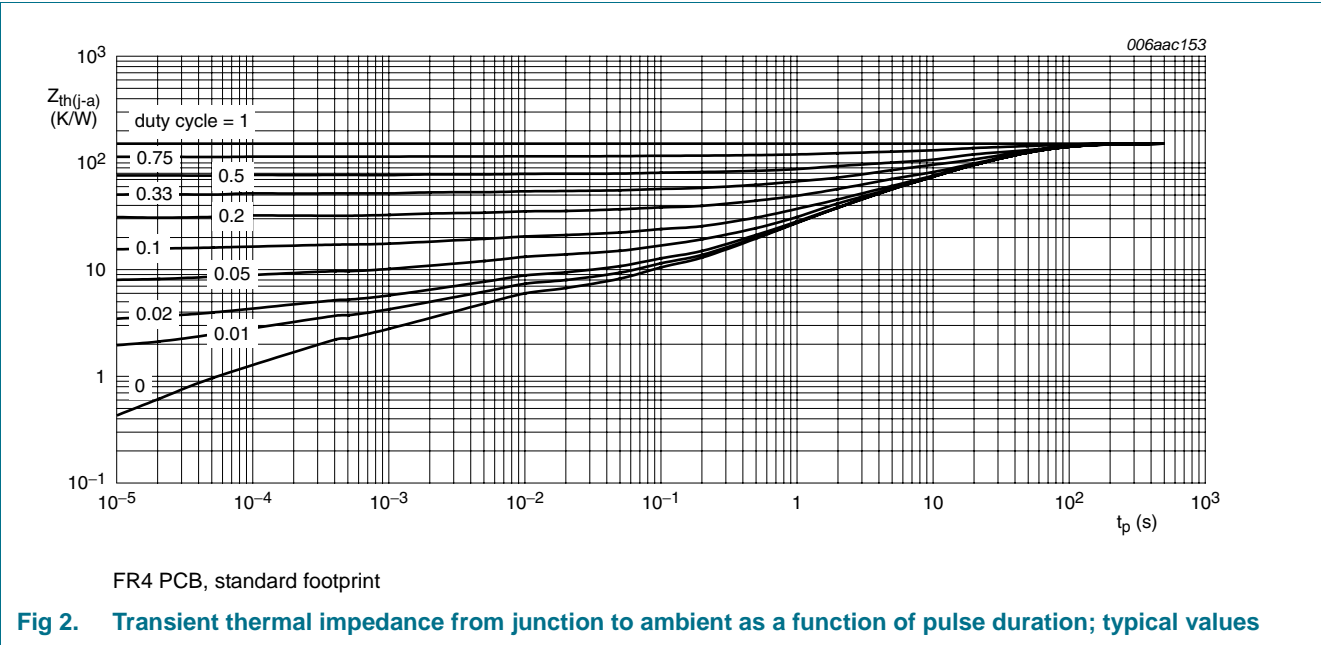


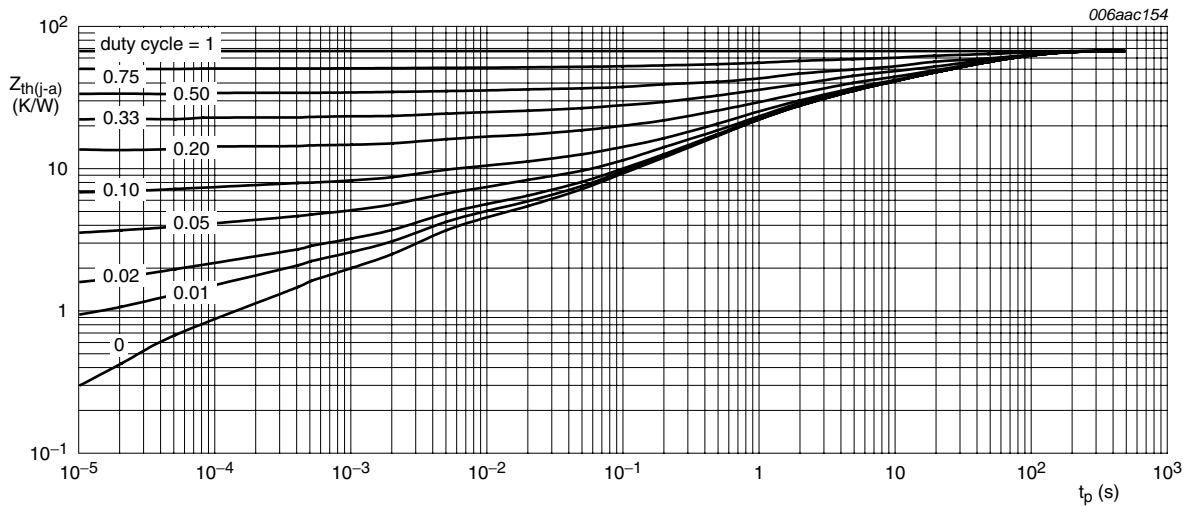
6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] -	-	180	K/W
			[2] -	-	75	K/W
			[3] -	-	65	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	-	15	K/W

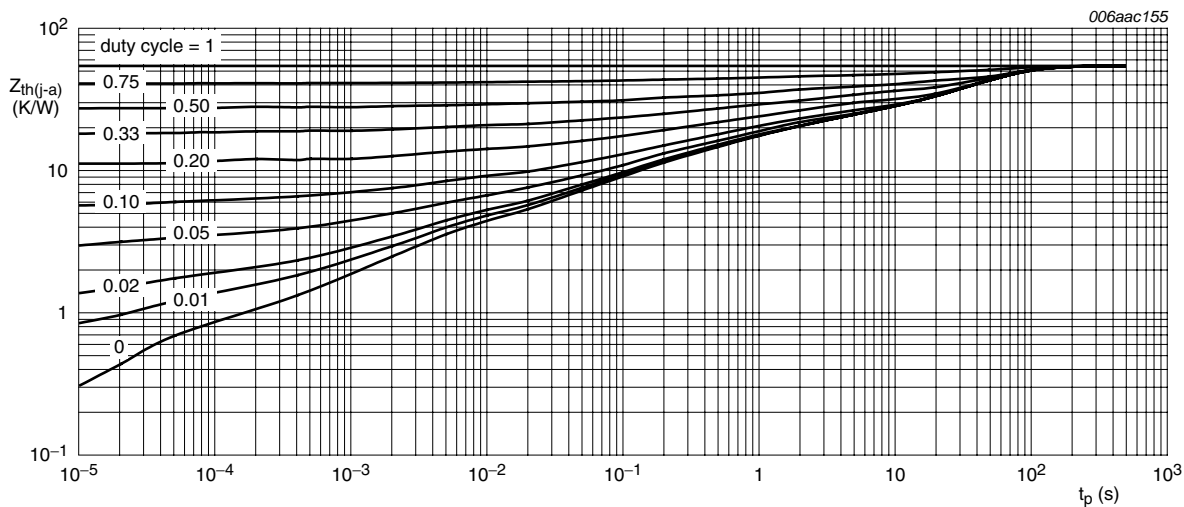
- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.





FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>

Fig 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

Fig 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

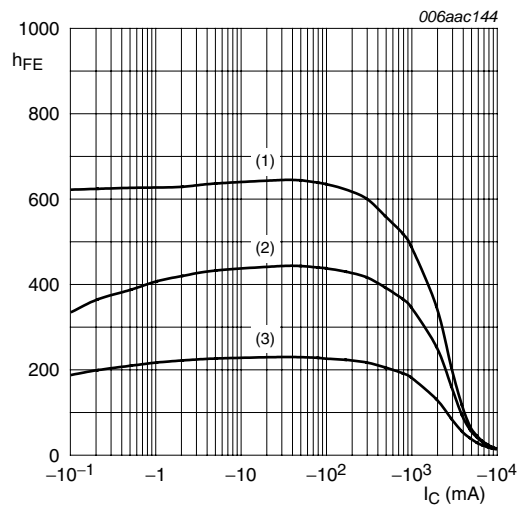
## 7. Characteristics

**Table 7. Characteristics**

$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

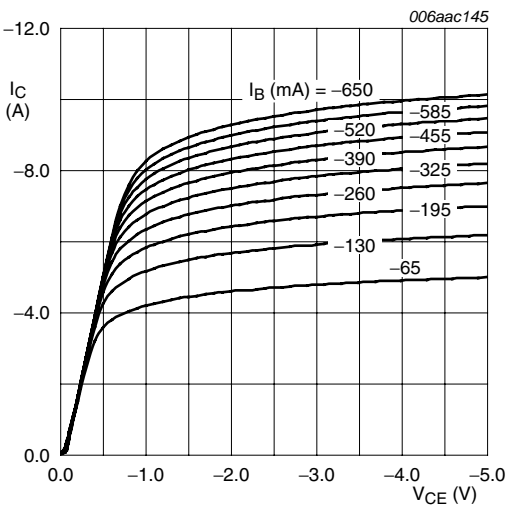
Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = −30 V; I <sub>E</sub> = 0 A	-	-	−100	nA	
		V <sub>CB</sub> = −30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	−55	μA	
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = −24 V; V <sub>BE</sub> = 0 V	-	-	−100	nA	
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = −5 V; I <sub>C</sub> = 0 A	-	-	−100	nA	
h <sub>FE</sub>	DC current gain		[1]				
		V <sub>CE</sub> = −2 V; I <sub>C</sub> = −500 mA	200	350	-		
		V <sub>CE</sub> = −2 V; I <sub>C</sub> = −1 A	200	320	-		
		V <sub>CE</sub> = −2 V; I <sub>C</sub> = −2 A	150	240	-		
		V <sub>CE</sub> = −2 V; I <sub>C</sub> = −4 A	60	100	-		
V <sub>CEsat</sub>	collector-emitter saturation voltage		[1]				
		I <sub>C</sub> = −1 A; I <sub>B</sub> = −50 mA	-	−110	−165	mV	
		I <sub>C</sub> = −1 A; I <sub>B</sub> = −10 mA	-	−160	−240	mV	
		I <sub>C</sub> = −2 A; I <sub>B</sub> = −40 mA	-	−200	−300	mV	
		I <sub>C</sub> = −4 A; I <sub>B</sub> = −400 mA	-	−230	−345	mV	
	I <sub>C</sub> = −4 A; I <sub>B</sub> = −200 mA	-	−270	−400	mV		
R <sub>CEsat</sub>	collector-emitter saturation resistance	I <sub>C</sub> = −4 A; I <sub>B</sub> = −400 mA	[1]	-	58	86	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = −1 A; I <sub>B</sub> = −100 mA	[1]	-	−0.79	−0.9	V
		I <sub>C</sub> = −4 A; I <sub>B</sub> = −400 mA	[1]	-	−0.99	−1.05	V
V <sub>BEon</sub>	base-emitter turn-on voltage	V <sub>CE</sub> = −2 V; I <sub>C</sub> = −2 A	[1]	-	−0.81	−0.9	V
t <sub>d</sub>	delay time	V <sub>CC</sub> = −12.5 V;	-	30	-	ns	
t <sub>r</sub>	rise time	I <sub>C</sub> = −1 A; I <sub>Bon</sub> = −0.05 A;	-	60	-	ns	
t <sub>on</sub>	turn-on time	I <sub>Boff</sub> = 0.05 A	-	90	-	ns	
t <sub>s</sub>	storage time		-	140	-	ns	
t <sub>f</sub>	fall time		-	80	-	ns	
t <sub>off</sub>	turn-off time		-	220	-	ns	
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = −10 V; I <sub>C</sub> = −100 mA; f = 100 MHz	-	130	-	MHz	
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = −10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz	-	65	-	pF	

[1] Pulse test:  $t_p \leq 300\text{ }\mu\text{s}$ ;  $\delta \leq 0.02$ .



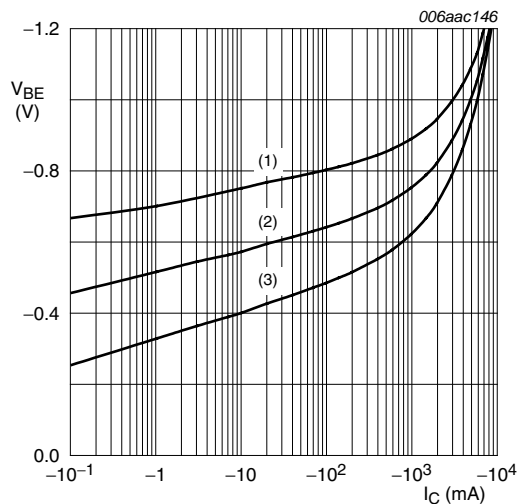
- $V_{CE} = -2\text{ V}$
- (1)  $T_{amb} = 100\text{ °C}$
  - (2)  $T_{amb} = 25\text{ °C}$
  - (3)  $T_{amb} = -55\text{ °C}$

Fig 5. DC current gain as a function of collector current; typical values



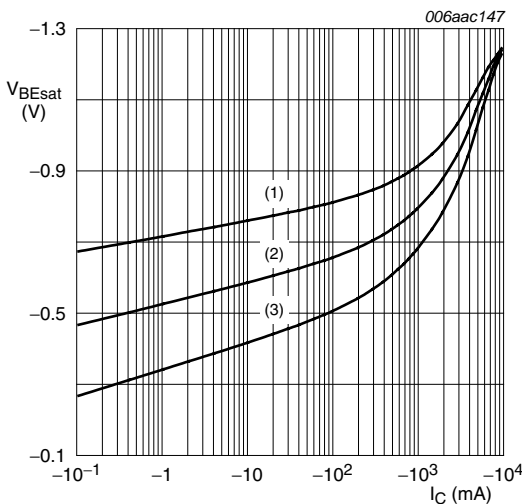
$T_{amb} = 25\text{ °C}$

Fig 6. Collector current as a function of collector-emitter voltage; typical values



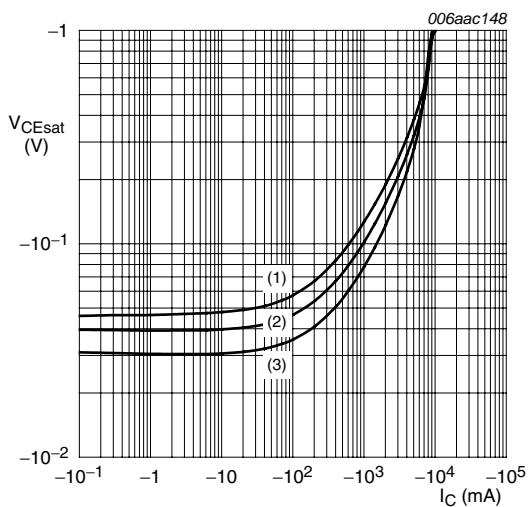
- $V_{CE} = -2\text{ V}$
- (1)  $T_{amb} = -55\text{ °C}$
  - (2)  $T_{amb} = 25\text{ °C}$
  - (3)  $T_{amb} = 100\text{ °C}$

Fig 7. Base-emitter voltage as a function of collector current; typical values



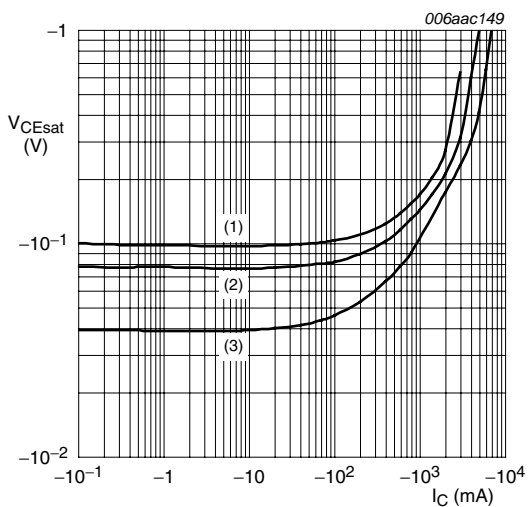
- $I_C/I_B = 20$
- (1)  $T_{amb} = -55\text{ °C}$
  - (2)  $T_{amb} = 25\text{ °C}$
  - (3)  $T_{amb} = 100\text{ °C}$

Fig 8. Base-emitter saturation voltage as a function of collector current; typical values



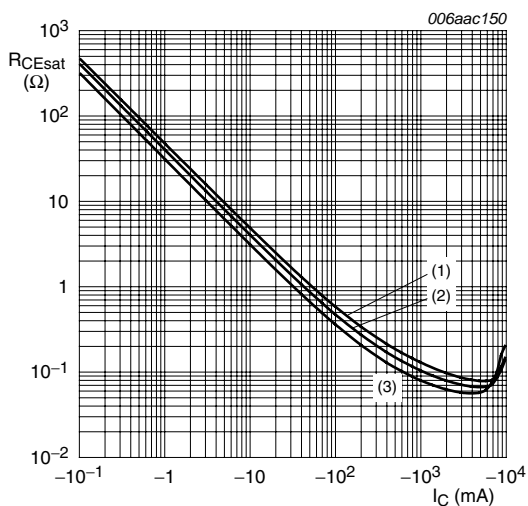
- $I_C/I_B = 20$
- (1)  $T_{amb} = 100\text{ }^{\circ}\text{C}$
  - (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$
  - (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

Fig 9. Collector-emitter saturation voltage as a function of collector current; typical values



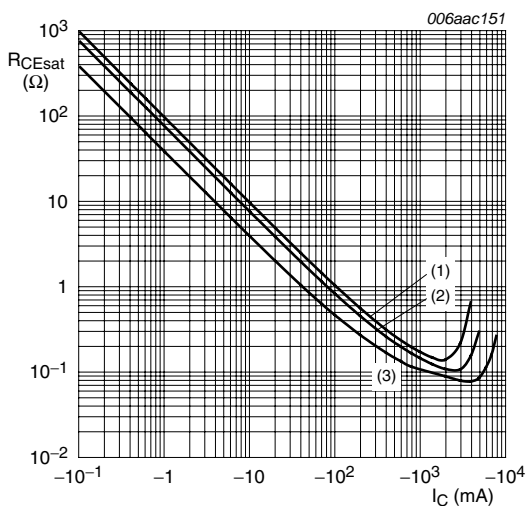
- $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (1)  $I_C/I_B = 100$
  - (2)  $I_C/I_B = 50$
  - (3)  $I_C/I_B = 10$

Fig 10. Collector-emitter saturation voltage as a function of collector current; typical values



- $I_C/I_B = 20$
- (1)  $T_{amb} = 100\text{ }^{\circ}\text{C}$
  - (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$
  - (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

Fig 11. Collector-emitter saturation resistance as a function of collector current; typical values



- $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (1)  $I_C/I_B = 100$
  - (2)  $I_C/I_B = 50$
  - (3)  $I_C/I_B = 10$

Fig 12. Collector-emitter saturation resistance as a function of collector current; typical values

## 8. Test information

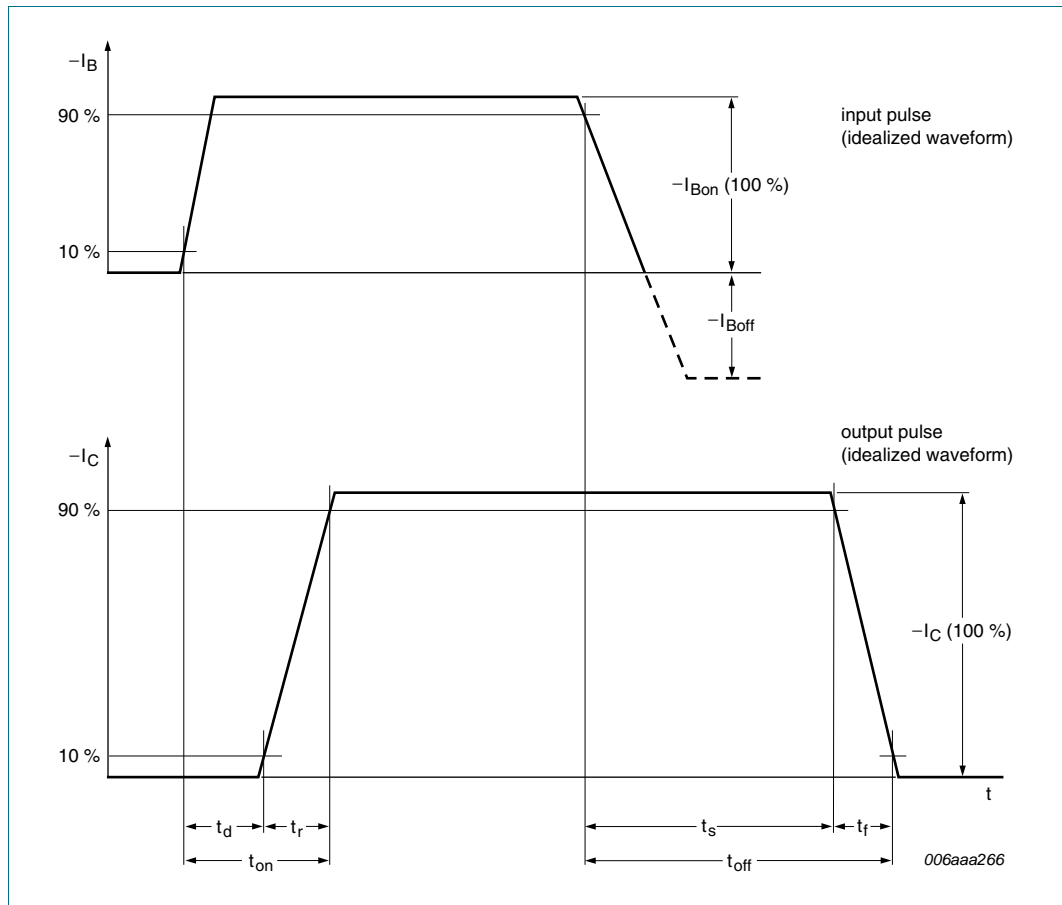


Fig 13. BISS transistor switching time definition

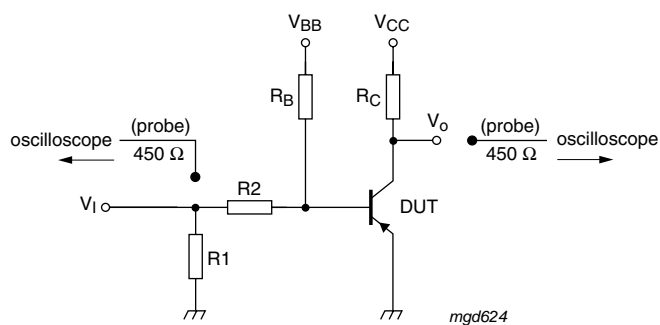
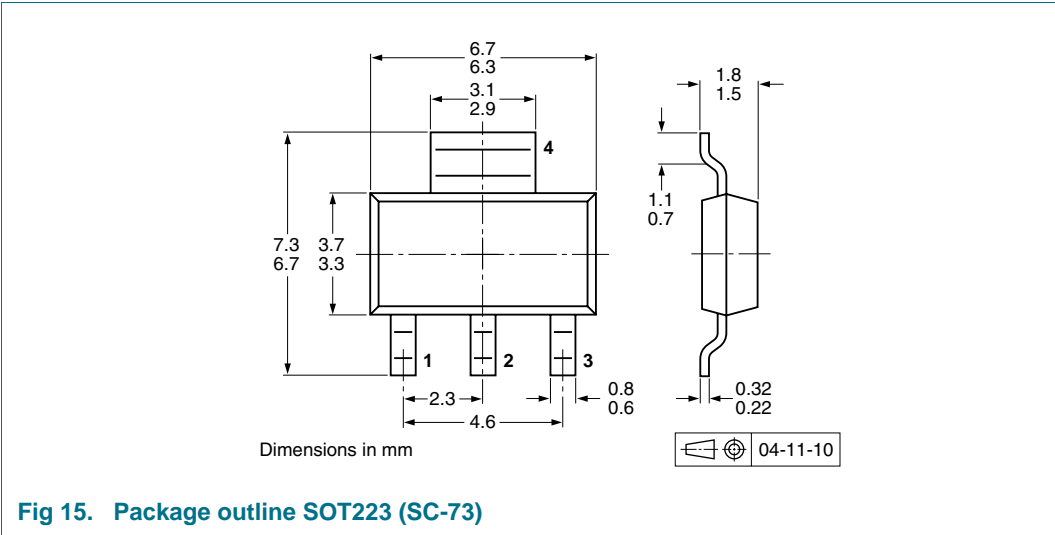


Fig 14. Test circuit for switching times

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



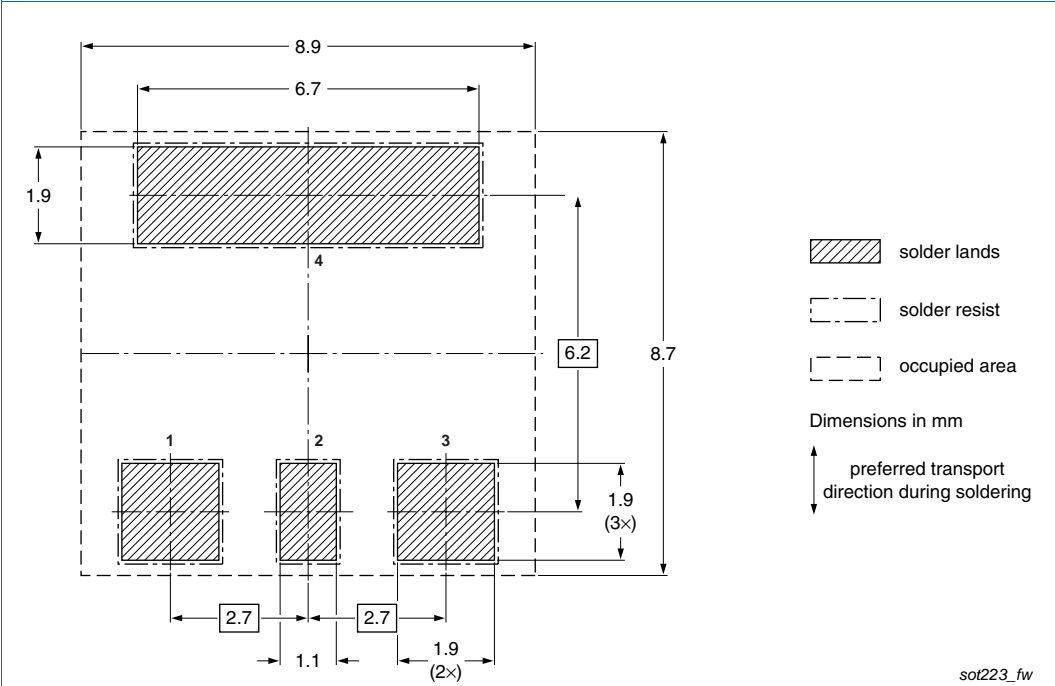
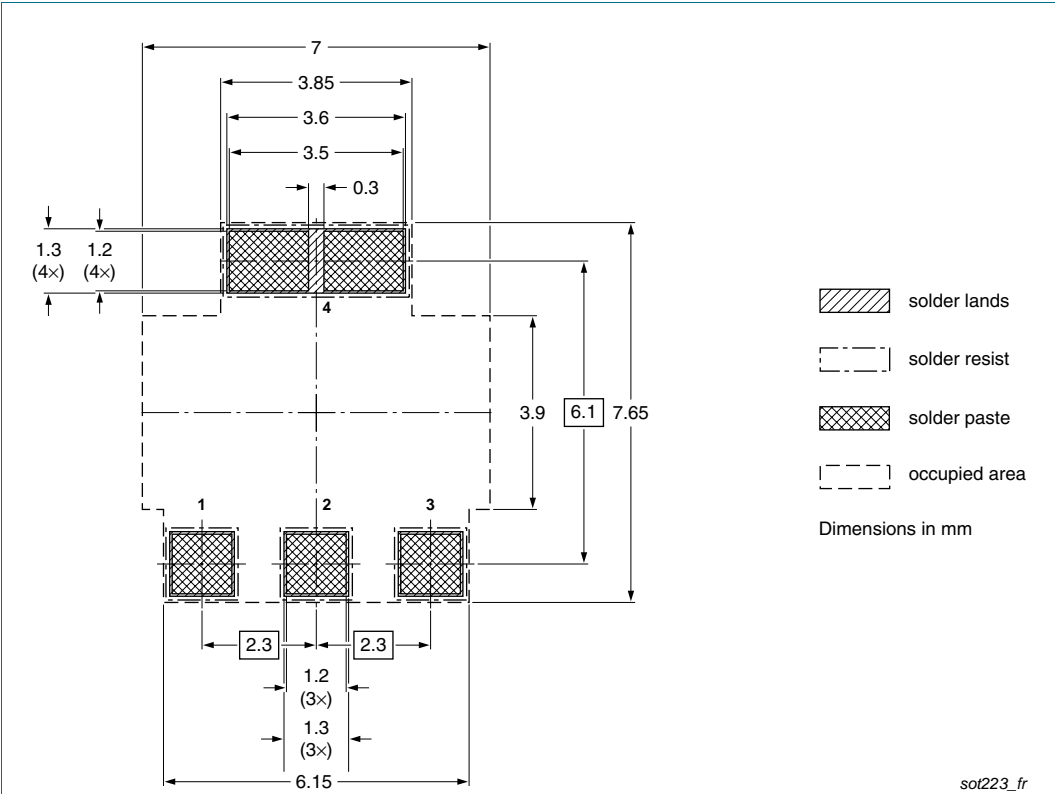
10. Packing information

**Table 8. Packing methods**  
The indicated -xxx are the last three digits of the 12NC ordering code.<sup>[1]</sup>

Type number	Package	Description	Packing quantity	
			1000	4000
PBSS4032PZ	SOT223	8 mm pitch, 12 mm tape and reel	-115	-135

[1] For further information and the availability of packing methods, see [Section 14](#).

11. Soldering



12. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4032PZ_1	20100331	Product data sheet	-	-

## 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

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For sales office addresses, please send an email to: [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)

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