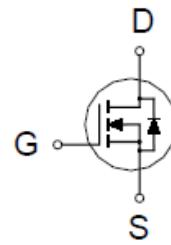
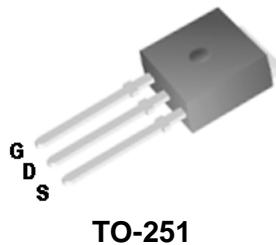


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N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
60V	65mΩ @ $V_{GS} = 10V$	18A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_C = 25^\circ C$	I_D	18	A
	$T_C = 100^\circ C$		11.8	
Pulsed Drain Current ¹		I_{DM}	34	A
Avalanche Current		I_{AS}	18	
Avalanche Energy	$L = 0.1mH$	E_{AS}	16	mJ
Power Dissipation	$T_C = 25^\circ C$	P_D	50	W
	$T_C = 100^\circ C$		20	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.5	°C / W

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

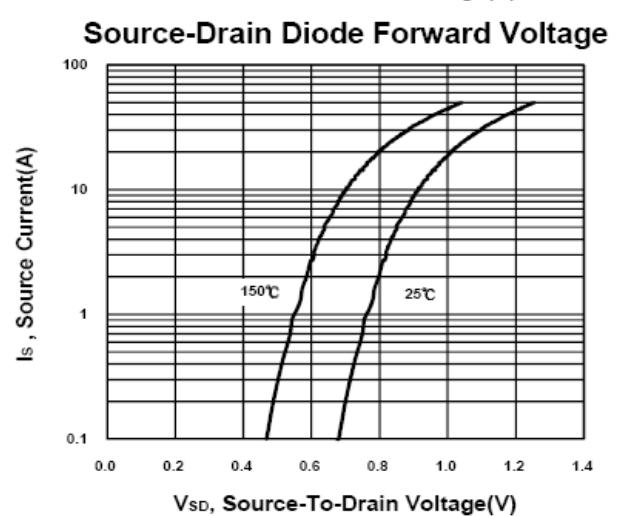
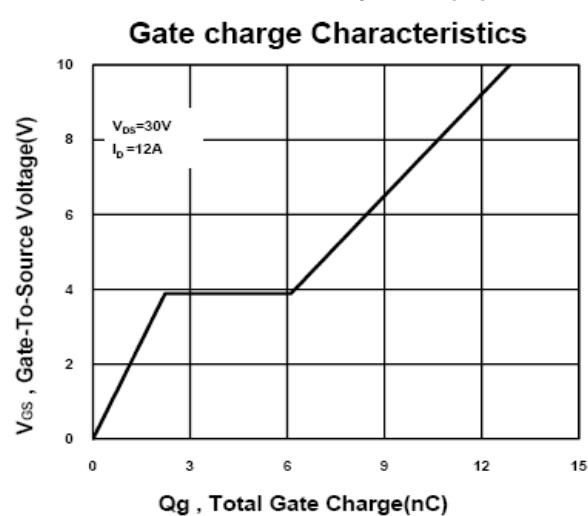
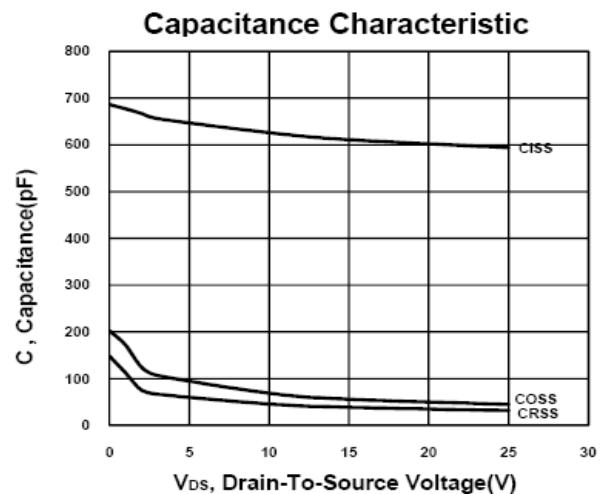
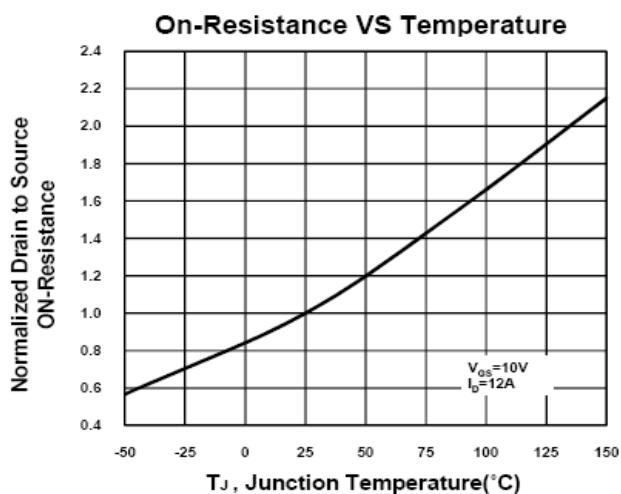
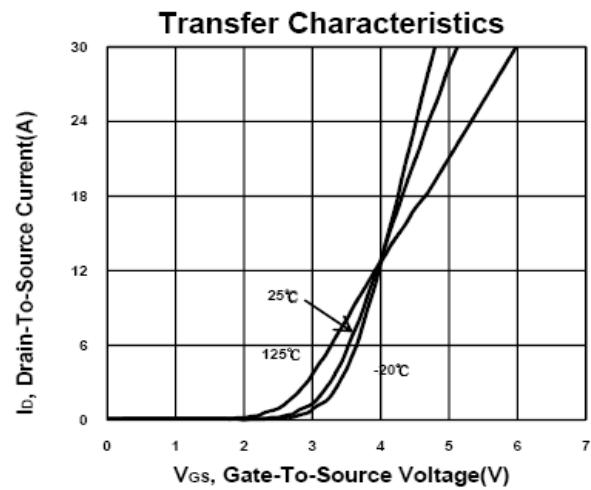
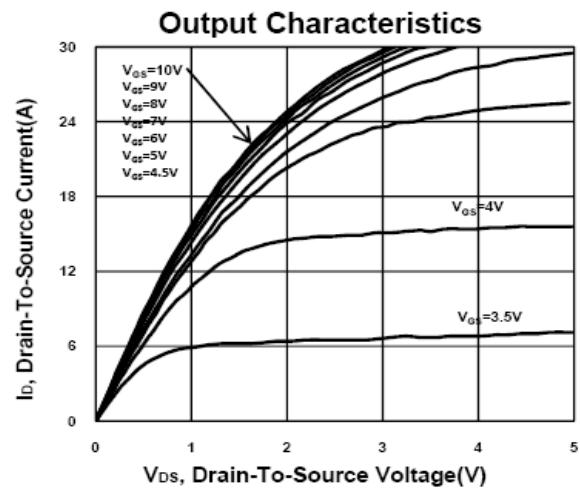
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.0	1.6	2.5	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 48\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 5\text{V}, I_D = 8\text{A}$		63	80	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 12\text{A}$		53	65	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 15\text{V}, I_D = 12\text{A}$		22		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$		601		pF
Output Capacitance	C_{oss}			48		
Reverse Transfer Capacitance	C_{rss}			33		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		2.2		Ω
Total Gate Charge ²	Q_g	$V_{\text{GS}} = 10\text{V}$		13		nC
				7.6		
Gate-Source Charge ²	Q_{gs}	$V_{\text{DS}} = 0.5V_{(\text{BR})\text{DSS}}, I_D = 12\text{A}$		4.5		nC
Gate-Drain Charge ²	Q_{gd}			2.4		
Turn-On Delay Time ²	$t_{\text{d(on)}}$			8.3		nS
Rise Time ²	t_r			37		
Turn-Off Delay Time ²	$t_{\text{d(off)}}$	$V_{\text{DS}} = 30\text{V}, I_D \geq 12\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		44		nS
Fall Time ²	t_f			53		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				18	A
Forward Voltage ¹	V_{SD}	$I_F = 12\text{A}, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 12\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		23		nS
Reverse Recovery Charge	Q_{rr}			19		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

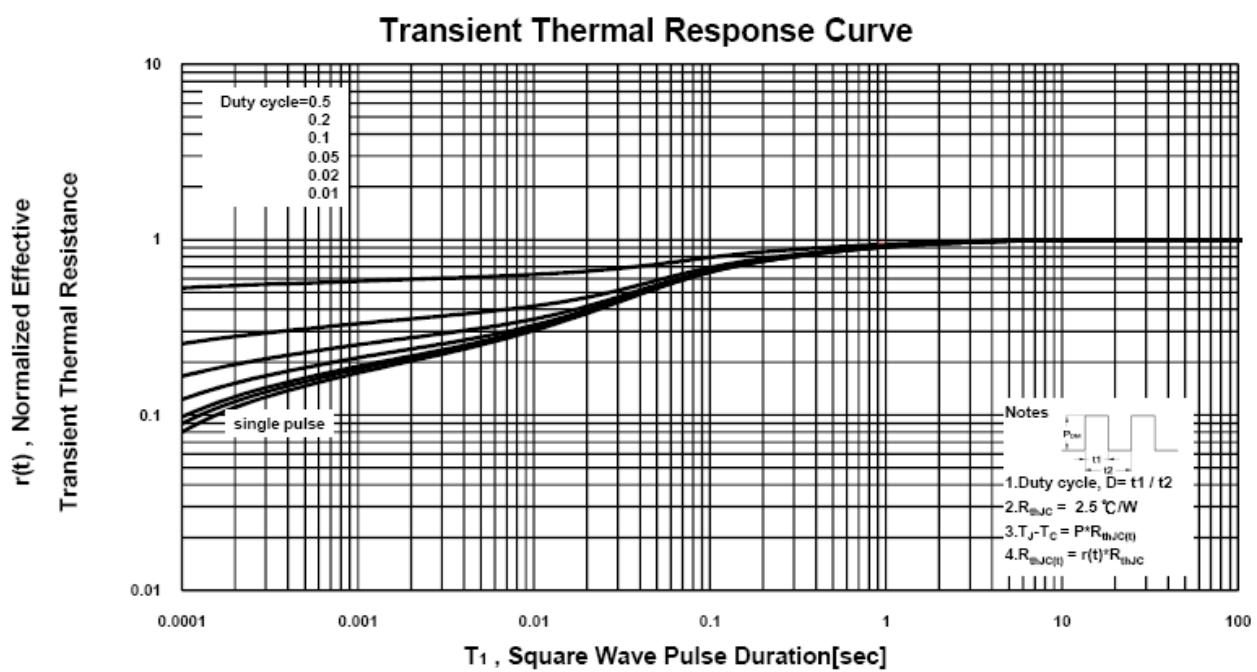
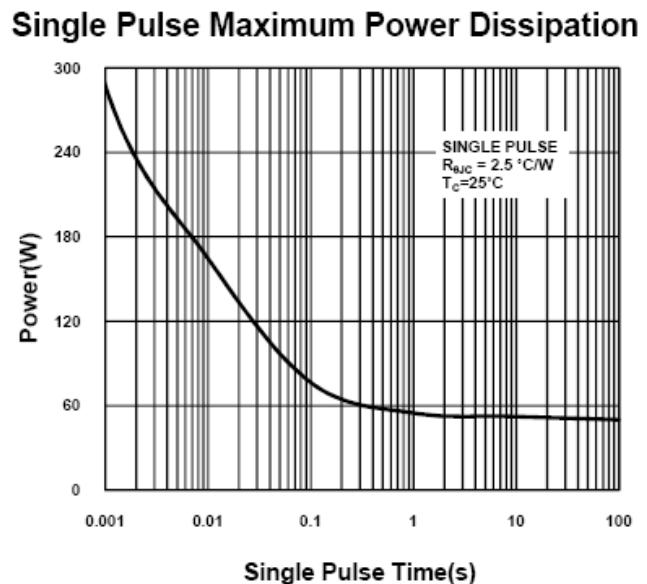
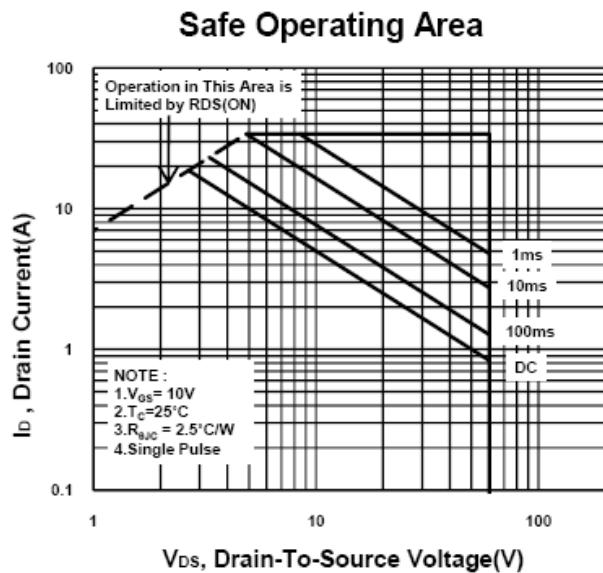
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Package Dimension

TO-251 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	14	15	17.14	H	0.89		1.7
B	2.1	2.3	2.5	I	6.3		6.8
C	0.4	0.5	0.6	J	4.8		5.5
D	0.35	0.5	0.65	K	0.5	0.84	1.14
E	0.9	1.1	1.5	L	0.4	0.76	0.912
F	7		9.65	M		2.3	
G	5.3		6.22	N	1.4	2.16	2.23

