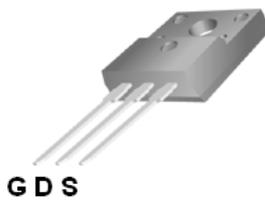


# P5015ATF

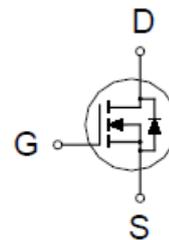
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
150V	50m $\Omega$ @ $V_{GS} = 10V$	22A



TO-220F



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>2</sup>	$T_C = 25\text{ }^\circ\text{C}$	$I_D$	22	A
	$T_C = 100\text{ }^\circ\text{C}$		14	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	90	
Avalanche Current		$I_{AS}$	26	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	35	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	$P_D$	56	W
	$T_C = 100\text{ }^\circ\text{C}$		22	
Operating Junction & Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.25	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.

# P5015ATF

## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

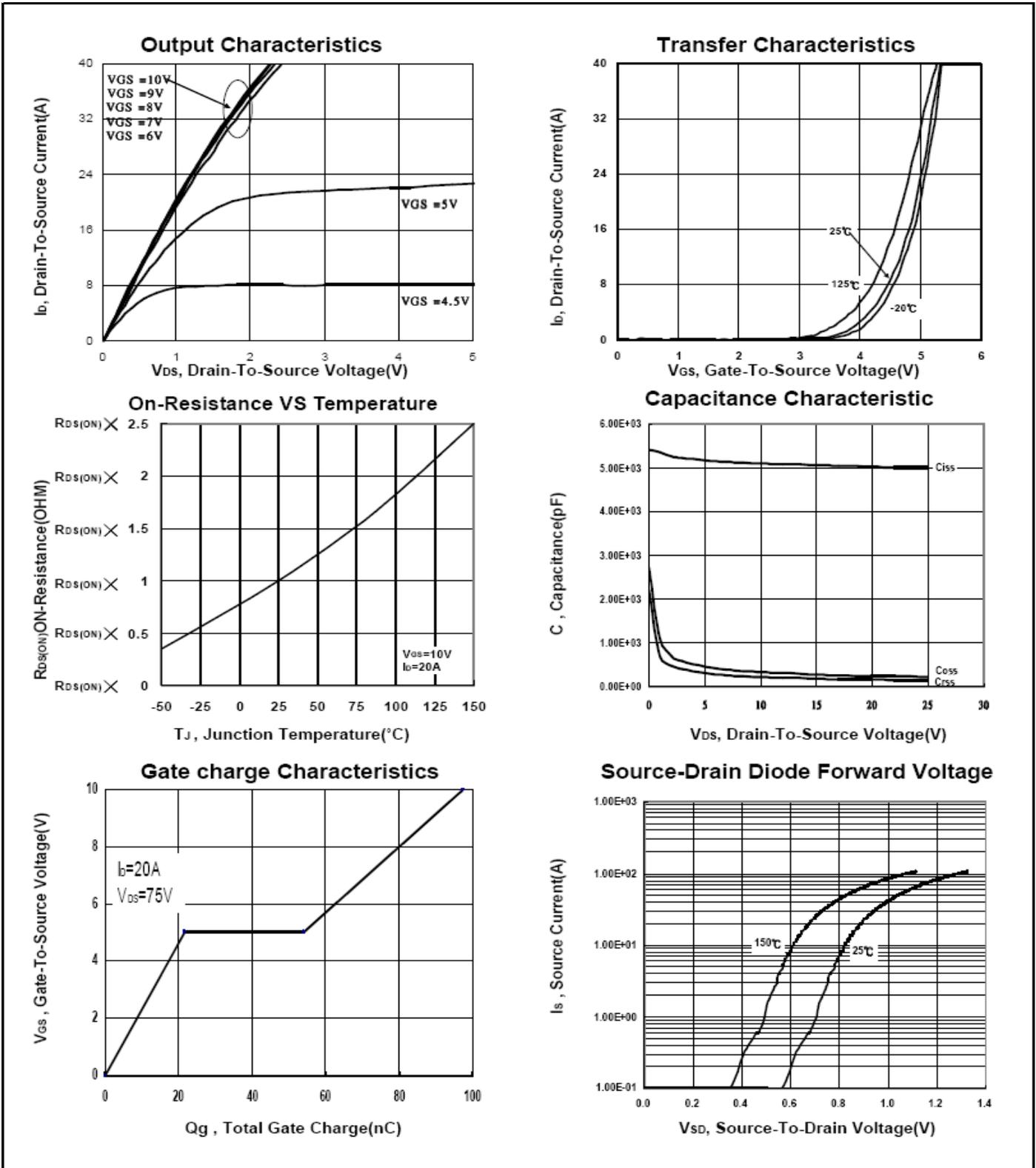
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	150			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.5	2.5	4.0	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V			±250	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V			1	μA
		V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C			10	
On-State Drain Current <sup>1</sup>	I <sub>D(ON)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 10V	90			A
Drain-Source On-State	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		40	50	mΩ
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 25V, I <sub>D</sub> = 20A		40		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz		5240		pF
Output Capacitance	C <sub>oss</sub>			223		
Reverse Transfer Capacitance	C <sub>riss</sub>			152		
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 75V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		100		nC
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>			23		
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>			37		
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 75V, I <sub>D</sub> ≅ 20A V <sub>GS</sub> = 10V, R <sub>GS</sub> = 2.5Ω		16		nS
Rise Time <sup>2</sup>	t <sub>r</sub>			70		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>			25		
Fall Time <sup>2</sup>	t <sub>f</sub>			40		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current <sup>3</sup>	I <sub>S</sub>				22	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V			1.4	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A, dI <sub>F</sub> /dt = 100A / μS		87		nS
Reverse Recovery Charge	Q <sub>rr</sub>				265	

<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

<sup>2</sup>Independent of operating temperature.

# P5015ATF

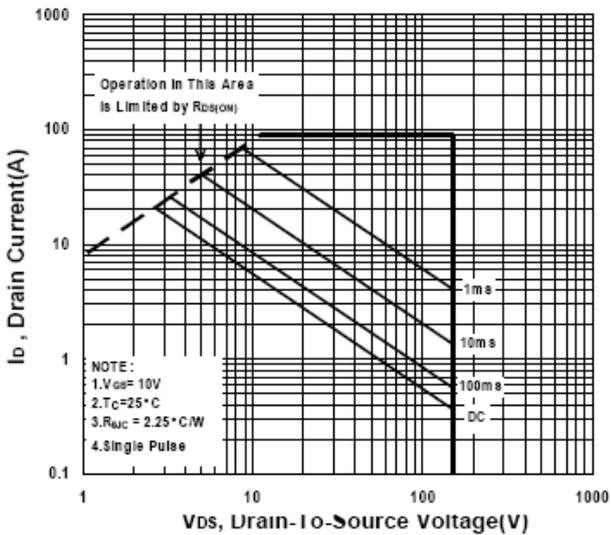
## N-Channel Enhancement Mode MOSFET



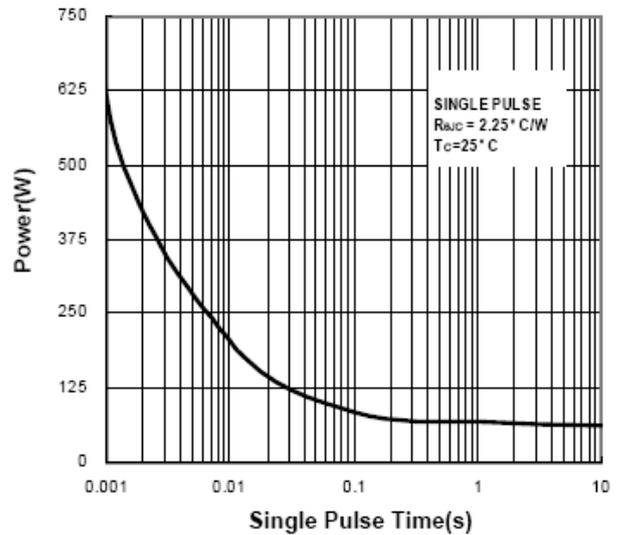
# P5015ATF

## N-Channel Enhancement Mode MOSFET

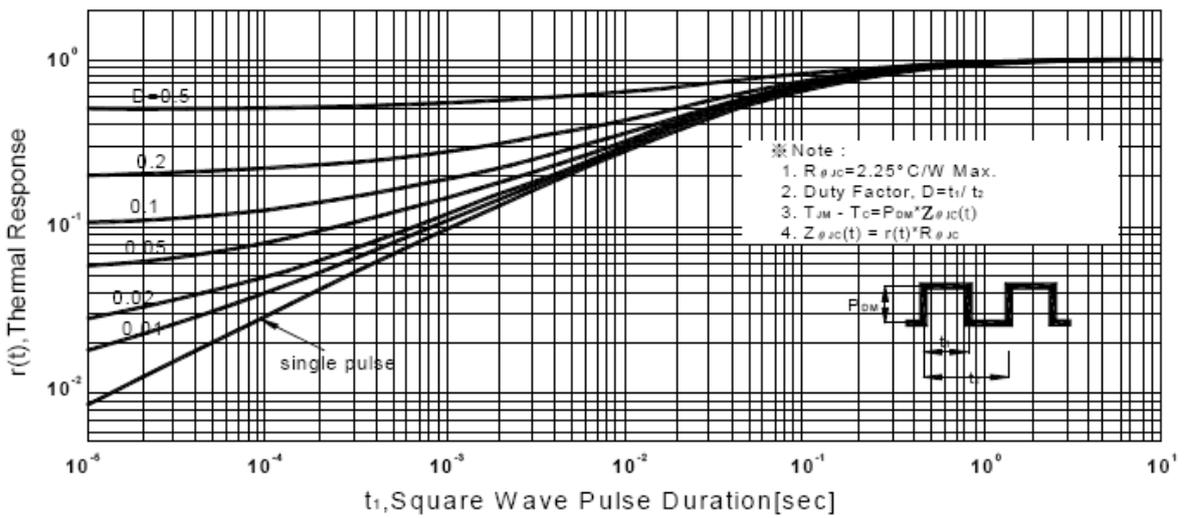
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**



# P5015ATF

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-220F (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.93	e	2.05	2.55	3.05
A1	2.34		3.1	F	27.45		30.6
B	17.77		20.3	G	7.72		9.3
b	0.6		1.05	H	6.1		7.1
b1	0.9	1.23	1.62	L	12.5		14.5
b2	0.6		1.9	L1	1.97		3.8
c	0.4		1.0	P	2.98		3.4
D	14.7		16.4	Q	2.1		2.96
D1	6.4		7.5	q	3.0		3.8
E	9.7		10.4				

