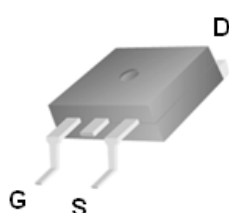


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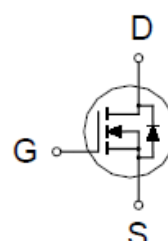
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	$50m\Omega$ @ $V_{GS} = 10V$	34A



TO-263



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	34	A
	$T_C = 100\text{ }^{\circ}\text{C}$		21	
Pulsed Drain Current ¹		I_{DM}	120	
Avalanche Current		I_{AS}	37	
Avalanche Energy	$L = 0.1mH$	E_{AS}	70	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	125	W
	$T_C = 100\text{ }^{\circ}\text{C}$		50	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80V, V_{GS} = 0V$			1	μA
		$V_{DS} = 80V, V_{GS} = 0V, T_J = 125^\circ C$			10	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = 10V, V_{GS} = 10V$	120			A
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 7V, I_D = 10A$		60	80	$m\Omega$
		$V_{GS} = 10V, I_D = 15A$		37	50	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 20V, I_D = 15A$		10		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		1770		pF
Output Capacitance	C_{oss}			201		
Reverse Transfer Capacitance	C_{rss}			137		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		2		Ω
Total Gate Charge ²	Q_g	$V_{DS} = 50V, V_{GS} = 10V, I_D = 15A$		42		nC
Gate-Source Charge ²	Q_{gs}			11		
Gate-Drain Charge ²	Q_{gd}			22		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 50V, I_D \cong 15A,$ $V_{GS} = 10V, R_{GS} = 2.5\Omega$		76		nS
Rise Time ²	t_r			251		
Turn-Off Delay Time ²	$t_{d(off)}$			134		
Fall Time ²	t_f			61		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current	I_S				34	A
Forward Voltage ¹	V_{SD}	$I_F = 15A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$V_{GS} = 0V, I_F = 15A,$ $dI_F/dt = 100A / \mu S$		70.6		nS
Reverse Recovery Charge	Q_{rr}			189		μC

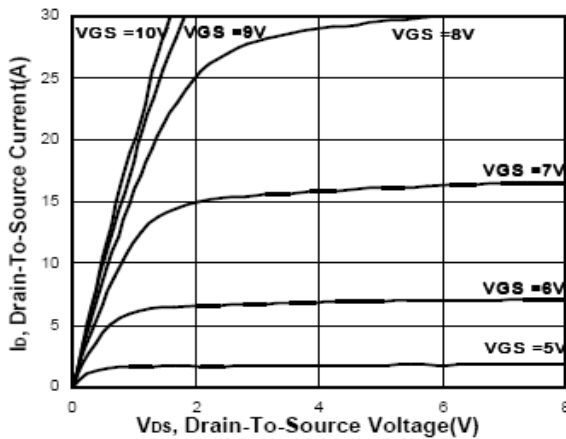
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

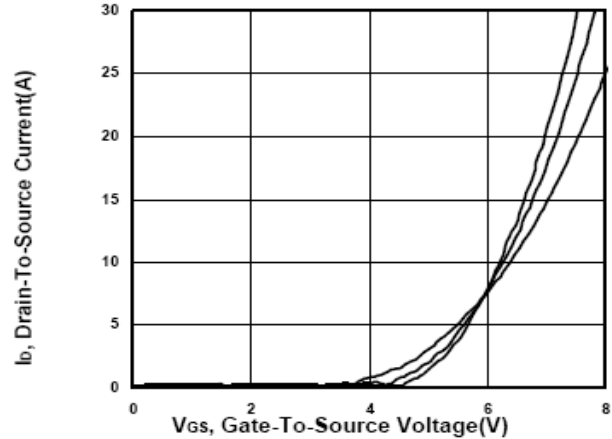
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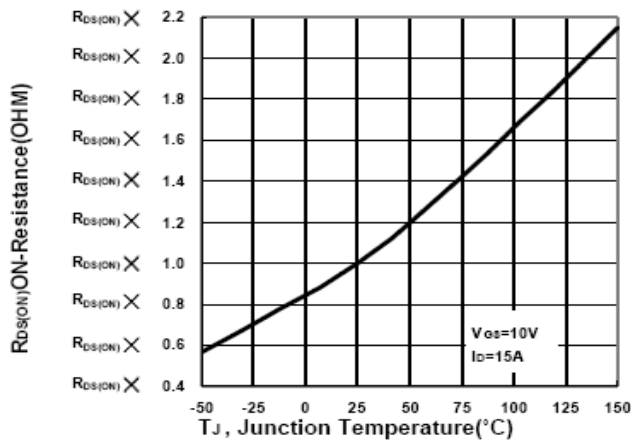
Output Characteristics



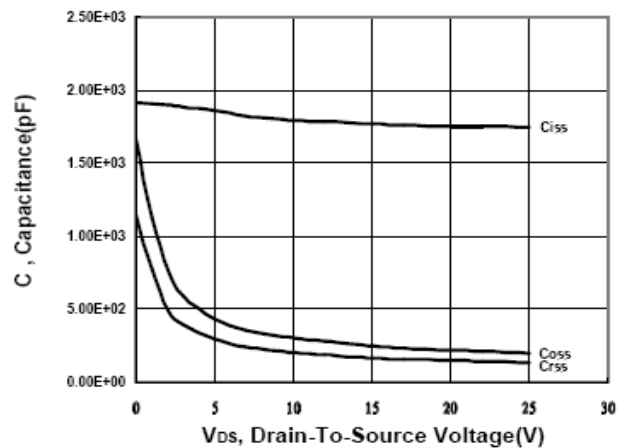
Transfer Characteristics



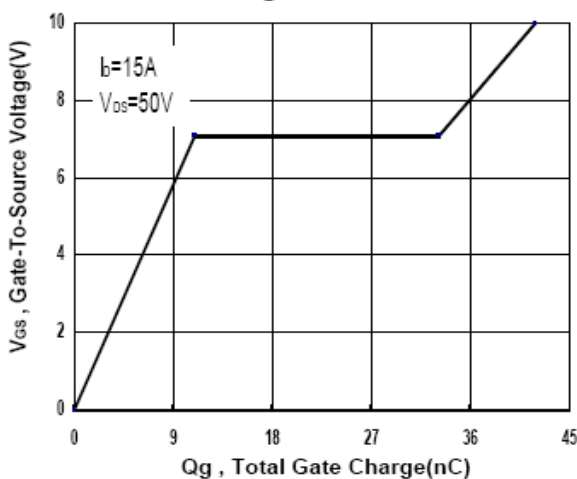
On-Resistance VS Temperature



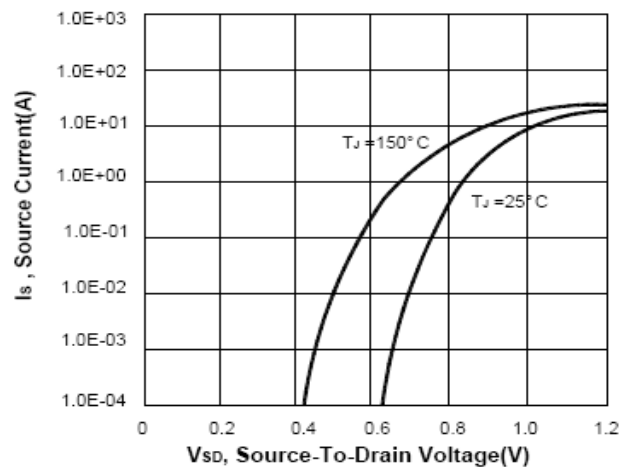
Capacitance Characteristic



Gate charge Characteristics



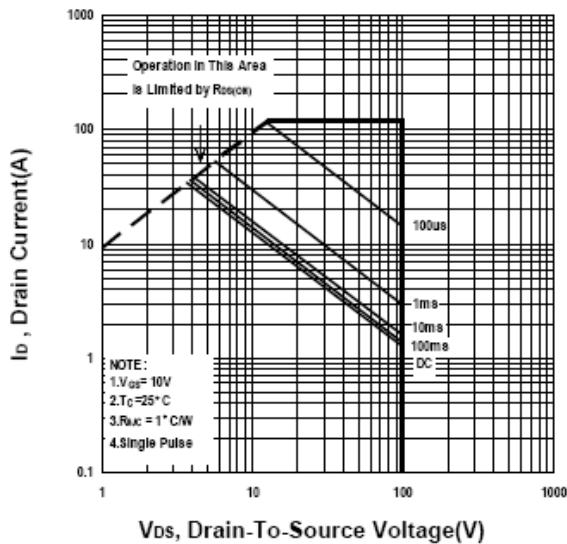
Source-Drain Diode Forward Voltage



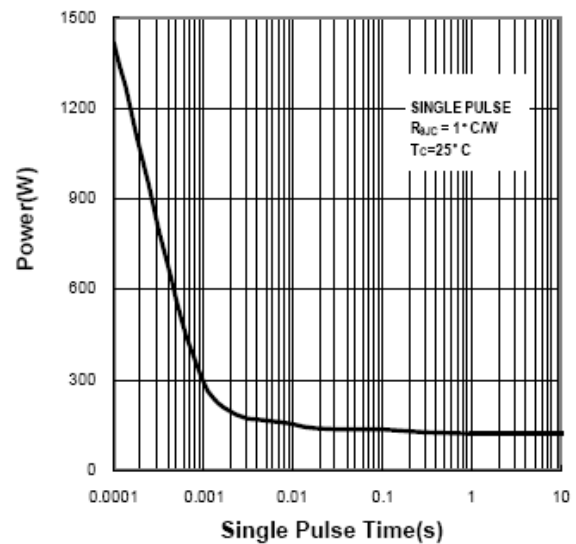
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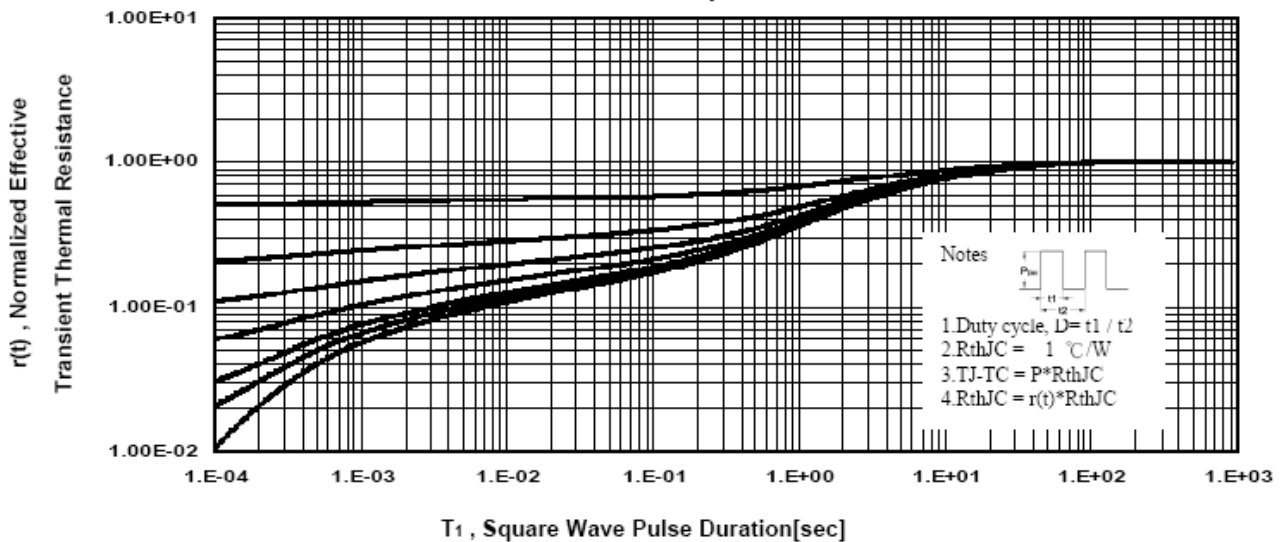
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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Package Dimension

TO-263 (D²PAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.2		4.8	e	4.08	5.08	6.08
A1	0		0.3	E	9.8		10.55
b	0.71		1.06	E1	6.9		8.7
b2	1.07		1.47	H	14.2		15.8
C	0.3		0.69	L	1.2		2.79
C2	1.15		1.45	L1	1		1.65
D	8.3		9.4	L2	1.2		1.78
D1	6.37		8.23				

