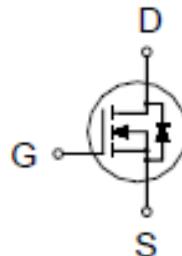
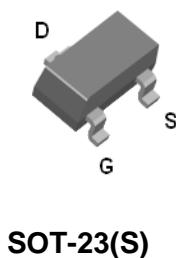


# P3202CMA

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
20V	32mΩ @ $V_{GS} = 4.5V$	5A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$I_D$	5	A
		4	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	30	
Power Dissipation	$P_D$	1.3	W
		0.8	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$R_{\theta JA}$		100	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper,in a still air environment with  $T_A = 25^\circ C$ .

## P3202CMA

### N-Channel Enhancement Mode MOSFET

#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

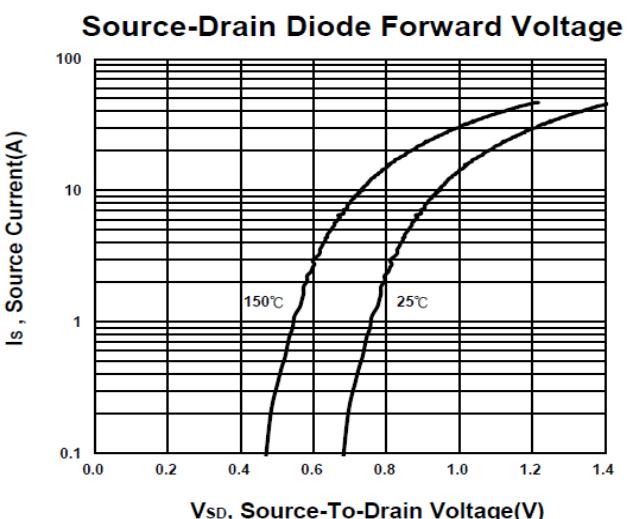
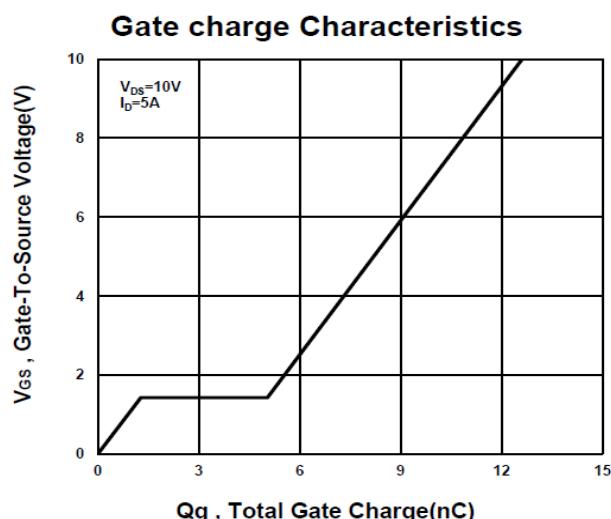
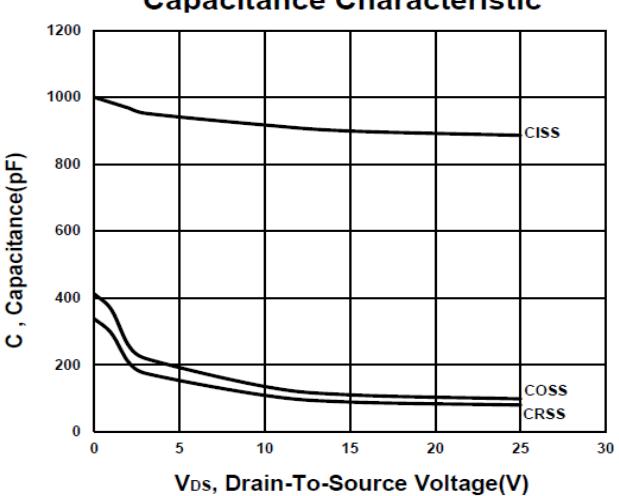
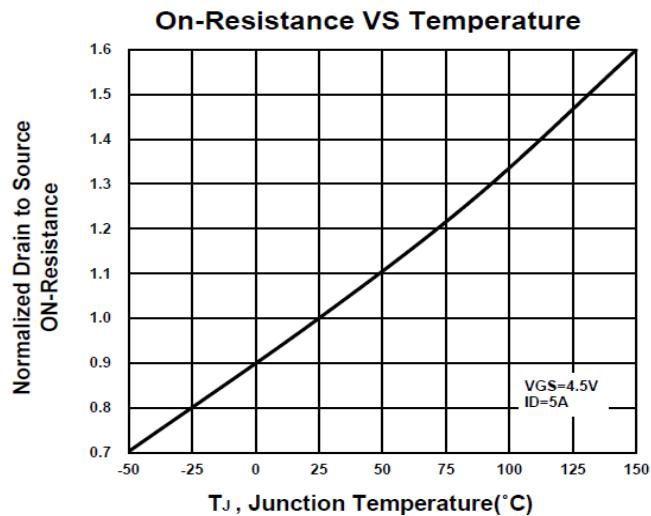
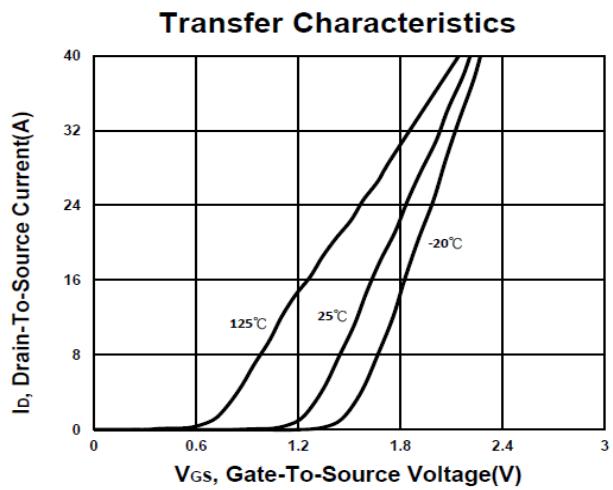
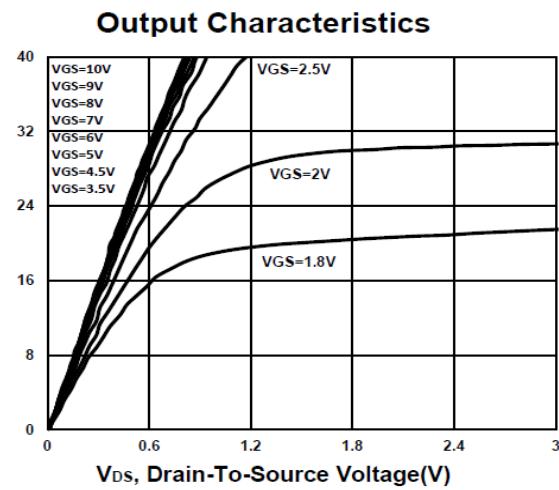
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.45	0.7	1.2	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 12\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 16\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 70^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 1.8\text{V}, I_D = 2\text{A}$		29	80	mΩ
		$V_{\text{GS}} = 2.5\text{V}, I_D = 4\text{A}$		22	50	
		$V_{\text{GS}} = 4.5\text{V}, I_D = 5\text{A}$		19	32	
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5\text{V}, I_D = 5\text{A}$		30		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 10\text{V}, f = 1\text{MHz}$		917		pF
Output Capacitance	$C_{\text{oss}}$			140		
Reverse Transfer Capacitance	$C_{\text{rss}}$			119		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, I_D = 5\text{A}$		13		nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			1.3		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			4.4		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}$ $I_D \geq 5\text{A}, V_{\text{GEN}} = 4.5\text{V}, R_G = 6\Omega$		23		nS
Rise Time <sup>2</sup>	$t_r$			31		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			55		
Fall Time <sup>2</sup>	$t_f$			26		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$				0.9	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 5\text{A}, V_{\text{GS}} = 0\text{V}$			1.3	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 5\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		12		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			4		nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

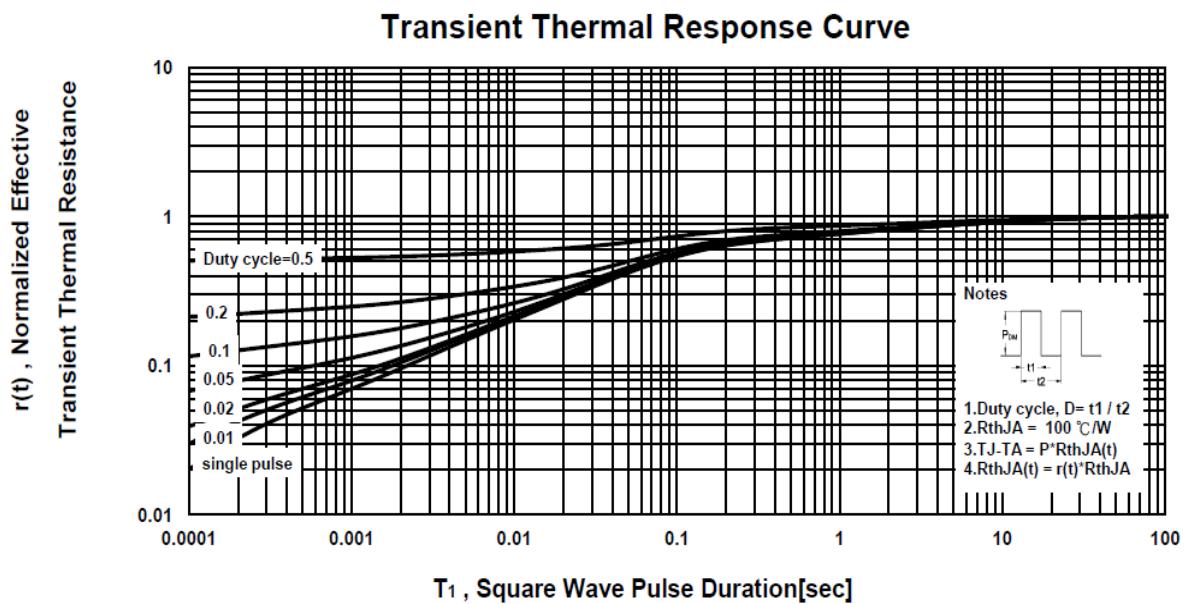
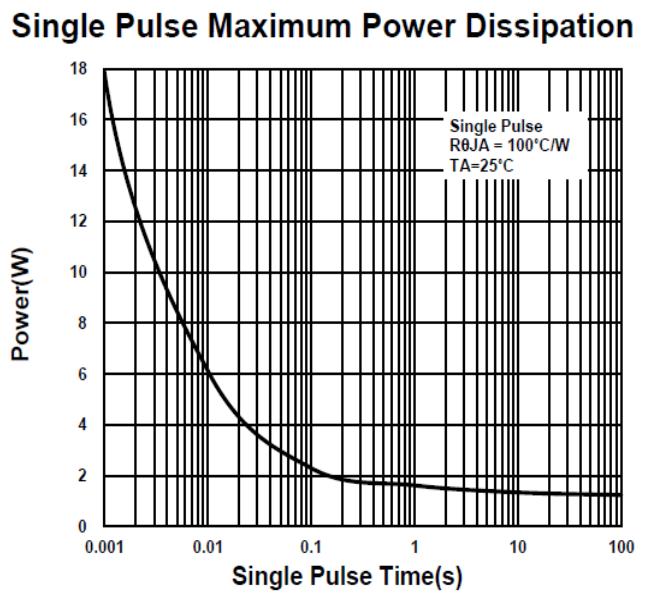
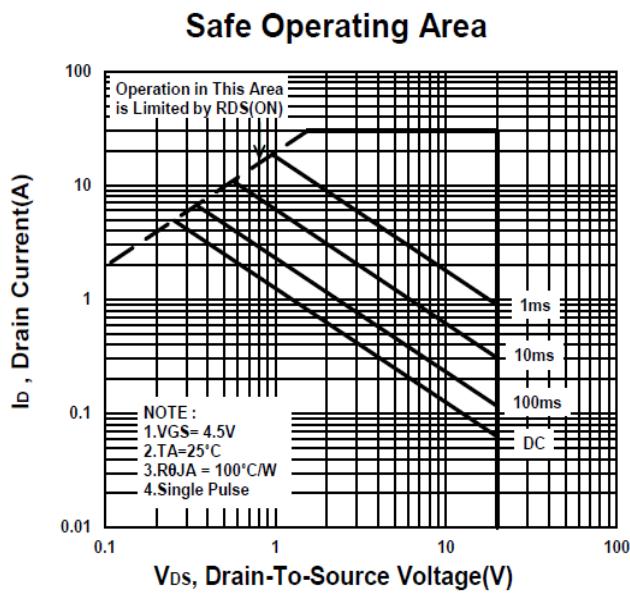
# P3202CMA

## N-Channel Enhancement Mode MOSFET



## P3202CMA

### N-Channel Enhancement Mode MOSFET



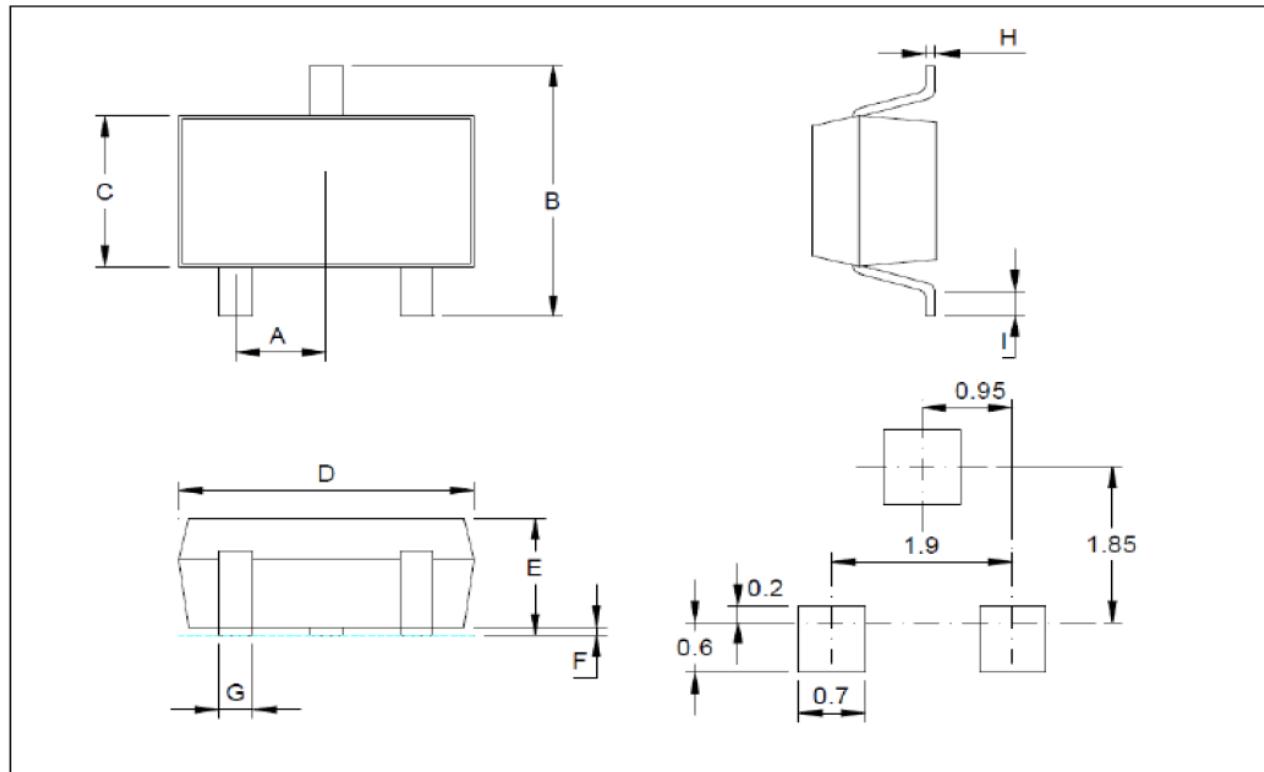
## P3202CMA

### N-Channel Enhancement Mode MOSFET

#### Package Dimension

#### SOT-23 (S) MECHANICAL DATA

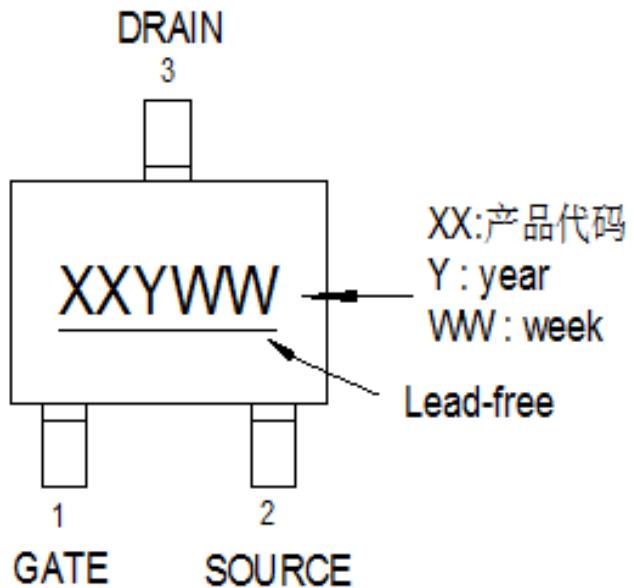
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.25		2.85	I	0.15		0.6
C	1.2		1.4				
D	2.8		3.04				
E	0.89		1.2				
F	0		0.1				
G	0.3		0.5				



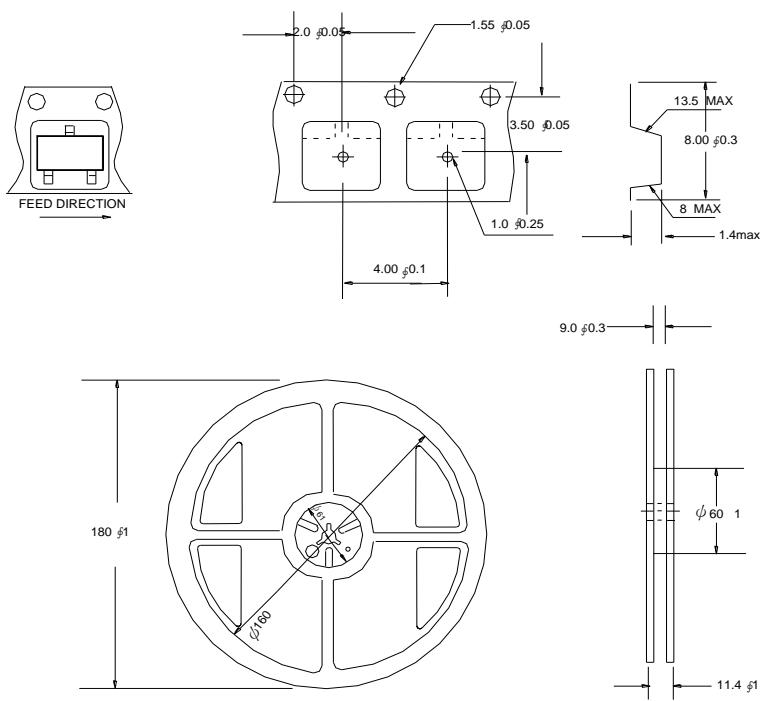
## P3202CMA

### N-Channel Enhancement Mode MOSFET

#### A. Marking Information (此产品代码为：15)



#### B. Tape&Reel Information: 3000pcs/Reel



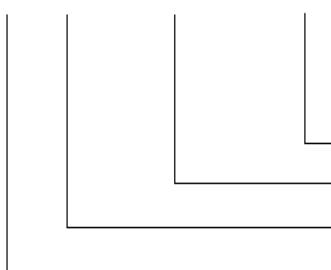
## P3202CMA

### N-Channel Enhancement Mode MOSFET

#### C. Lot.No. & Date Code rule

##### 1.LOT.NO.

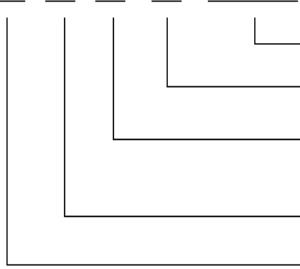
M N 15M21 03



- #8~9 Sub-lot No
- Order series no.
- Foundry site
- Assembly site

##### 2.Date Code

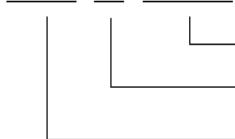
D Y M X XXX



- Order series no. & Sub-lot No
- Week
- M : Month (A:Jan , B:Feb , C:Mar ,D :Apr ,E:May ,F:Jun,G:Jul,H:Aug,I:Sep,J:Oct,K:Nov,L:Dec.)
- Y : Year (N : 2011, O : 2012 ...)
- Assembly site

##### 3.Date Code (for Small package)

XX Y WW



- Week
- Y : Year (9: 2009,A : 2010, B : 2011 ...)
- Device Name

## P3202CMA

### N-Channel Enhancement Mode MOSFET

#### D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert “ / “ between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least