

P30FE7R5SLK

Power MOSFETs
75V, 30A, N-channel

Feature

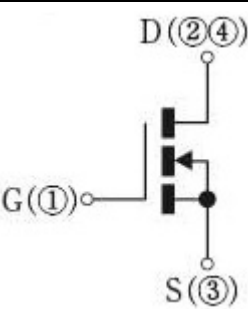
- N-channel
- SMD
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FE
Package (JEDEC Code): TO-252AB similar
Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel tempertature	Tch		175	°C
Drain-source voltage	V _{DSS}		75	V
Gate-source voltage	V _{GSS}		±20	V
Continuous drain current(DC)	I _D		30	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10μs, duty=1/100	90	A
Total power dissipation	P _T		44	W
Single avalanche current	I _{AS}	Starting Tch=25°C Tch≤150°C	21	A
Single avalanche energy	E _{AS}	Starting Tch=25°C Tch≤150°C	22	mJ

※ :See the original Specifications

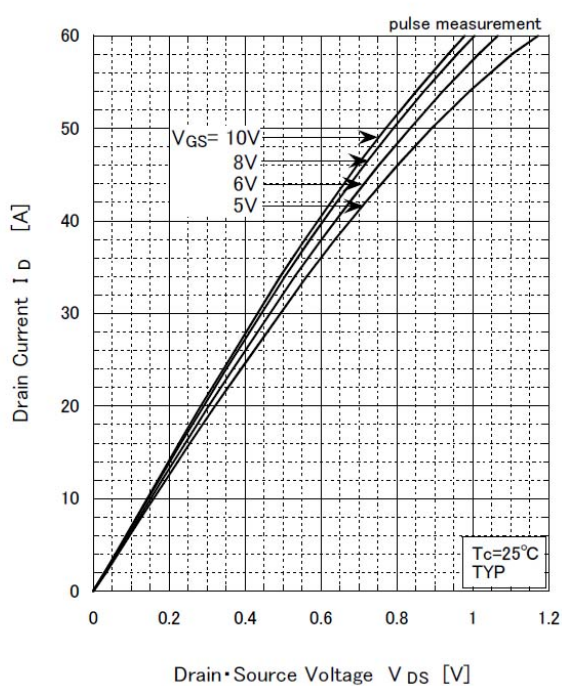
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	75			V
Zero gate voltage drain current	I_{DSS}	VDS=75V, VGS=0V			1	μA
Gate-source leakage current	I_{GSS}	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	g_{fs}	ID=15A, VDS=10V	9			S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=15A, VGS=10V		0.0141	0.0178	Ω
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=15A, VGS=4.5V		0.0167	0.023	Ω
Gate threshold voltage	V_{th}	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	V_{SD}	IS=30A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case			3.4	°C/W
Total gate charge	Q_g	VDD=60V, VGS=10V, ID=30A		45		nC
Gate to source charge	Q_{gs}	VDD=60V, VGS=10V, ID=30A		9.5		nC
Gate to drain charge	Q_{gd}	VDD=60V, VGS=10V, ID=30A		13		nC
Input capacitance	C_{iss}	VDS=25V, VGS=0V, f=1MHz		2020		pF
Reverse transfer capacitance	C_{rss}	VDS=25V, VGS=0V, f=1MHz		97		pF
Output capacitance	C_{oss}	VDS=25V, VGS=0V, f=1MHz		193		pF
Turn-on delay time	$t_{d(on)}$	ID=15A, RL=2.5Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		6		ns
Rise time	t_r	ID=15A, RL=2.5Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		17		ns
Turn-off delay time	$t_{d(off)}$	ID=15A, RL=2.5Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		29		ns
Fall time	t_f	ID=15A, RL=2.5Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		18		ns
Diode reverse recovery time	t_{rr}	IF=30A, VGS=0V, di/dt=100A/μs		45		ns
Diode reverse recovery charge	Q_{rr}	IF=30A, VGS=0V, di/dt=100A/μs		63		nC

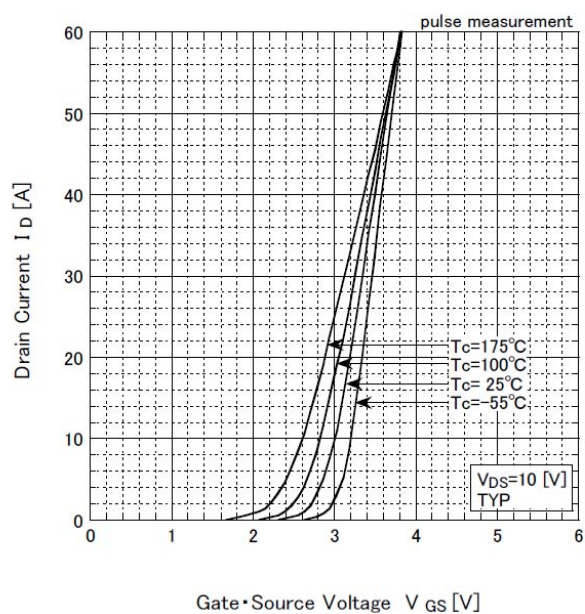
※ : See the original Specifications

CHARACTERISTIC DIAGRAMS

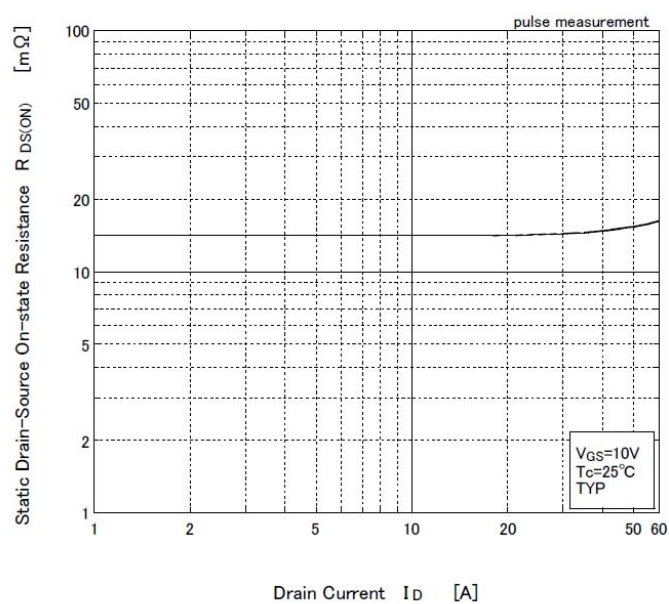
Typical Output Characteristics



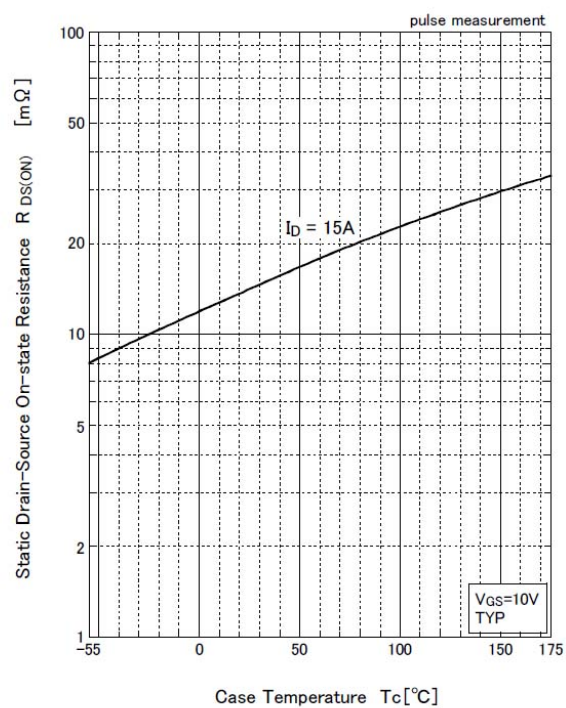
Transfer Characteristics

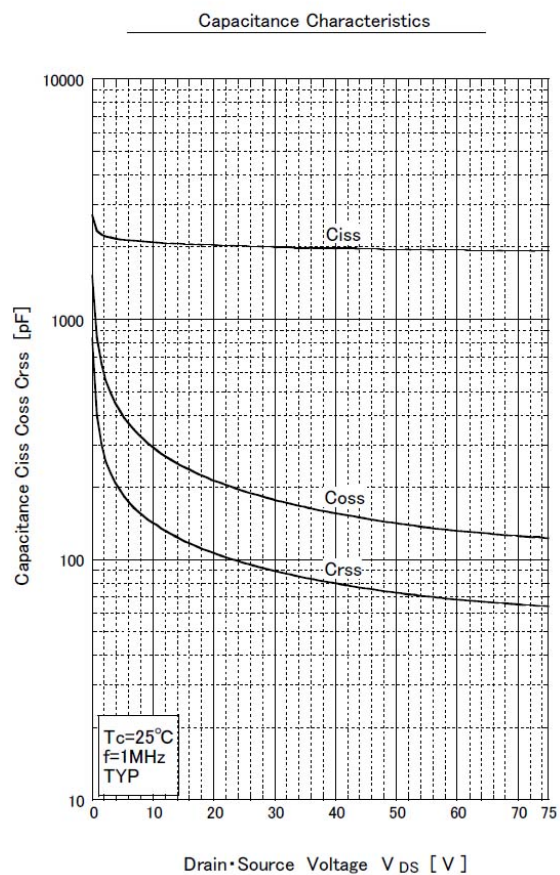
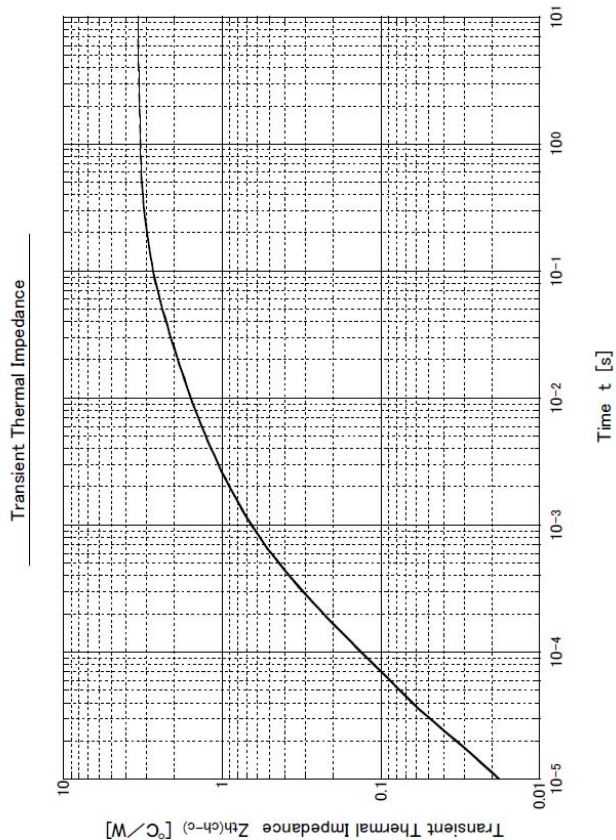
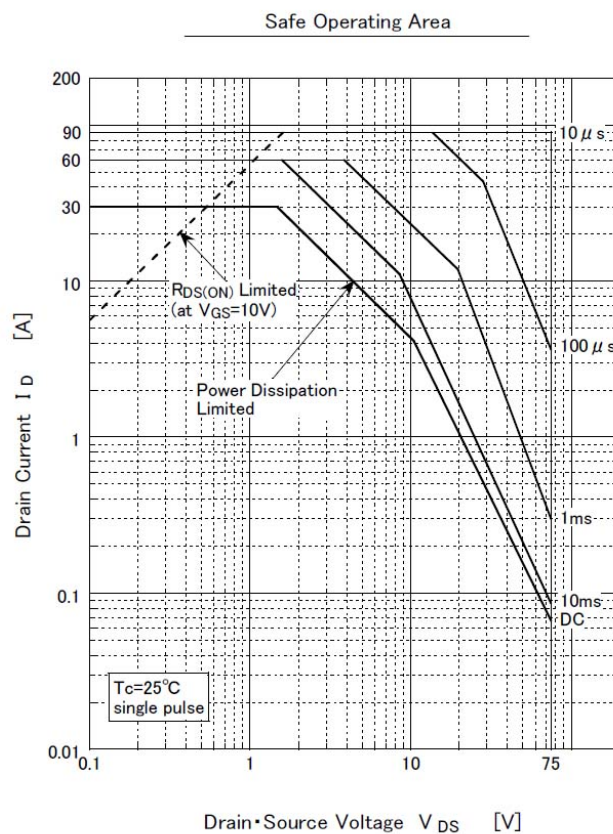
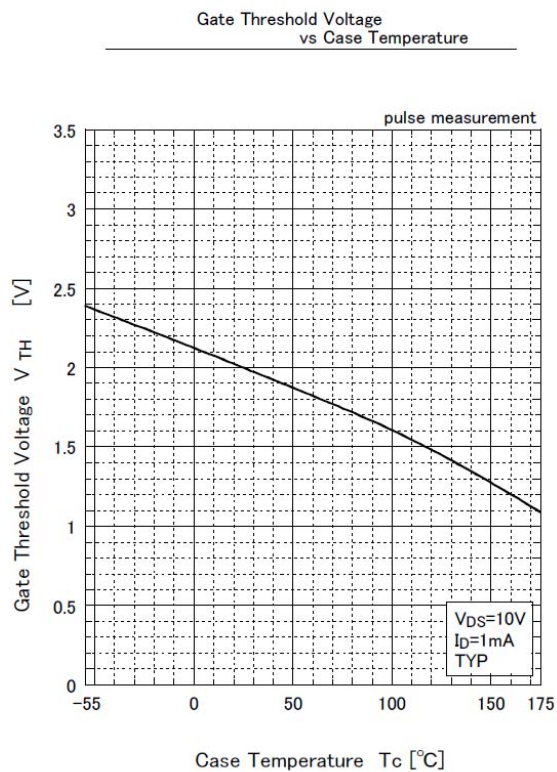


Static Drain-Source On-state Resistance vs Drain Current

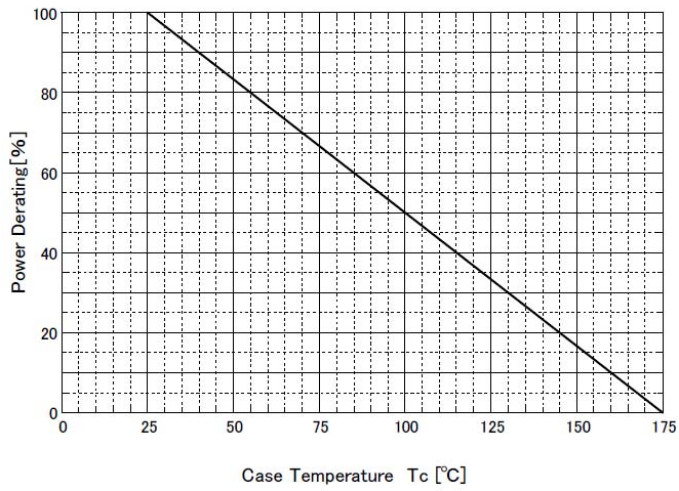


Static Drain-Source On-state Resistance vs Case Temperature

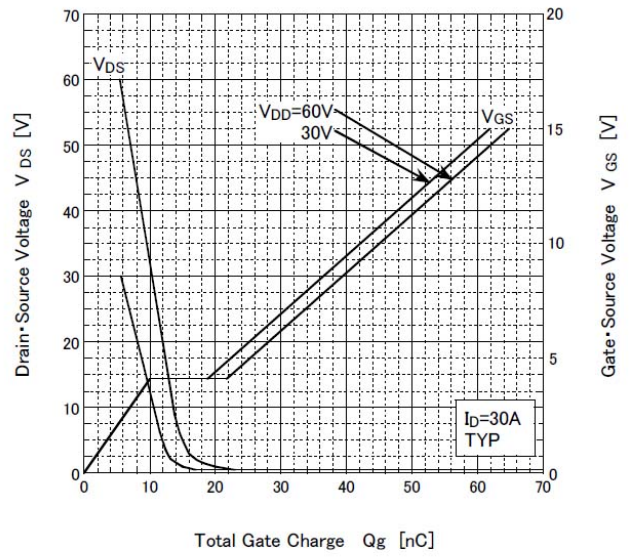




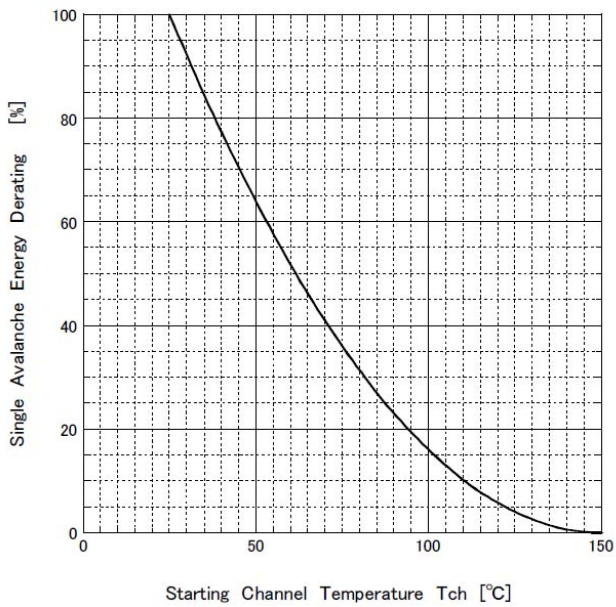
Power Derating – Case Temperature



Gate Charge Characteristics

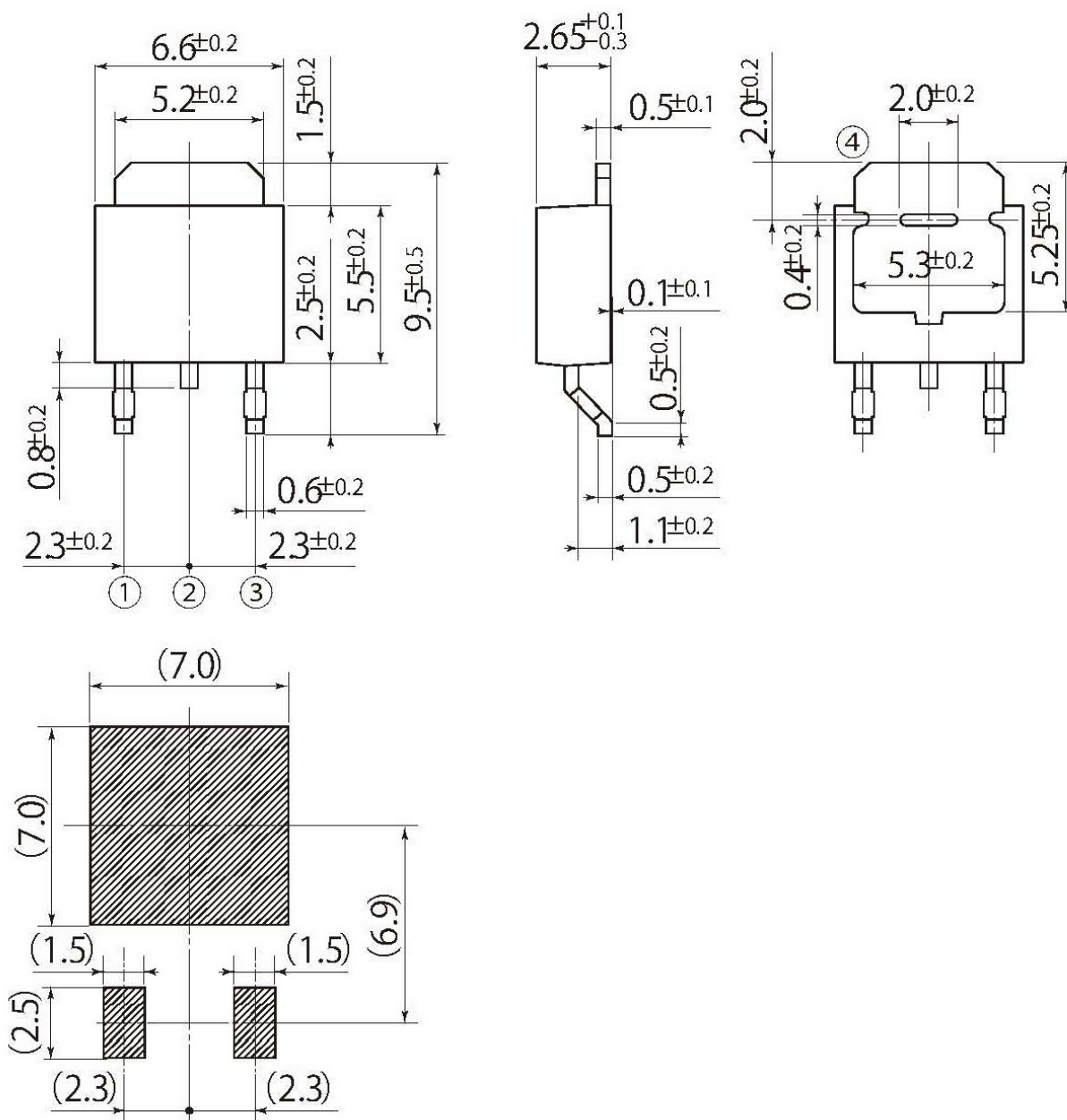


Single Avalanche Energy Derating
vs Channel Temperature



G3

JEDEC Code	TO-252AB similar
JEITA Code	SC-63
House Name	FE



Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

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