

NUF6105FC

6 Channel EMI Pi-Filter Array with ESD Protection

This device is a 6 channel EMI filter array for data lines. Greater than -35 dB attenuation is obtained at frequencies from 800 MHz to 2.2 GHz. It also offers ESD protection – clamping transients from static discharges to protect delicate data line circuitry.

Features

- EMI Filtering and ESD Protection for Data Lines
- Integration of 30 Discretes Offers Cost and Space Savings
- Exceeds IEC61000-4-2 (Level 4) Specifications
- Low Profile Flip-Chip Packaging
- MSL 1
- Pb-Free Package is Available

Typical Applications

- EMI Filtering and ESD Protection for Data Lines
- Cell Phones
- Handheld Portables
- Notebook Computers
- MP3 Players

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Rating	Symbol	Value	Unit
ESD Discharge IEC61000-4-2, – Air Discharge – Contact Discharge Human Body Model	V_{PP}	30 30 16	kV
DC Power per Resistor	P_R	100	mW
DC Power per Package	P_T	600	mW
Junction Temperature	T_J	150	$^\circ\text{C}$
Operating Temperature Range	T_{op}	-40 to +85	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to +150	$^\circ\text{C}$

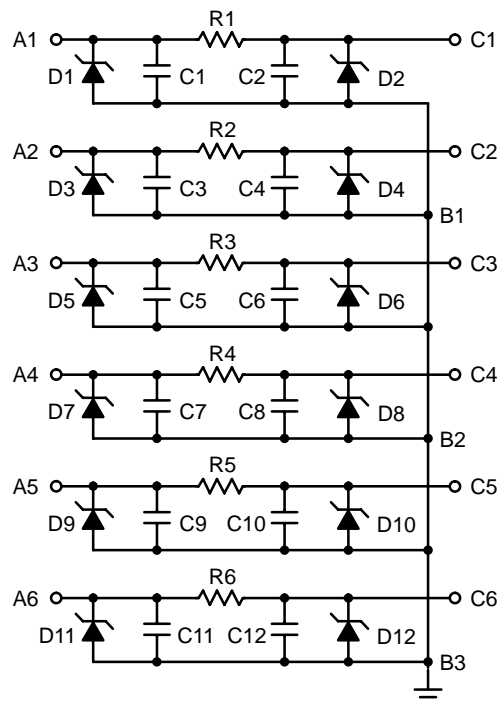
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



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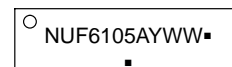
CIRCUIT DESCRIPTION



A1

**FLIP-CHIP
CASE 499D
PLASTIC**

MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NUF6105FCT1	Flip-Chip	3000/Tape & Reel
NUF6105FCT1G	Flip-Chip (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

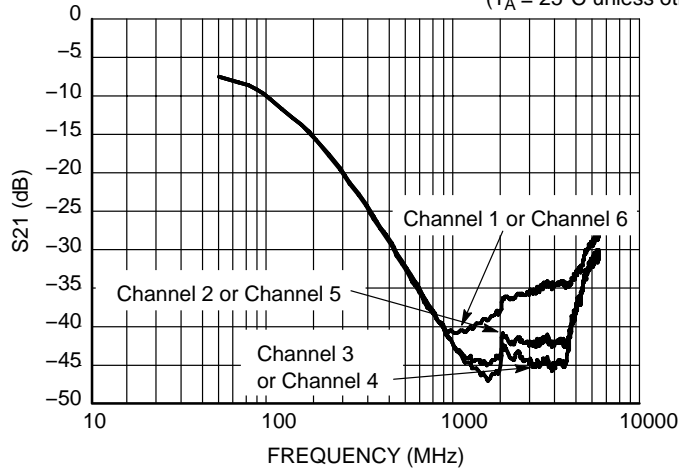
ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Characteristic	Min	Typ	Max	Unit
V_{RWM}	Maximum Reverse Working Voltage	–	–	5.0	V
V_{BR}	$I_Z = 10\text{ mA}$	6.0	7.0	8.0	V
I_R	$V_{RWM} = 3.3\text{ V per line}$	–	–	0.1	μA
$R_{I/O}$	$I_R = 20\text{ mA}$	80	100	120	Ω
C_d	$V_R = 2.5\text{ V, } f = 1\text{ MHz}$ (Note 1 and 2)	–	27	–	pF

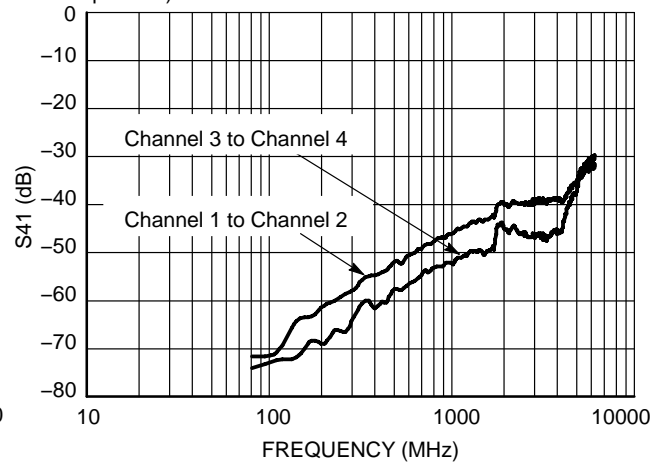
1. Measured at 25°C , $V_R = 2.5\text{ V}$, $f = 1.0\text{ MHz}$.
2. Total line capacitance is 2 times the diode capacitance (C_d).

TYPICAL PERFORMANCE CURVES

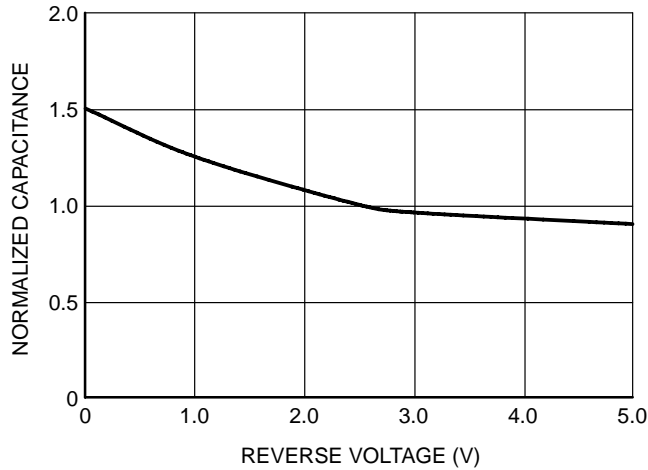
($T_A = 25^\circ\text{C}$ unless otherwise specified)



**Figure 1. Insertion Loss Curve
(S21 Measurement)**



**Figure 2. Analog Crosstalk Curve
(S41 Measurement)**



**Figure 3. Typical Capacitance vs.
Reverse Biased Voltage
(Normalized Capacitance C_d at 2.5 V)**

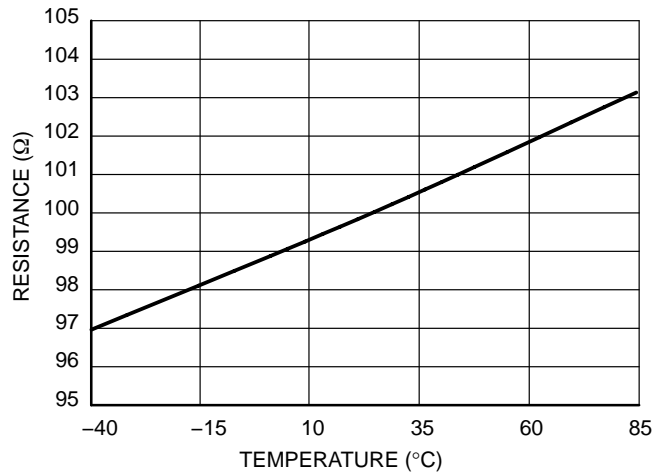


Figure 4. Resistance Over Temperature

NUF6105FC

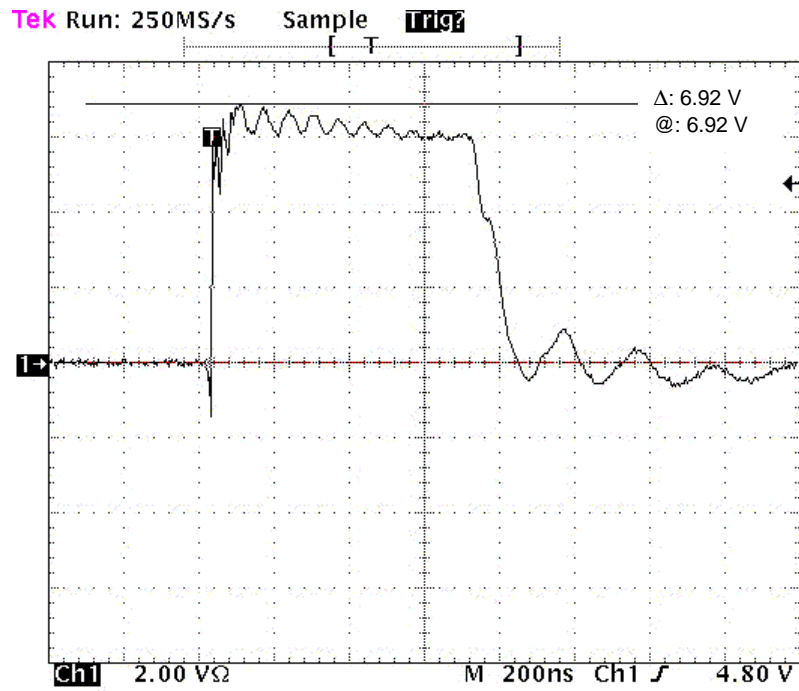


Figure 5. ESD Scope Trace Human Body Model (-8 kV)

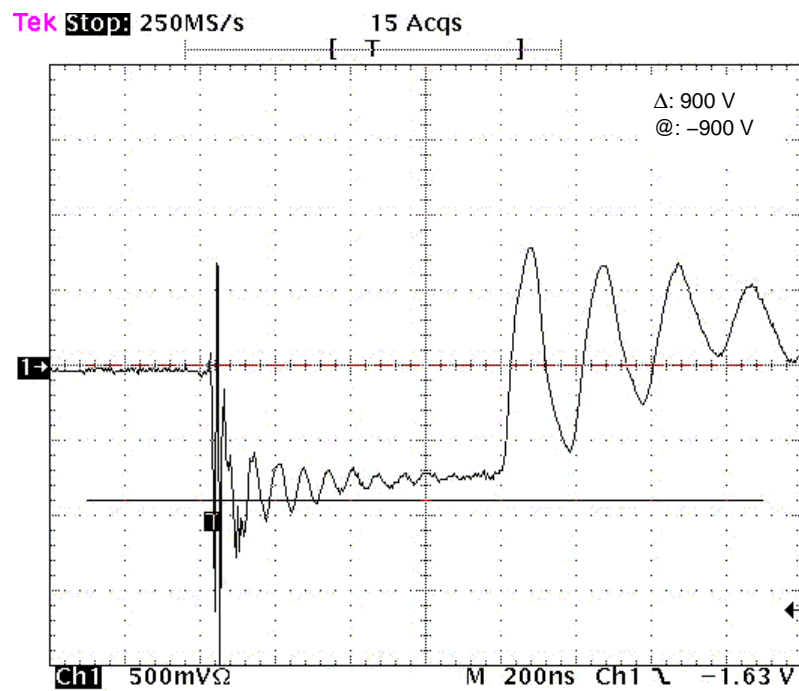


Figure 6. ESD Scope Trace Human Body Model (+8 kV)

Printed Circuit Board Recommendations

Parameter	500 μm Pitch 300 μm Solder Ball
PCB Pad Size	250 μm +25 -0
Pad Shape	Round
Pad Type	NSMD
Solder Mask Opening	350 μm \pm 25
Solder Stencil Thickness	125 μm
Stencil Aperture	250 x 250 μm sq.
Solder Flux Ratio	50/50
Solder Paste Type	No Clean Type 3 or Finer
Trace Finish	OSP Cu
Trace Width	150 μm Max

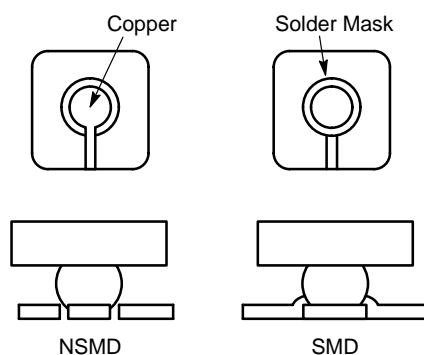


Figure 7. Solder Mask versus Non-Solder Mask Definition

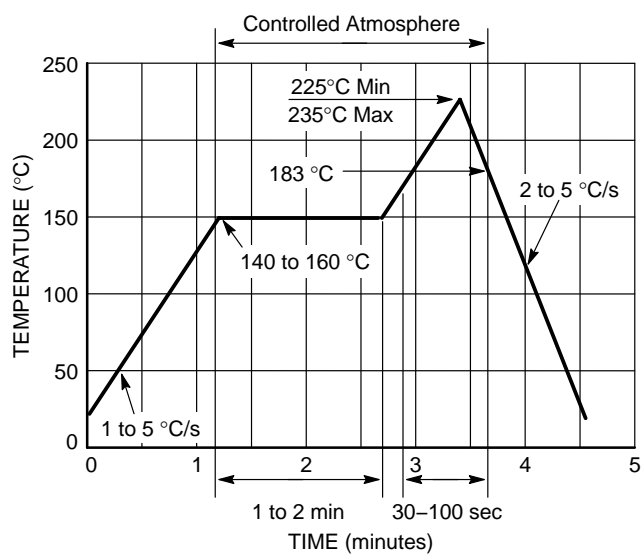


Figure 8. Solder Reflow Profile

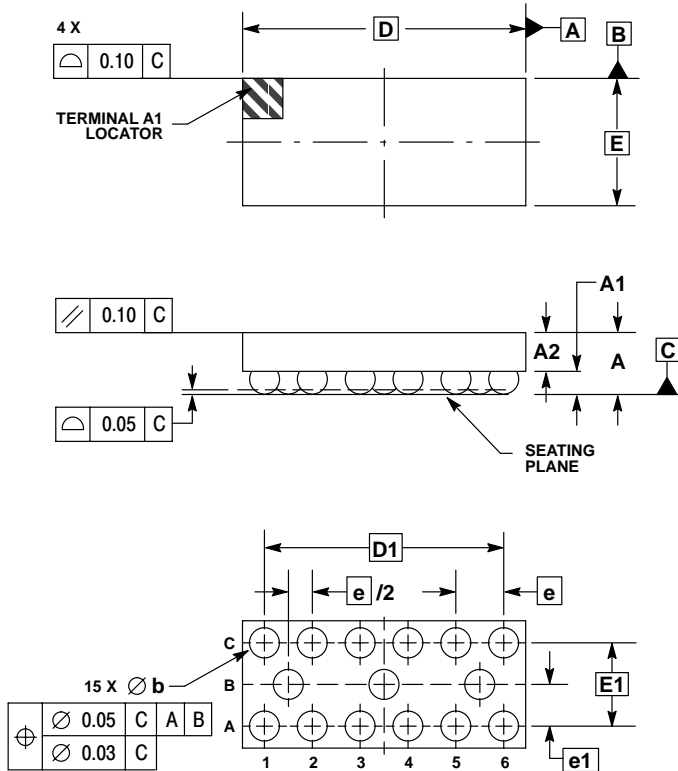
NUF6105FC

PACKAGE DIMENSIONS

15 PIN FLIPCHIP CSP

CASE 499D-01


ISSUE O



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETER.
3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

DIM	MILLIMETERS	
	MIN	MAX
A	---	0.700
A1	0.210	0.270
A2	0.380	0.430
D	2.960 BSC	
E	1.330 BSC	
b	0.290	0.340
e	0.500 BSC	
e1	0.435 BSC	
D1	2.500 BSC	
E1	0.870 BSC	

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