3.3V, 8-Channel, 2:1 Gigabit Ethernet LAN Switch with LED Switch

The NS3L500 is a 8-channel 2:1 LAN switch with 3 additional built-in SPDT switches for LED routing. This switch is ideal for Gigabit LAN applications due to its low ON-state resistance and capacitance giving the switch a typical bandwidth of 800 MHz. The switch also has excellent ON-state resistance match, low bit-to-bit skew, and low crosstalk among channels. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs.

This part can be used to replace mechanical relays in low-voltage LAN applications that interface a physical layer over CAT 5 or CAT 6 unshielded twisted pair cable through an isolation transformer. The NS3L500 is available in a 56-pin WQFN package and operates over the extended -40° C to $+85^{\circ}$ C temperature range.

Features

- V_{CC} Operating Range: +3.0 V to +3.6 V
- Low ON-State Resistance ($R_{ON} = 4 \Omega$ Typical)
- Low ON-State Capacitance (C_{ON} = 7 pF Typical)
- Flat ON–State Resistance ($R_{ON}(flat) = 0.5 \Omega$ Typical)
- Wide Bandwidth (800 MHz Typical)
- Low Crosstalk (X_{TALK} = -37 dB Typical)
- Near-Zero Propagation Delay: 250 ps
- Low Bit-to-Bit Skew (tsk(o) = 100 ps Max)
- Three SPDT Channels for LED Signal Switching
- Packaging: 56-Pin WQFN
- Pin-to-Pin Compatible with PI3L500-A, TS3L500AE and MAX4927
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

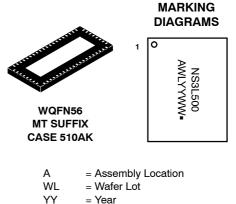
Typical Applications

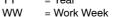
- 10/100/1000 Base-T Ethernet Signal Switching
- Notebooks and Docking Stations
- Hub and Router Signal Switching
- Differential (LVDS, LVPECL) Signal Switching



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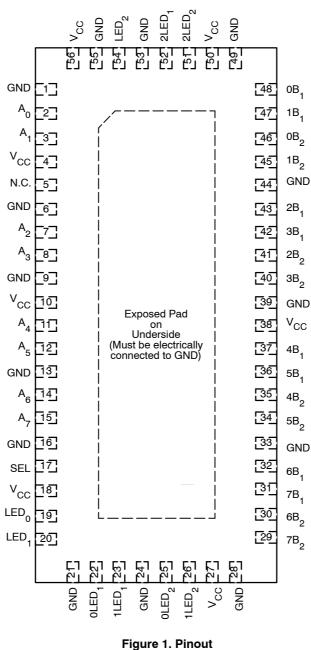


= Pb-Free Package

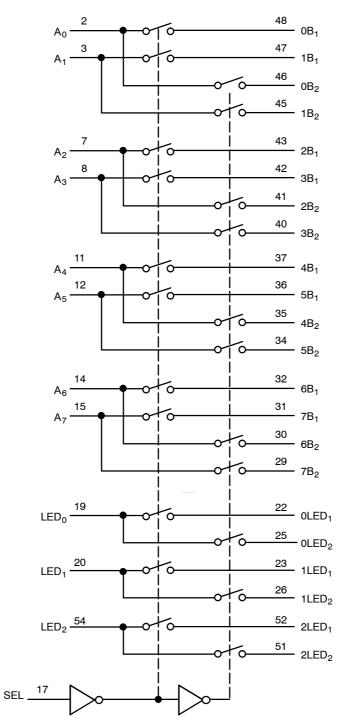
ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

1



(Top View)





PIN DESCRIPTION

Pin Name	Description
A _x	Data I/Os
хВ _у	Data I/Os
SEL	Select Input
LED _x	LED I/O Port
xLEDy	LED I/O Port

TRUTH TABLE

SEL	Function
L	A_x to xB_1 : LED _x to $xLED_1$
Н	A_x to xB_2 : LED _x to $xLED_2$

MAXIMUM RATINGS

Symbol	Pins	Parameter	Value	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	–0.5 to +5.5	V
V _{IN}	SEL	Control Input Voltage	–0.5 to +5.5	V
V _{I/O}	A _X , xB _Y , LED _X , xLED _Y	Switch I/O Voltage Range	-0.5 to V _{CC} +0.5	V
I _{CC}	V _{CC}	DC Output Current	±120	mA
I _{IK}	SEL	Control Input Clamp Current	-50	mA
I _{I/O}	A _X , xB _Y , LED _X , xLED _Y	ON-State Switch Current	± 120	mA
$R_{\theta JA}$		Thermal Resistance, Junction-to-Air	125	°C/W
Τs		Storage Temperature	-65 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

Symbol	Pins	Parameter	Value	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	+3.0 to +3.6	V
V _{IN}	SEL	Control Input Voltage	0 to +5.5	V
V _{I/O}	A _X , xB _Y , LED _X , xLED _Y	Switch I/O Voltage Range	0 to V _{CC}	V
T _A		Operating Temperature	-40 to +85	°C

Minimum and maximum values are guaranteed through test or design across the Recommended Operating Conditions, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.

DC ELECTRICAL CHARACTERISTICS (Typical: T = 25°C, V_{CC} = 3.3 V)

				-40	°C to +8	85°C	
Symbol	Pins	Parameters	Conditions	Min	Тур	Max	Unit
1000 BASE-	T ETHERNET	SWITCHING	•				
V _{IH}	SEL	Control Input HIGH Voltage		2		5.5	V
V _{IL}	SEL	Control Input LOW Voltage		-0.5		0.8	V
V _{IK}	SEL	Clamp Diode Voltage	V _{CC} = Max, I _{IN} = -18 mA		-0.7	-1.2	V
I _{IH}	SEL	Input HIGH Current	$V_{CC} = Max, V_{IN} = V_{CC}$	-1		+1	μΑ
۱ _{IL}	SEL	Input LOW Current	V _{CC} = Max, V _{IN} = GND	-1		+1	μA
I _{OFF}	SEL	Off-Leakage Current	V_{CC} = 0 V, V_{IN} = 0 V to 3.6 V			±1.5	μA
I _{CC}	V _{CC}	Quiescent Supply Current	$V_{CC} = 3.6 \text{ V}, \text{ V}_{IN} = V_{CC} \text{ or}$ GND, I _O = 0 mA		250	600	μA
I _{LA(OFF)}	A_X,xB_Y	Off-Leakage Current	$\begin{array}{l} {\sf V}_{CC} = 3.6 \; {\sf V}, \; {\sf VA}_X = 0.3 \; {\sf V}, \; 3.3 \; {\sf V}; \; {\sf VxB}_1 \\ {\sf or} \; {\sf VxB}_2 = 3.3 \; {\sf V}, \; 0.3 \; {\sf V} \end{array}$	-1		+1	μA
I _{LA_(ON)}	A_X,xB_Y	On-Leakage Current		-1		+1	μA
R _{ON}	A_X, xB_Y	On-Resistance	$V_{CC} = 3 \text{ V}, 1.5 \text{ V} \le V_{IN} \le V_{CC}, \ I_O = -40 \text{ mA}$		4	7	Ω
R _{ON(FLAT)}	A_X, xB_Y	On-Resistance Flatness	V_{CC} = 3 V, V_{IN} = 1.5 V and $V_{CC},$ I_O = –40 mA		0.5		Ω
ΔR_{ON}	A_X,xB_Y	On-Resistance Match Between Switch Pairs	$\begin{array}{l} V_{CC} = 3 \; V\!\!, \; 1.5 \; V \; \leq \; V_{IN} \; \leq \; V_{CC}, \\ I_{O} = -40 \; mA \end{array}$		0.4	1	Ω
10/100 BAS	E-T ETHERNE	T SWITCHING	•				
V _{IH}	SEL	Control Input HIGH Voltage		2		5.5	V
V _{IL}	SEL	Control Input LOW Voltage		-0.5		0.8	V
V _{IK}	SEL	Clamp Diode Voltage	V _{CC} = Max, I _{IN} = -18 mA		-0.7	-1.2	V
I _{IH}	SEL	Input HIGH Current	$V_{CC} = Max, V_{IN} = V_{CC}$	-1		+1	μA
IIL	SEL	Input LOW Current	V _{CC} = Max, V _{IN} = GND	-1		+1	μA
I _{OFF}	SEL	Off-Leakage Current	$V_{CC} = 0 \text{ V}, \text{ V}_{IN} = 0 \text{ V} \text{ to } 3.6 \text{ V}$			±1.5	μA
I _{CC}	V _{CC}	Quiescent Supply Current	V_{CC} = 3.6 V, V_{IN} = V_{CC} or GND I_O = 0 mA		250	600	μA
I _{LA(OFF)}	A_X,xB_Y	Off-Leakage Current		-1		+1	μA
I _{LA_(ON)}	A_X,xB_Y	On-Leakage Current		-1		+1	μA
R _{ON}	A_X,xB_Y	On-Resistance	V_{CC} = 3 V, 1.25 V $\leq V_{IN} \leq V_{CC}$, I _O = -10 mA to -30 mA		4	6	Ω
R _{ON(FLAT)}	A_X , xB_Y	On-Resistance Flatness	V_{CC} = 3 V, V_{IN} = 1.25 V and $V_{CC},$ I_O = –10 mA to –30 mA		0.5		Ω
ΔR_{ON}	A _X , xB _Y	On-Resistance Match Between Switch Pairs	$V_{CC} = 3 \text{ V}, 1.25 \text{ V} \le \text{V}_{IN} \le \text{V}_{CC},$ $I_{O} = -10 \text{ mA to } -30 \text{ mA}$	1	0.4	1	Ω

DC ELECTRICAL CHARACTERISTICS (Typical: T = 25° C, V_{CC} = 3.3 V)

				-40)°C to +8	5°C	
Symbol	Pins	Parameters	Conditions	Min	Тур	Max	Unit
LED SWITC	HING						
R _{ON}	LED _X , xLED _Y	On-Resistance	$\begin{array}{l} V_{CC} = 3 V\!\!\!, 1.25 V \leq V_{IN} \leq V_{CC}, \\ I_{O} = -40 mA \end{array}$		15	25	Ω
R _{ON(FLAT)}	LED _X , xLED _Y	On-Resistance Flatness	V_{CC} = 3 V, V_{IN} = 1.25 V and $V_{CC},$ I_O = –40 mA		8		Ω
ΔR _{ON}	LED _X , xLED _Y	On-Resistance Match Between Switch Pairs	$\begin{array}{l} V_{CC}$ = 3 V, 1.25 V \leq V_{IN} \leq $V_{CC},$ I_{O} = -40 mA		1	2	Ω

AC ELECTRICAL CHARACTERISTICS (Typicals: T = 25°C, V_{CC} = 3.3 V)*

				-40	°C to +8	5°C	
Symbol	Pins	Parameters	Conditions	Min	Тур	Max	Unit
SWITCHING	G CHARACTERI	STICS					
t _{PLH} , t _{PHL}	A _x , xB _y	Propagation Delay	V _{CC} = 3.0 V to 3.6 V (Figure 3)		0.25		ns
t _{ON}	SEL, xLED _y	Line Enable Time – SEL to xLED _Y	Output: Closed to Open V_{CC} = 3.0 V to 3.6 V (Figure 4)	0.5		15	ns
	SEL, xB _y	Lines Enable Time – SEL to xB _y		0.5		3	μs
t _{OFF}	SEL, xLED _y	Line Enable Time – SEL to xLED _Y	Output: Open to Closed V_{CC} = 3.0 V to 3.6 V (Figure 4)	0.5		9	ns
	SEL, xB _y	Lines Enable Time – SEL to xB _y		0.5		35	ns
t _{SK(O)}	A _x , xB _y	Output Skew between center port to any other port	V _{CC} = 3.0 V to 3.6 V (Calculated, Figure 3)		50	100	ps
t _{SK(P)}	A _x , xB _y	Skew between opposite transition of the same output (t _{PHL} – t _{PLH})	V _{CC} = 3.0 V to 3.6 V (Calculated, Figure 3)		50	100	ps

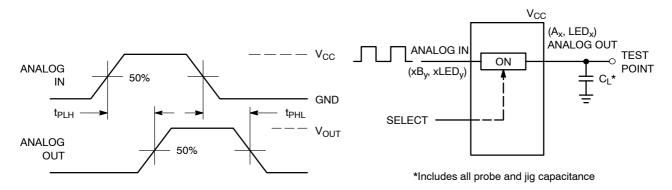
DYNAMIC ELECTRICAL CHARACTERISTICS

BW	xB _{y,} xLED _y	-3 dB Bandwidth	$R_L = 100 \Omega$ (Figure 5)	800	MHz
O _{IRR}	A_X , LED _X	Off - Isolation	R_L = 100 Ω , f = 250 MHz (Figure 6)	-37	dB
X _{TALK}	$\begin{array}{c} A_X \text{ to } xB_Y \\ A_{(X+2)} \text{ to} \\ (X+2)B_Y \end{array}$	Crosstalk	R_L = 100 $\Omega,$ f = 250 MHz (Figure 7)	-37	dB

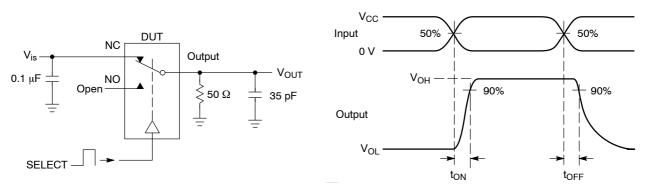
CAPACITANCE

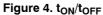
C _{IN}	SEL	Control Pin Input Capacitance	V _{IN} = 0 V, f = 1 MHz	2	3	pF
C _{ON}	A _X , xB _y	ON Capacitance	V _{IN} = 0 V, f = 1 MHz, Outputs Open, Switch ON	7	10	pF
C _{OFF}	хВу	B Port Switch Capacitance	V_{IN} = 0 V, f = 1 MHz, Outputs Open, Switch OFF	5	6	pF

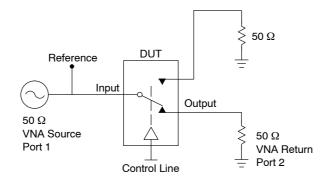
*Guaranteed by design and/or characterization.













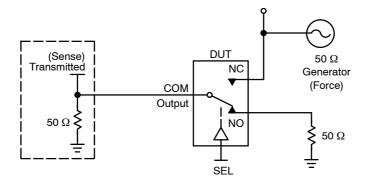
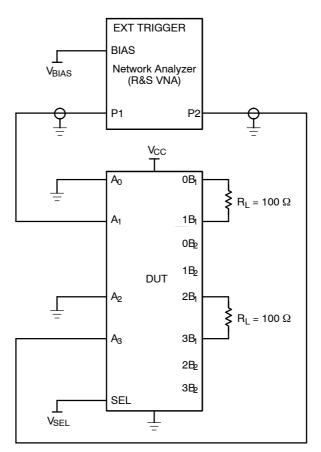


Figure 6. Off-Isolation



1. C_L includes probe and jig capacitance. 2. A 50 Ω termination resistor is needed to match the loading of the network analyzer.

Figure 7. Test Circuit for Crosstalk (X_{TALK})

Crosstalk is measured at the output of the nonadjacent ON channel. For example, when $V_{SEL} = 0$ and A_0 is the input, the output is measured at 1B₁. All unused analog input (A) ports are connected to GND, and output (B) ports are connected to GND through 50 Ω pulldown resistors.

APPLICATION INFORMATION

Logic Inputs

The logic control inputs can be driven up to +3.6 V regardless of the supply voltage. For example, given a +3.3 V supply, the output enables or select pins may be driven low to 0 V and high to 3.6 V> Driving IN Rail-to-Rail® minimizes power consumption.

Power-Supply Sequencing

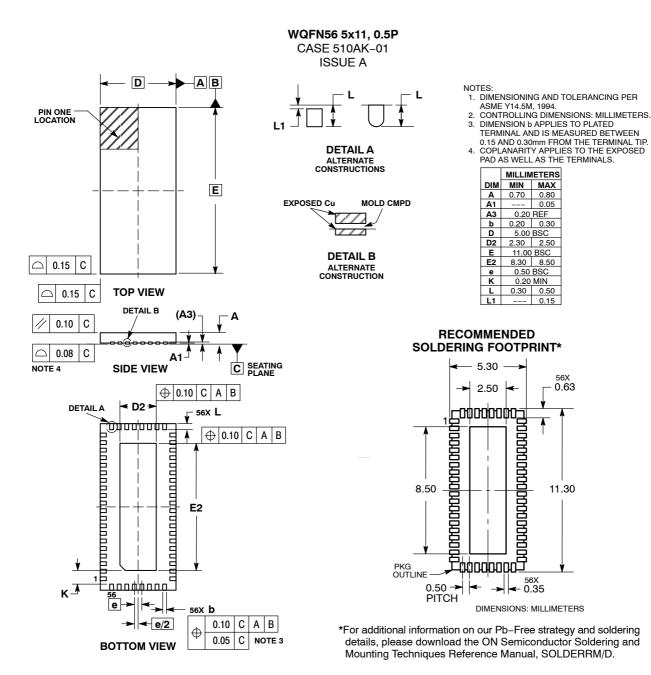
Proper power–supply sequencing is advised for all CMOS devices. It is recommended to always apply V_{CC} before applying signals to the input/output or control pins.

ORDERING INFORMATION

Device	Package	Shipping [†]
NS3L500MTTWG	WQFN56 (Pb-free)	2000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS



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