

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE80H11H uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

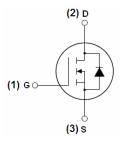
- $V_{DS} = 80V, I_D = 105A$ $R_{DS(ON)} < 6.5m\Omega @ V_{GS} = 10V (Typ:5.8m\Omega)$
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% AVds TESTED!



Schematic diagram



Marking and pin assignment



TO-220H-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE80H11H	NCE80H11H	TO-220H-3L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	80	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	105	Α
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	74	Α
Pulsed Drain Current	I _{DM}	420	Α
Maximum Power Dissipation	P _D	200	W
Derating factor		1.33	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	800	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$



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NCE80H11H

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2)	$R_{ heta JC}$	0.75	°C/W	
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA 8		86	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =80V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						•
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A	-	5.8	6.5	mΩ
Forward Transconductance	g FS	V _{DS} =25V,I _D =40A	80	-	-	S
Dynamic Characteristics (Note4)						•
Input Capacitance	C _{lss}	\/ O5\/\/ O\/	-	4900	-	PF
Output Capacitance	C _{oss}	V_{DS} =25V, V_{GS} =0V,	-	410	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	315	-	PF
Switching Characteristics (Note 4)						•
Turn-on Delay Time	t _{d(on)}		-	20	-	nS
Turn-on Rise Time	t _r	VDD=40V,ID=2A,RL=15Ω,	-	19	-	nS
Turn-Off Delay Time	t _{d(off)}	RG=2.5Ω,VGS=10V	-	70	-	nS
Turn-Off Fall Time	t _f		-	30	-	nS
Total Gate Charge	Qg		-	125	-	nC
Gate-Source Charge	Q _{gs}	ID=30A,VDD=30V,VGS=10V	-	24	-	nC
Gate-Drain Charge	Q_{gd}		-	49	-	nC
Drain-Source Diode Characteristics						•
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =40A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	105	Α
Reverse Recovery Time	t _{rr}	Tj=25℃,IF=75A,	-	37		nS
Reverse Recovery Charge	Qrr	di/dt=100A/uS (Note3)	-	58		nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

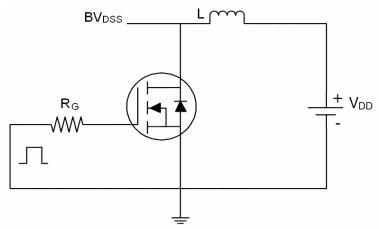
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,V_{DD}=40V,V_G=10V,L=0.5mH,Rg=25 Ω

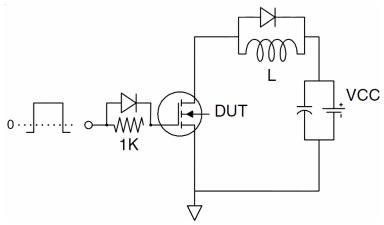


Test circuit

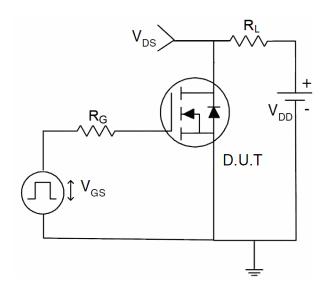
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

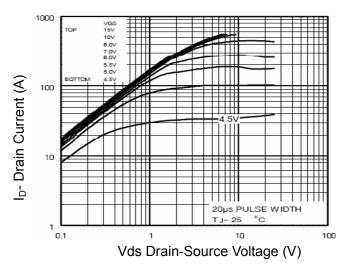


Figure 1 Output Characteristics

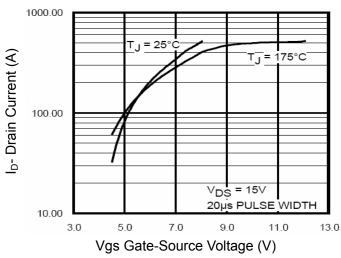


Figure 2 Transfer Characteristics

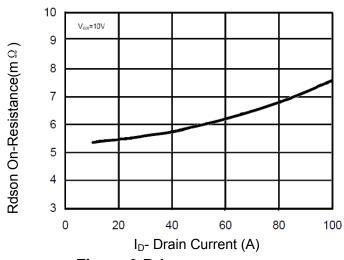


Figure 3 Rdson- Drain Current

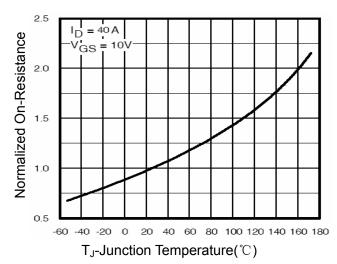


Figure 4 Rdson-JunctionTemperature

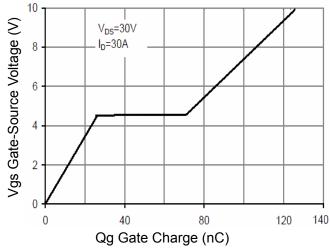


Figure 5 Gate Charge

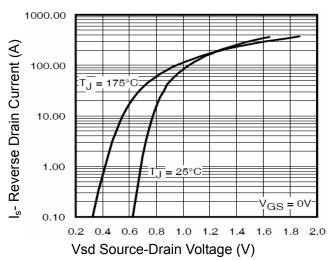
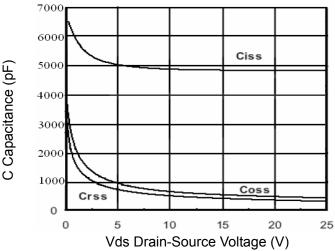
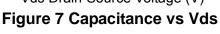


Figure 6 Source- Drain Diode Forward







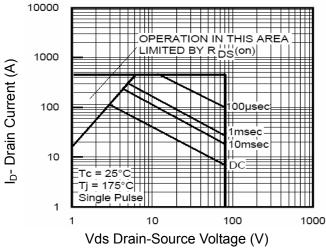


Figure 8 Safe Operation Area

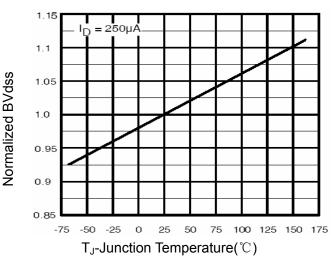


Figure 9 BV_{DSS} vs Junction Temperature

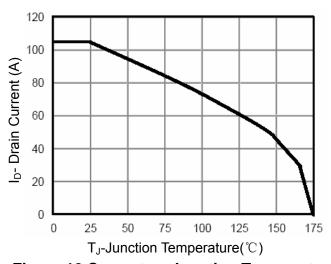


Figure 10 Current vs Junction Temperature

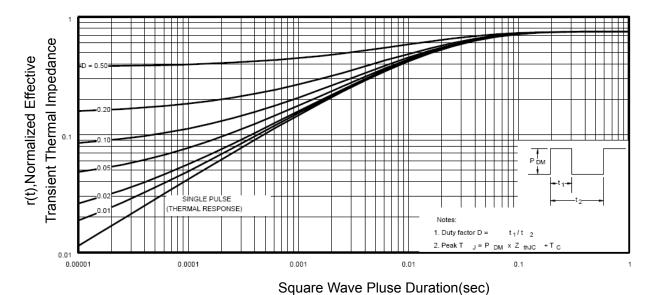
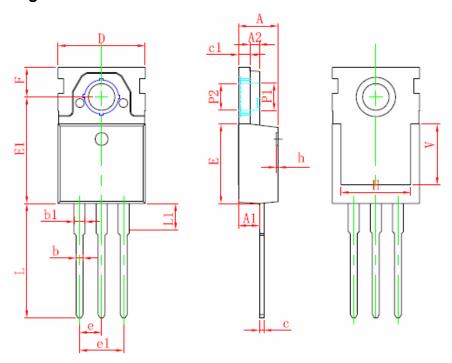


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-220H-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Syllibol	Min.	Max.	Min.	Max.	
Α	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
A2	1.000	1.200	0.039	0.047	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.820	10.220	0.387	0.402	
E	8.950	9.350	0.352	0.368	
E1	12.000	12.500	0.472	0.492	
е	2.540	TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	3.250	3.550	0.128	0.140	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.930	13.330	0.509	0.525	
L1	3.450	3.850	0.136	0.152	
P1	3.15	TYP.	0.124 TYP.		
P2	3.05	TYP.	0.120 TYP.		
V	6.900	REF.	0.272 REF.		

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