

## NCE P-Channel Enhancement Mode Power MOSFET

### Description

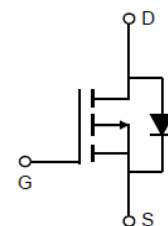
The NCE20P85G uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### General Features

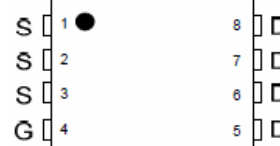
- $V_{DS} = -20V, I_D = -85A$   
 $R_{DS(ON)} < 2.5m\Omega @ V_{GS} = -4.5V$   
 $R_{DS(ON)} < 4m\Omega @ V_{GS} = -2.5V$
- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high  $E_{AS}$
- Excellent package for good heat dissipation

### Application

- Load switch
- Battery protection



Schematic diagram



Pin Assignment



Top View



Bottom View

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE20P85G	NCE20P85G	DFN 5x6 -8L	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Drain Current-Continuous	$I_D$	-85	A
Drain Current-Continuous ( $T_C = 100^\circ C$ )	$I_D (100^\circ C)$	-49.5	A
Pulsed Drain Current	$I_{DM}$	-340	A
Maximum Power Dissipation	$P_D$	135	W
Derating factor		1.08	W/ $^\circ C$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	0.93	$^\circ C/W$
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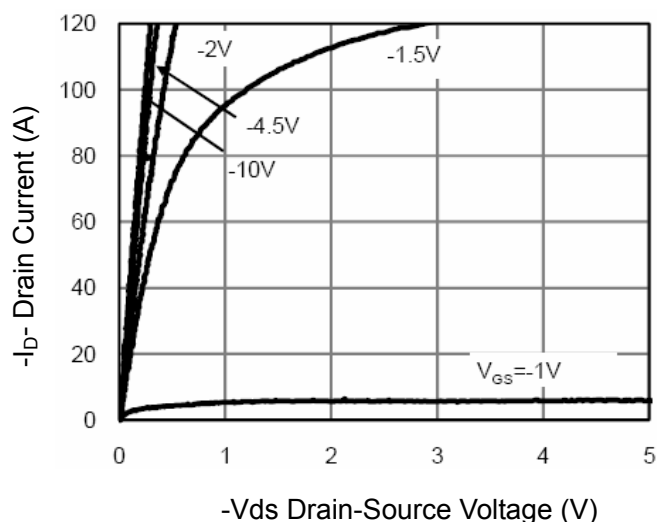
# Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics <sup>(Note 3)</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.4	-0.6	-1.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-20A	-	2.0	2.5	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-20A	-	2.7	4	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-20A	100	-	-	S
Dynamic Characteristics <sup>(Note4)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, F=1.0MHz	-	18567.5	-	PF
Output Capacitance	C <sub>oss</sub>		-	1662	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	1432	-	PF
Switching Characteristics <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-10V, R <sub>GEN</sub> =1Ω V <sub>GS</sub> =-4.5V, R <sub>L</sub> =0.5Ω	-	16	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	70	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	460	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	240	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V	-	482.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	28.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	75.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A	-	-	-1.2	V
Diode Forward Current <sup>(Note 2)</sup>	I <sub>S</sub>		-	-	-85	A
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = -10A	-	157	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs <sup>(Note3)</sup>	-	246	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

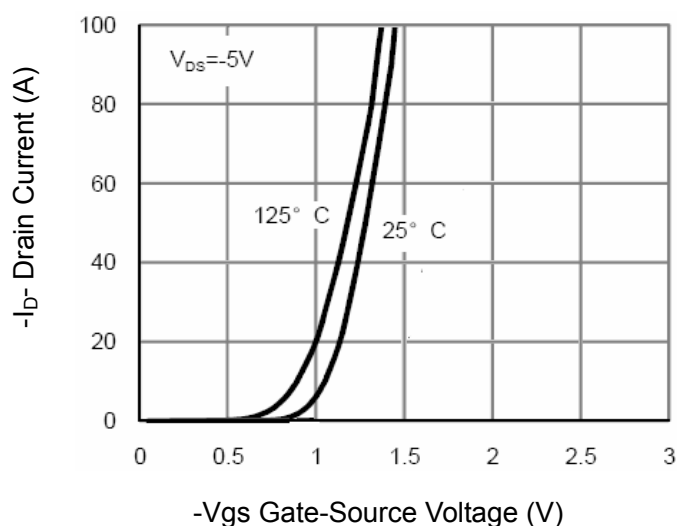
## Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

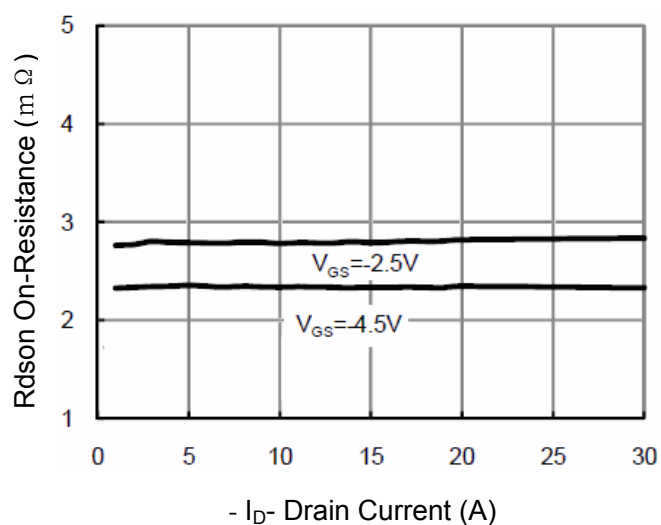
## Typical Electrical and Thermal Characteristics (Curves)



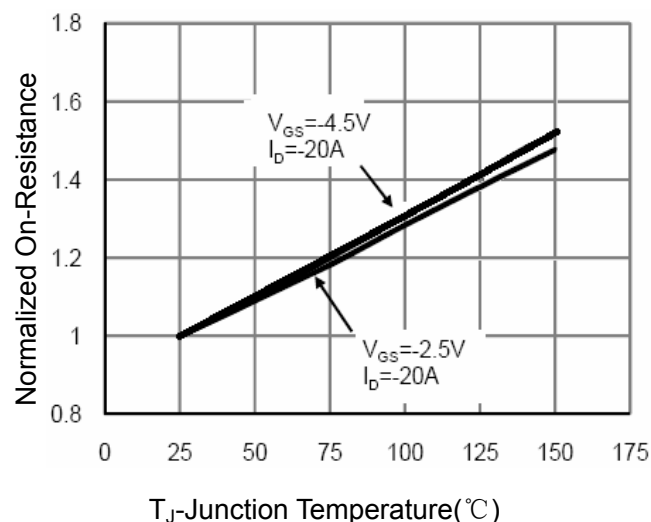
**Figure 1 Output Characteristics**



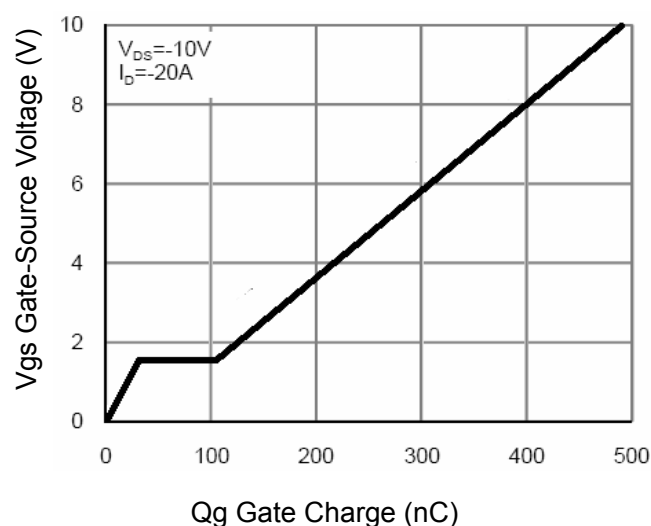
**Figure 2 Transfer Characteristics**



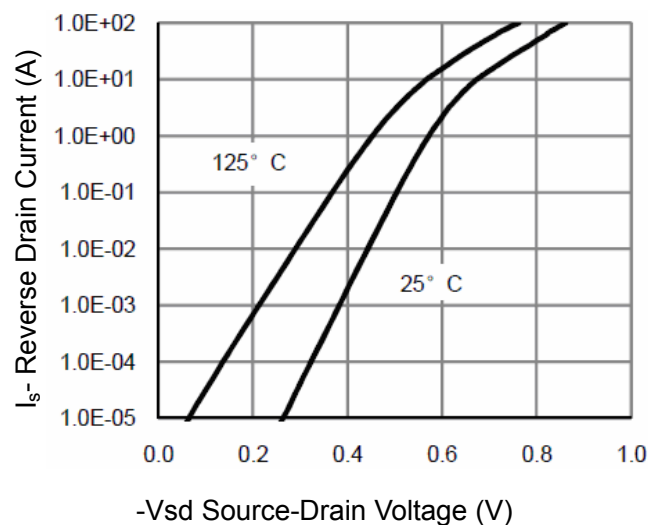
**Figure 3 Rdson- Drain Current**



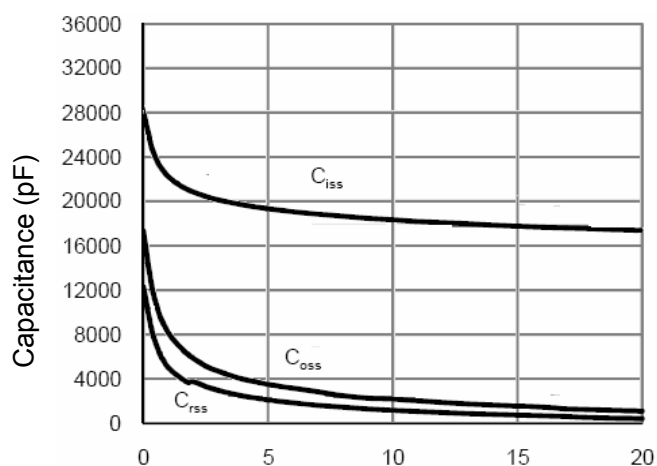
**Figure 4 Rdson-Junction Temperature**



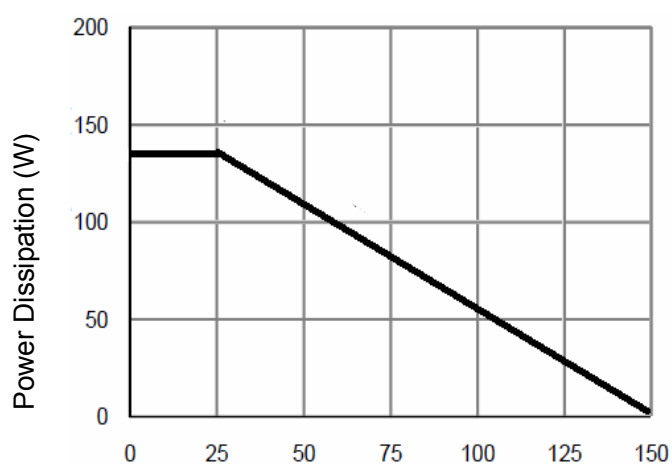
**Figure 5 Gate Charge**



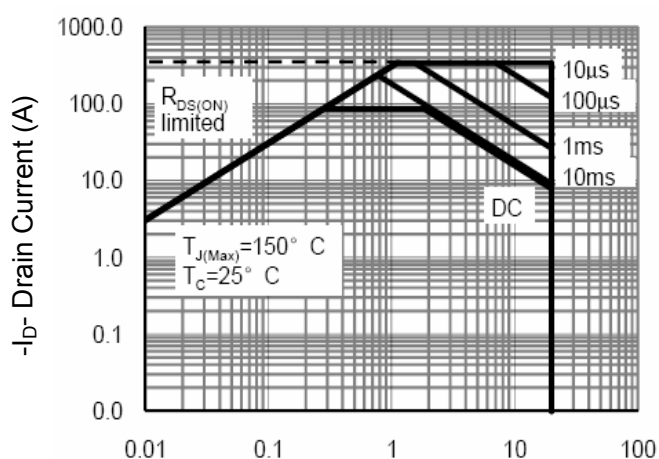
**Figure 6 Source- Drain Diode Forward**



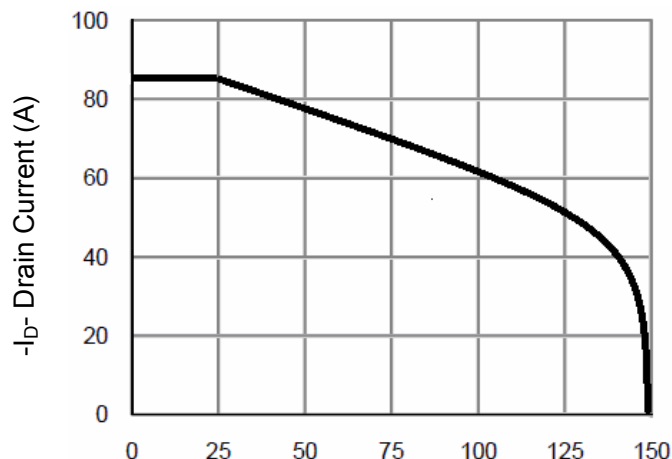
-Vds Drain-Source Voltage (V)  
**Figure 7 Capacitance vs Vds**



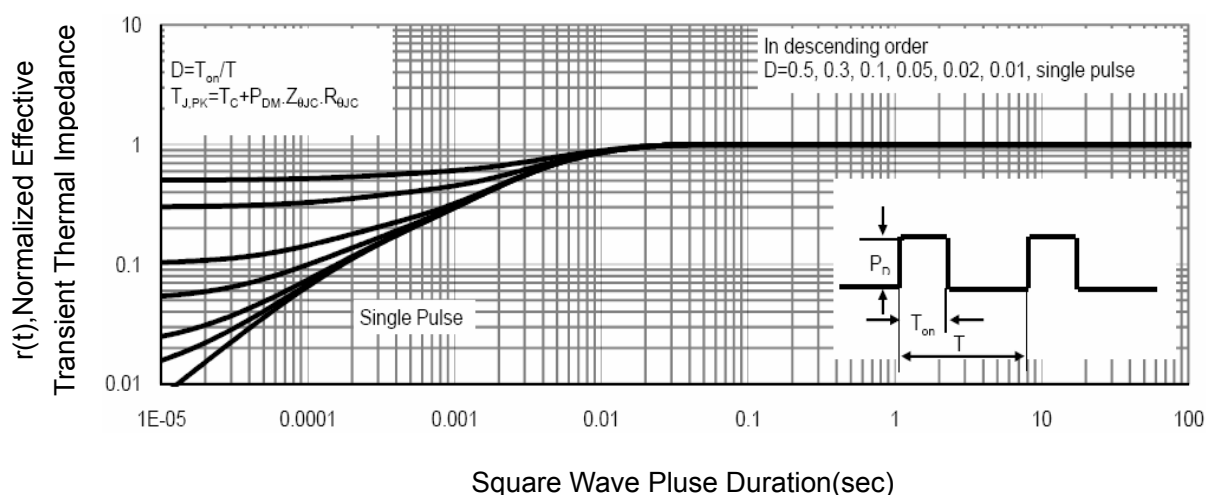
TJ-Junction Temperature(°C)  
**Figure 9 Power De-rating**



-Vds Drain-Source Voltage (V)  
**Figure 8 Safe Operation Area**

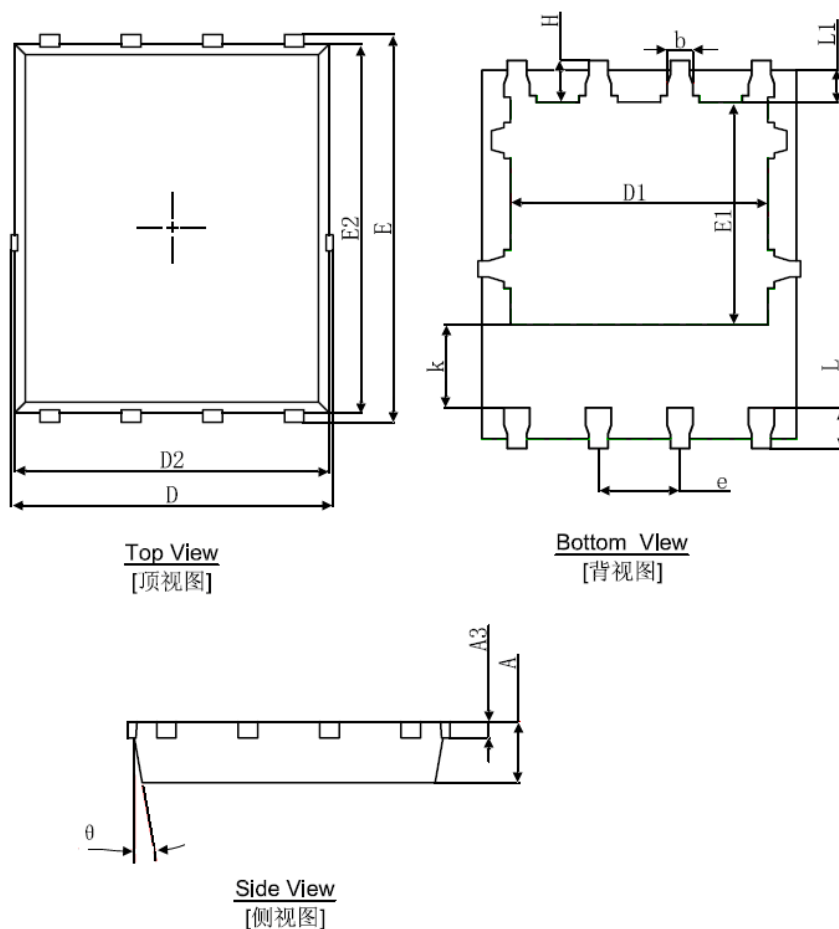


TJ-Junction Temperature(°C)  
**Figure 10 -Current De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## DFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
K	1.190	1.390	0.047	0.055
b	0.035	0.450	0.014	0.018
e	1.270(TYP.)		0.050(TYP.)	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	8°	12°	8°	12°

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