



# NCE N-Channel Enhancement Mode Power MOSFET

### Description

The NCE1540KA uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

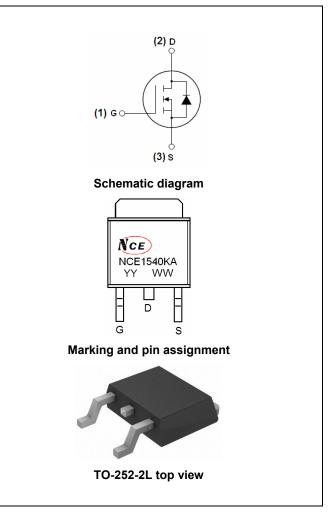
- $V_{DS} = 150V, I_D = 40A$  $R_{DS(ON)} < 45m\Omega @ V_{GS} = 10V$  (Typ:35m $\Omega$ )
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

### Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

**100% ΔVds TESTED!** 



#### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE1540K	NCE1540K	TO-252-2L	-	-	-

#### Absolute Maximum Ratings (T<sub>c</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	150	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	Ι <sub>D</sub>	40	А
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	29	A
Pulsed Drain Current	I <sub>DM</sub>	164	A
Maximum Power Dissipation	PD	140	W
Derating factor		0.93	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	310	mJ
Operating Junction and Storage Temperature Range	$T_J,T_STG$	-55 To 175	°C



NCE1540KA

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case(Note 2)	R <sub>θJC</sub>	1.07	°C/W
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#### Electrical Characteristics (T<sub>c</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	150	170	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =150V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	I <sub>GSS</sub> V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V		-	±100	nA
On Characteristics (Note 3)	·	·	•	•		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	0.8	1.05	1.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =18A	-	35	45	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =15V,I <sub>D</sub> =18A	38	-	-	S
Dynamic Characteristics (Note4)	·	·	•	•		
Input Capacitance	C <sub>lss</sub>		-	4200	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}=25V, V_{GS}=0V,$	-	203	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	96	-	PF
Switching Characteristics (Note 4)	·	·	•	•		
Turn-on Delay Time	t <sub>d(on)</sub>		-	17.8	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =30V,I <sub>D</sub> =2A,R <sub>L</sub> =15 $\Omega$	-	11.8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> =10V,R <sub>G</sub> =2.5Ω	-	56	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	14.6	-	nS
Total Gate Charge	Qg	N/ 201/1 201		105	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =30V,I <sub>D</sub> =30A,		21	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V		31.5	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =18A	-	0.82	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	40	A
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 18A	-	70	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs(Note3) -		230	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				y LS+LD)

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition:Tj=25 $^{\circ}$ C,V<sub>DD</sub>=50V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

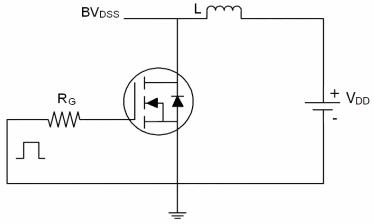


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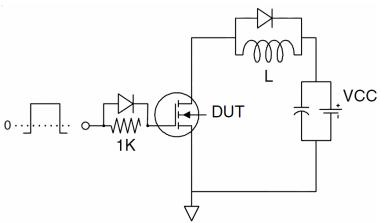




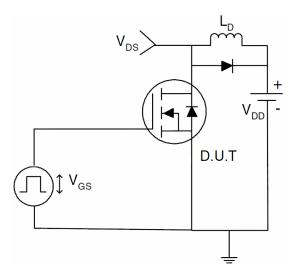
## Test Circuit 1) E<sub>AS</sub> test Circuit



### 2) Gate charge test Circuit



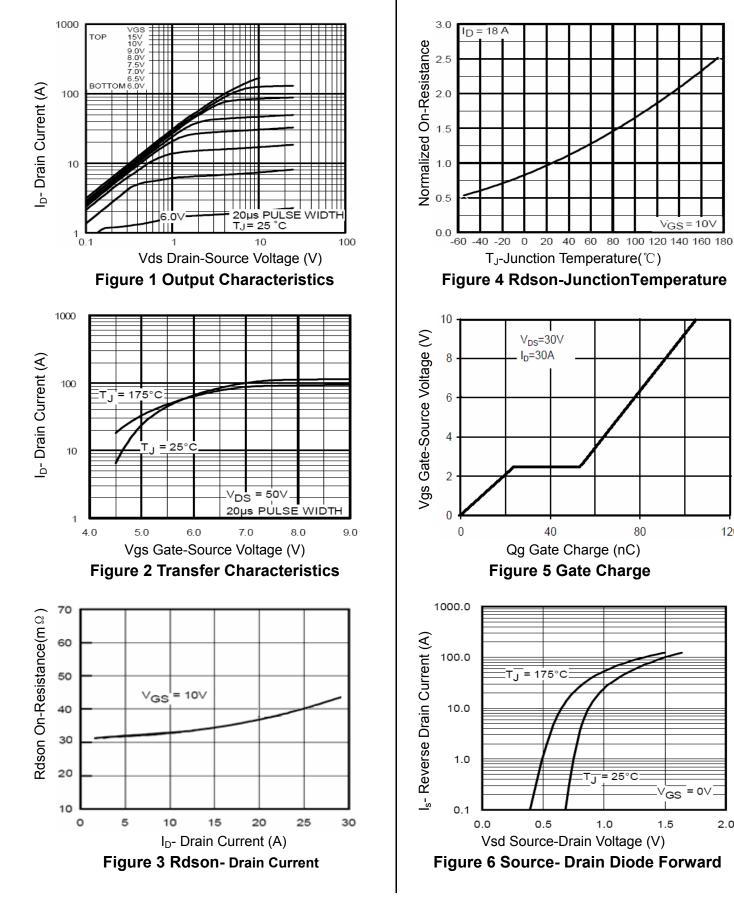
3) Switch Time Test Circuit:







## **Typical Electrical and Thermal Characteristics (Curves)**

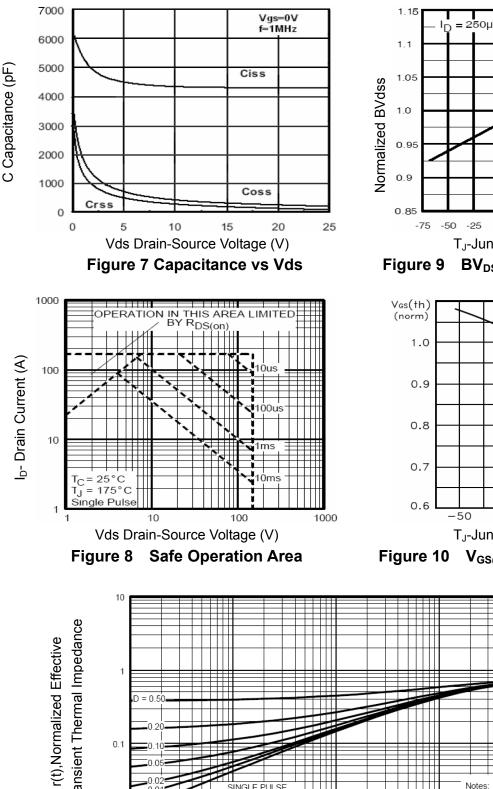


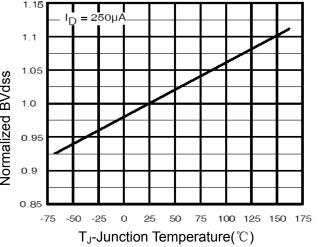
2.0

120

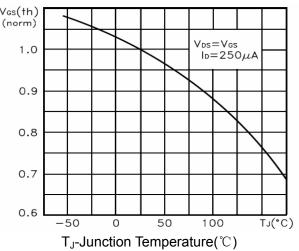




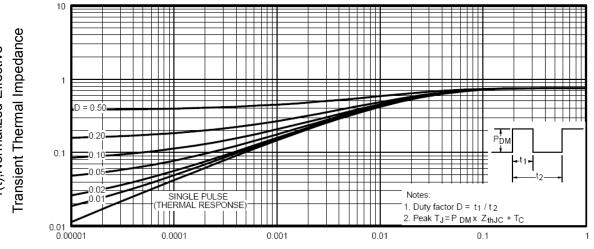


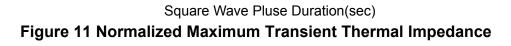


**BV**<sub>DSS</sub> vs Junction Temperature



V<sub>GS(th)</sub> vs Junction Temperature



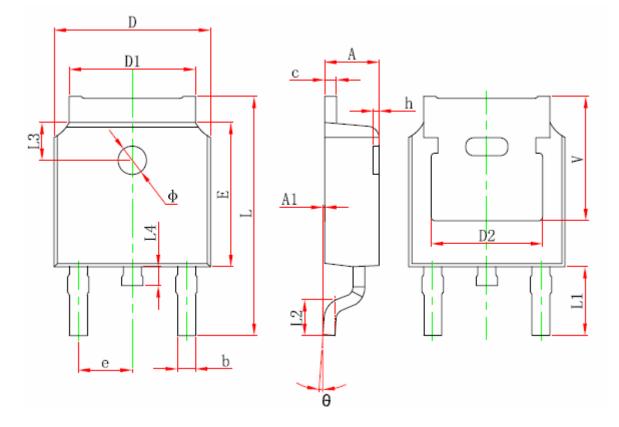




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## TO-252-2L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190 REF.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	REF.	0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600	REF.	0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	REF.	0.211 REF.		





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