To :

Specification of FUJITSU TFT-LCD module

NA19026-C081A

Approval
Date :
By :

This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, FDTC shall not be liable against the customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

Specification No. : Tech Bes LCD-00225

Issue Date : Oct. 15, 2003

Issued by : F. Yamada

F. Yamada
Director
Design Dept.
LCD Products Div.

FUJITSU DISPLAY TECHNOLOGIES CORPORATION

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02	20031015	SEKIDO	TAKAHASHI	YAMADA	add P19
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1. APPLICATIONS

This specification is applied to the 17.0 in. XGA-WIDE supported TFT-LCD module.

2. PRODUCT NAME AND MODEL NAME

2-1. Product Name : LCD Module

2-2. Model Name : NA19026-C081A

3. OVERVIEW

This LCD module has a TFT active matrix type liquid crystal panel 1280×768 pixels, and diagonal size of 43cm (17.0-inch). This module supports 1280×768 XGA-WIDE mode (Non-interlace).

This LCD has a XGA-WIDE-capable 1ch-LVDS interface and can display 16 million colors.

By timing control signal, images are displayed on the screen at given value under specified timing.

Control signals to this LCD module are ENAB (Data enable signal), DCLK signal and RGB data signal. Data signal is transmitted with single port. (8 bit for each R, G, B)

The power supply of this LCD module is +5V DC.

4. CONFIGURATION

This LCD module consists of a color TFT-LCD panel that mounted with TFT driver ICs, a cold-cathode fluorescent tube back-light, a LVDS interface PCB, a structure and a bezel. And these components are integrated in a LCD module.

Cold-cathode fluorescent tube and lamp holder are replaceable.

Fig.4-1 shows a block diagram of this LCD module.

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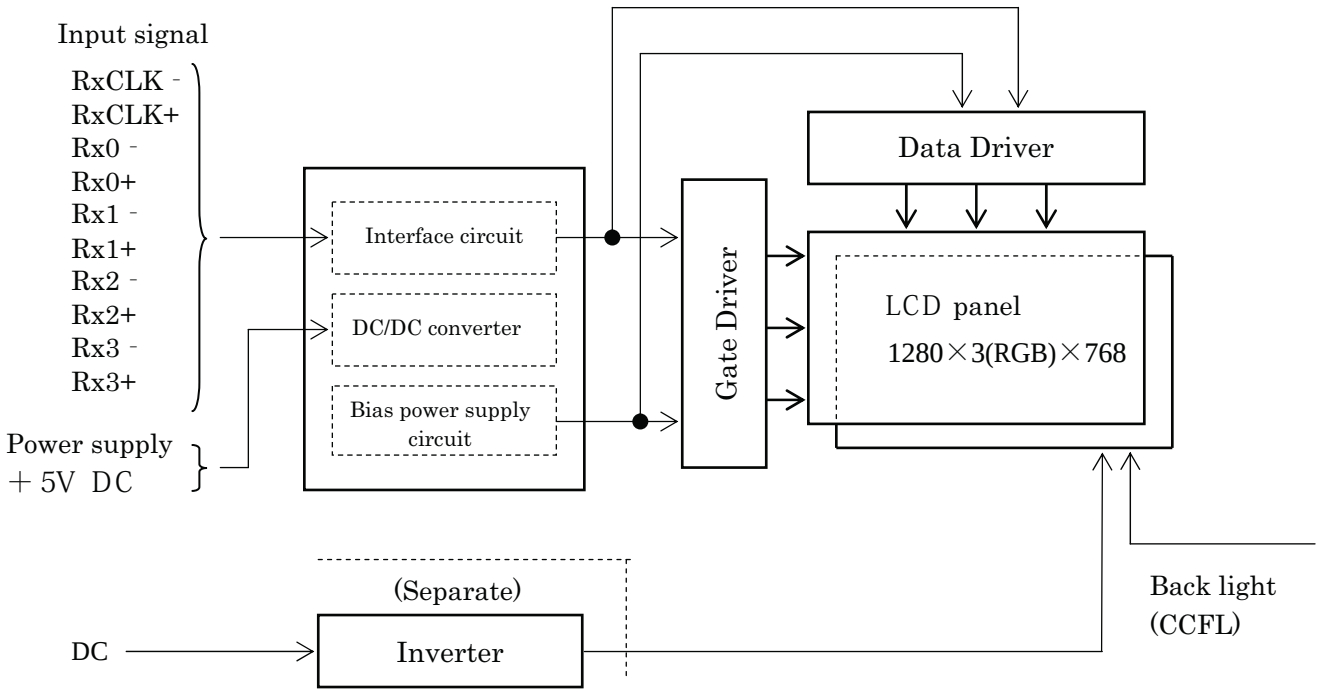

Figure 4-1. Block Diagram

5. MECHANICAL SPECIFICATIONS

Table 5-1 shows the mechanical specifications of this LCD module.

Table 5-1. Mechanical Specifications

Item	Specifications	Unit	Remark
Dimensions	413.8×263.5×18.4(TYP.)	mm	Edge type backlight is used. (ϕ2.6 CCFL×4)
Display Resolution	(1280×3) ×768	—	Outward Appearance is shown at page 32 and 33.
Display Dot Area	369.6×221.76	mm	
Dot Pitch	(0.09625×3) ×0.28875	mm	
Aspect Ratio	1 : 1	—	
Weight	2,000 max	g	
FG-SG	Short circuit	—	

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6. ABSOLUTE MAXIMUM RATINGS

Table 6-1 shows the absolute maximum rating of this LCD module.

Table 6-1. Absolute Maximum Ratings

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{CC}	−0.3	—	6.0	V
Input Voltage	V_{IN}	−0.3	—	Vcc+0.3	V

7. RECOMMENDED OPERATING CONDITIONS

Table 7-1 shows the recommended operating conditions of this LCD module.

Table 7-1. Recommended Operating Conditions

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{CC}	4.75	5.0	5.25	V
Ripple Voltage (Vcc)	V_{RP}	—	—	100	mVp-p

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8. ELECTRICAL SPECIFICATIONS

Table 8-1 shows the electrical specifications of this LCD module.

Table 8-1. Electrical Specifications

Item		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remark
Supply Current		I_{CC}	V_{CC}=+5.0±0.25V Vss=0V DCLK=32.498MHz	—	680	900	mA	*1
"H" Level Logic Input Voltage		V_{IH}		0.7×Vcc	—	V_{CC}+0.3	V	*2
"L" Level Logic Input Voltage		V_{IL}		GND	—	0.3×Vcc	V	*2
BACK LIGHT *3	CCFL Turn on Voltage	V_S	f_L=50kHz, Ta=25°C	—	1230	1600	Vrms	
			f_L=50kHz, Ta=0°C	—	—	1600		
	Lighting Voltage	V_L	f_L=50kHz I_L=10.5mA	590	630	670	Vrms	*4
	Lighting Frequency	f_L	V_L=630Vrms	40	50	60	kHz	
	Tube Current	I_L	f_L=50kHz V_L=630Vrms	9.5	10.5	11.0	mArms	*4

(*1) Typical current value is measured when gray scale (vertical 256 levels) is displayed at Vcc=5.0V.
Maximum current value is measured when stripes with respect to each RGB dot are displayed at Vcc=5.0V.
Without rush current.

(*2) Timing control circuit input voltage

(*3) Backlight specifications are valid when using a suitable inverter such as the "FLCV-15" of FDTC.

(*4) Tube current (I_L) shows the value of the current and voltage that is consumed at one lamp. (4 tubes/unit)
This LCD module has 4 lamps. Each 2 lamps are placed at upper and lower side of the display.
2 lamps are connected in parallel. Each low voltage terminals (GND side) are bound into 1 line cable.
(See 11-1. Pin configuration for backlight)

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Note 1) Measurement Circuit

Based on Fig.8-1.

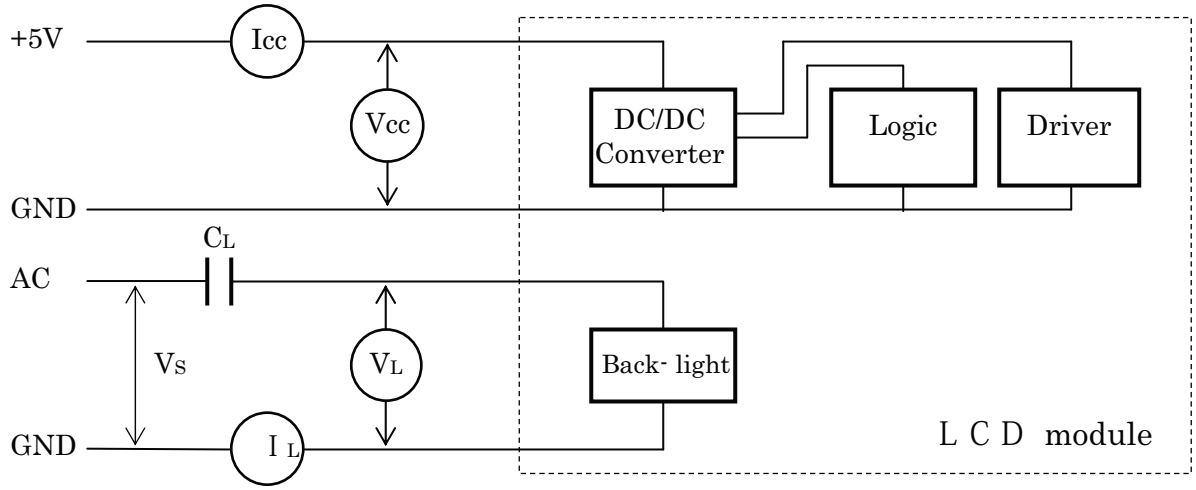


Fig.8-1. Measurement Circuit

Note 2) Equivalent Circuit

Based on Fig.8-2 (a), (b).

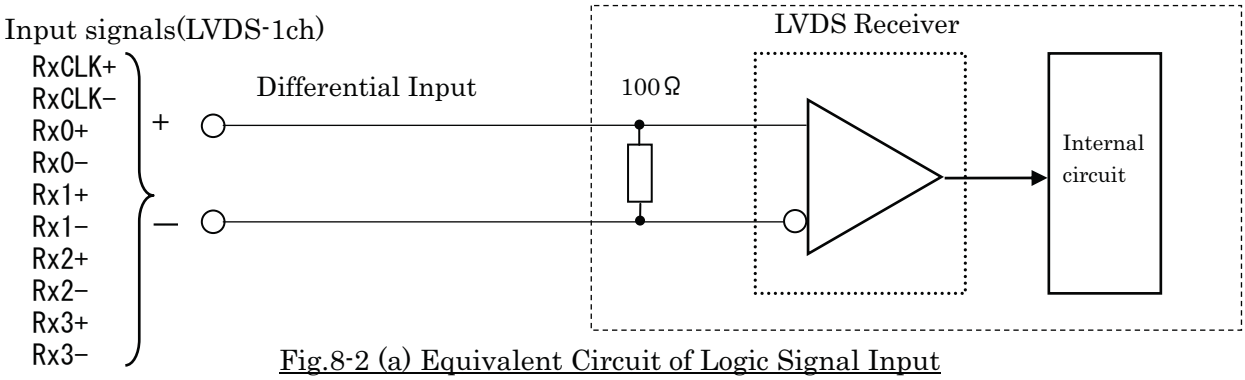
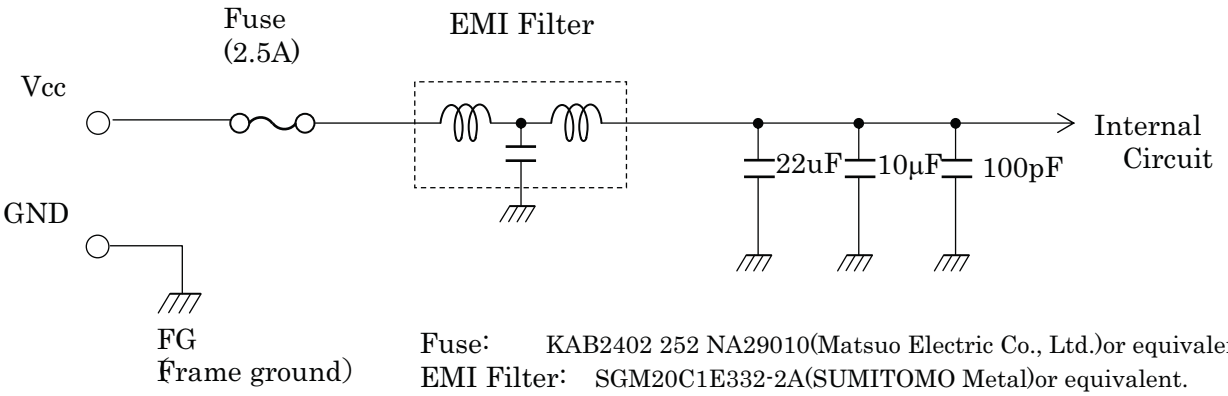


Fig.8-2 (a) Equivalent Circuit of Logic Signal Input



Fuse: KAB2402 252 NA29010(Matsuo Electric Co., Ltd.)or equivalent.
EMI Filter: SGM20C1E332-2A(SUMITOMO Metal)or equivalent.

Fig.8-2 (b) Equivalent Circuit of Power Supply

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9. OPTICAL SPECIFICATIONS

Table 9-1 shows the optical specifications of this LCD module.

Table 9-1. Optical Specifications — Ta=25°C

Item		Symbol	Condition		Specifications MIN.	Specifications TYP.	Specifications MAX	Unit	Remark	Remark Note
Visual Angle	Horizontal	$\theta_{L,R}$	CR≥10	$\theta_{U,D}$=0°	85	—	—	deg		(1)(2)(3)(5)(6)
	Vertical	$\theta_{U,D}$		$\theta_{L,R}$=0°	85	—	—	deg		
Contrast Ratio		CR	$\theta_{L,R,U,D}$=0°		350	600	—	—	White/Black	(1)(2)(3)(5)
Response Time(ON) (B→W)		t_{on}	$\theta_{L,R,U,D}$=0°	Ta=25°C	—	15	30	ms		(1)(4)(5)
				Ta=0°C	—	50	100	ms		
Response Time(OFF) (W→B)		t_{off}	$\theta_{L,R,U,D}$=0°	Ta=25°C	—	10	25	ms		
				Ta=0°C	—	50	100	ms		
Brightness		I	$\theta_{L,R,U,D}$=0° V_{CC}=5V, I_L=10.5mA (at maximum brightness)		350	450	—	cd/m²	White *1	(1)(5)
Brightness Uniformity		ΔI			75	—	—	%		(1)(5)(7)
Chromaticity	W	X			0.283	0.313	0.343	—		(1)(5)
		Y			0.299	0.329	0.359	—		
	R	(x, y)		Red	0.65, 0.34 Typ.					
	G			Green	0.30, 0.59 Typ.					
	B			Blue	0.15, 0.14 Typ.					
γ -curve		γ			2.4 Typ.					
LCD Panel Type					TFT Color					
Display Mode					Normally Black VA					
Wide Viewing Angle Technology					MVA-Premium					
Optimum Viewing Angle					— (symmetry)					(6)
Display Color					16 million (each 6-bit+2-bit FRC)					
Color of non-display area					Black					
Surface Treatment					Anti-glare (Haze value:25%, 3H(at weight of 300g))					

(*1) Specified value is measured in 20~30 minutes after lighting on (LCD module single).
A required value may not be achieved on condition that LCD module is built in the cabinet because of its radiation.
All items without "Brightness Uniformity" are measured at the center of display board.

(Note1) ・CS-1000 (MINOLTA Co., Ltd.) , BM-5A(Topcon) or equivalent luminance colorimeter should be used for the measurement.
Field=2°, L=500mm
・The specified value of viewing angle, contrast, brightness, brightness uniformity and chromaticity are under the dark room condition (1lux or less).

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Note 1) Definition of Viewing Angle (1)

Based on Fig.9-2.

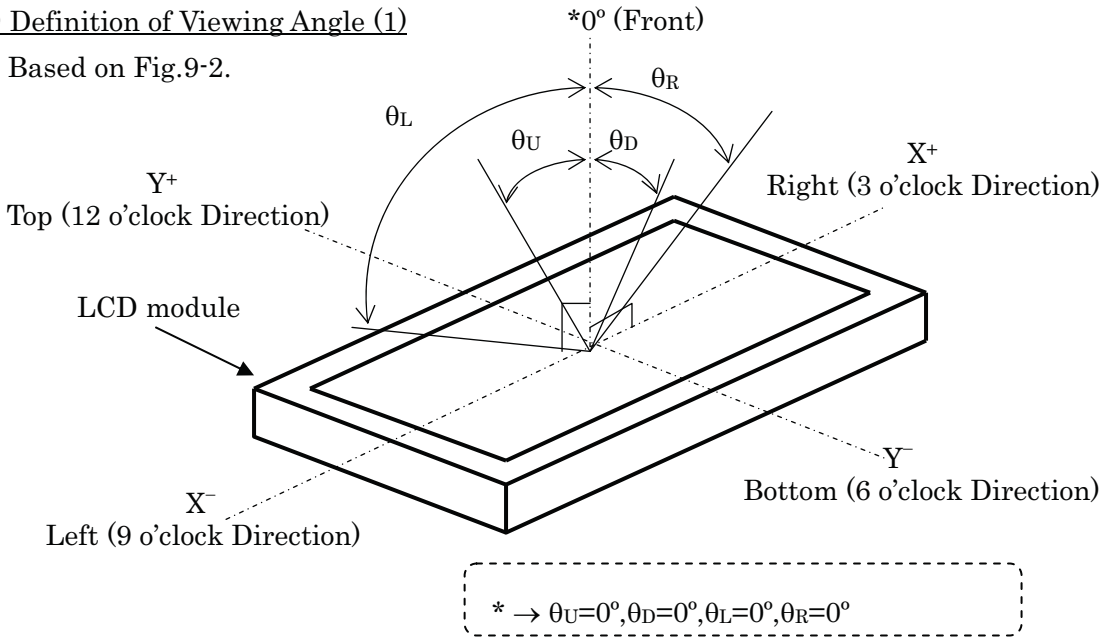

Fig.9-2. Definition of Viewing Angle (1)

Note 2) Definition of Viewing Angle (2)

Based on Fig.9-3.

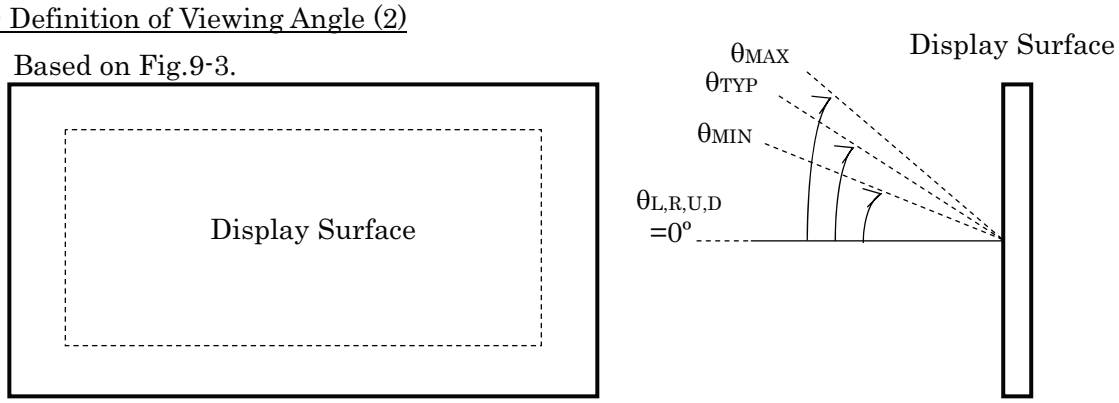

Fig.9-3. Definition of Viewing Angle (2)

Note 3) Definition of Contrast Ratio (CR)

Determined by Formula (1) based on Fig.9-4.Voltage-Brightness Characteristics.

$$= \frac{L_W \text{ (Brightness at white)}}{L_B \text{ (Brightness at black)}} \quad \text{-------- (1)}$$

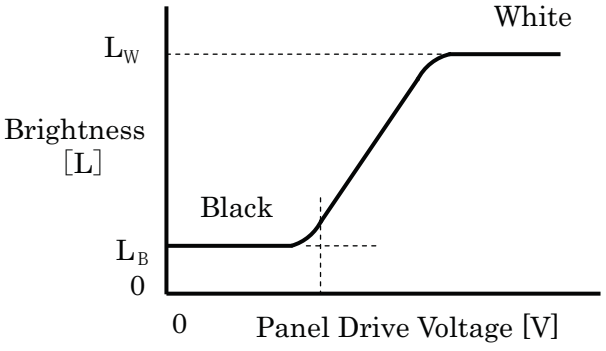

Fig.9-4. Voltage-Brightness Characteristics

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Note 4) Definition of Response Time

Based on Fig.9-5.

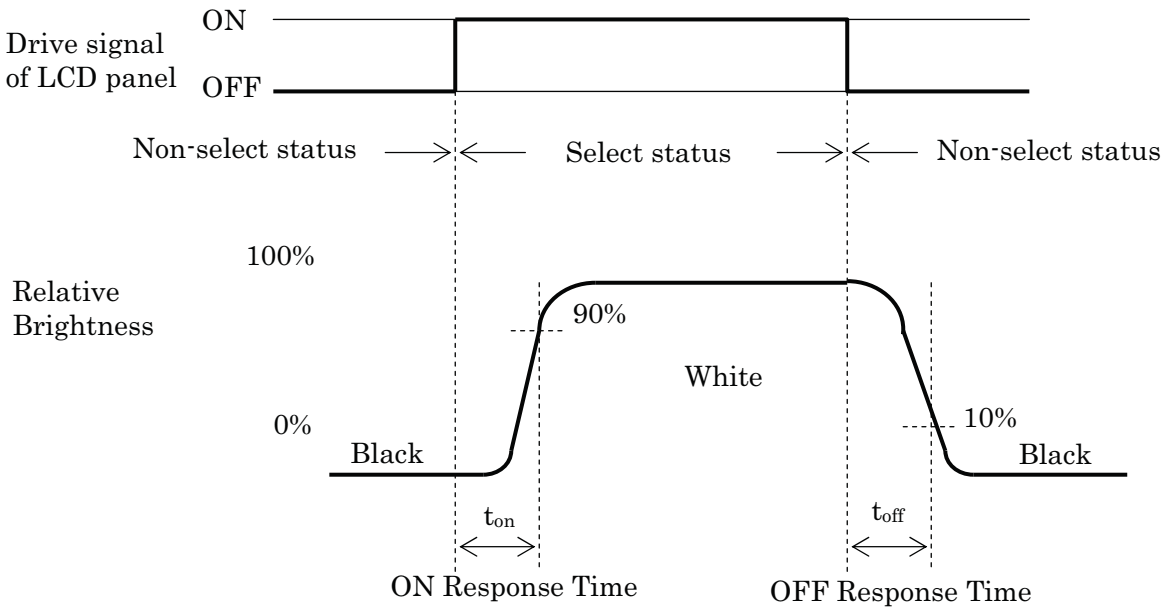

Fig.9-5. Definition of Response Time

Note 5) Contrast Ratio and Response Measurement System

Based on Fig.9-6.

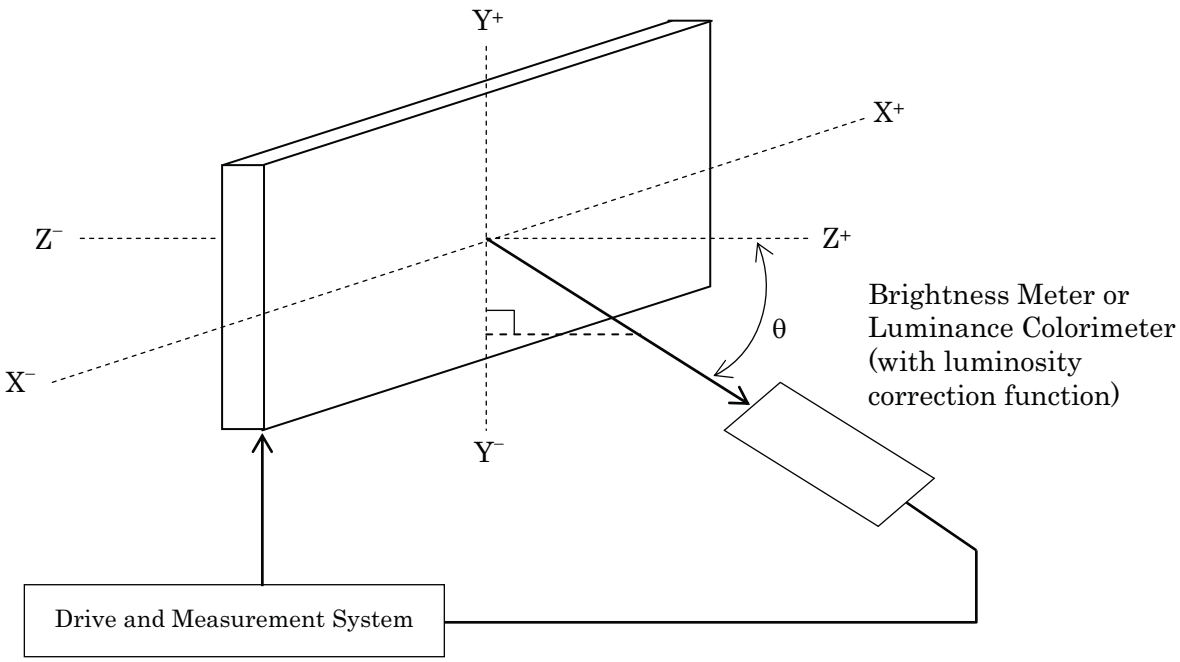

Fig.9-6. Contrast Ratio and Response Time Measurement System

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Note 6) Definition of Optimum Viewing Angle

Based on Fig.9-7.

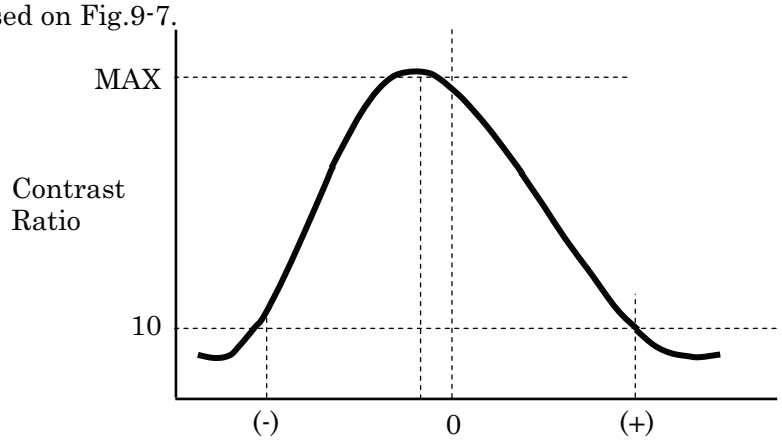
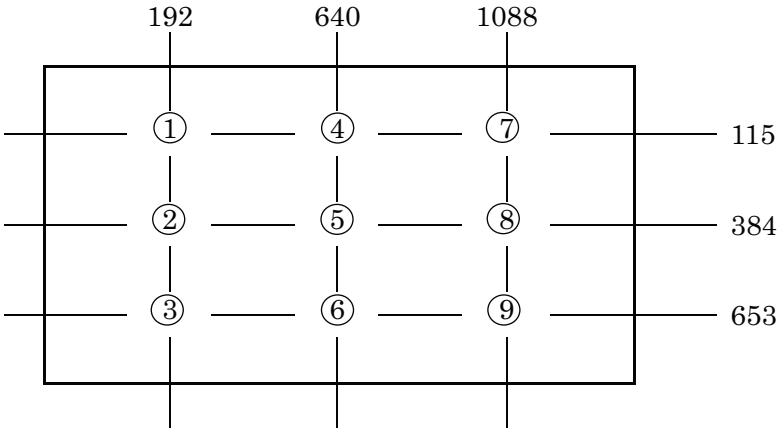


Fig.9-7. Definition of Viewing Angle

Note 7) Definition of Brightness Uniformity

Brightness uniformity is defined by the following formula.
Brightness (I1~I9) art measured at the following 9 points (① ~ ⑨) on the display area shown in Fig.9-8.

$$\text{Brightness Uniformity } (\Delta L) = \frac{|\text{Min. In}|}{|\text{Max. In}|} \times 100\ (\%),\ n=1 \text{ to } 9$$



Note) Each measurement point (① ~ ⑨) defines the center spot of Brightness Meter view.
The tolerance of measurement position is ±3mm.

Fig.9-8. Measurement Points

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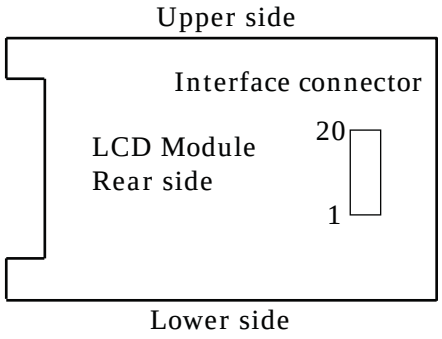
10. INTERFACE SPECIFICATIONS

10-1-1. Signal descriptions

Table 10-1 shows the description and configuration of Interface signals (CN1).

Table 10-1-1. Interface signals (CN1)

Pin No.	Symbol	I/O	Function
1	VDD	—	+5V Power suply
2	VDD	—	+5V Power suply
3	GND	—	Ground
4	GND	—	Ground
5	RX0-	I	LVDS Receiver Signal(-)
6	RX0+	I	LVDS Receiver Signal(+)
7	GND	—	Ground
8	RX1-	I	LVDS Receiver Signal(-)
9	RX1+	I	LVDS Receiver Signal(+)
10	GND	—	Ground
11	RX2-	I	LVDS Receiver Signal(-)
12	RX2+	I	LVDS Receiver Signal(+)
13	GND	—	Ground
14	RXCLK-	I	LVDS Receiver Clock Signal(-)
15	RXCLK+	I	LVDS Receiver Clock Signal(+)
16	GND	—	Ground
17	RX3-	I	LVDS Receiver Signal(-)
18	RX3+	I	LVDS Receiver Signal(+)
19	GND	—	Ground
20	Data Mapping Select Input	I	Low : table 10-1-2 Open or Hi : table 10-1-3



Connector ：D14H-20P-1.25H(HIROSE)

User's connector ：DF14-20S-1.25 (HIROSE)

Note)When using a interface connector other than the recommended one, a defect in the initial stage or a problem concerning long term reliabiliting may occur.

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10-1-2. LVDS Data Mapping 1

Table 10-1-2 shows the LVDS data mapping 1.(DataMappingSelectInput = Low)

Table 10-1-2. LVDS Data Mapping 1

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R0	TxOUT0− TxOUT0+	5 6	RX0− RX0+	27	RxOUT0	IR0
52	TxIN1	R1				29	RxOUT1	IR1
54	TxIN2	R2				30	RxOUT2	IR2
55	TxIN3	R3				32	RxOUT3	IR3
56	TxIN4	R4				33	RxOUT4	IR4
2	TxIN5	R7	TxOUT3− TxOUT3+	17 18	RX3− RX3+	34	RxOUT5	IR7
3	TxIN6	R5	TxOUT0− TxOUT0+	5 6	RX0− RX0+	35	RxOUT6	IR5
4	TxIN7	G0				37	RxOUT7	IG0
6	TxIN8	G1	TxOUT1− TxOUT1+	8 9	RX1− RX1+	38	RxOUT8	IG1
7	TxIN9	G2				39	RxOUT9	IG2
8	TxIN10	G6	TxOUT3− TxOUT3+	17 18	RX3− RX3+	41	RxOUT10	IG6
10	TxIN11	G7				42	RxOUT11	IG7
11	TxIN12	G3	TxOUT1− TxOUT1+	8 9	RX1− RX1+	43	RxOUT12	IG3
12	TxIN13	G4				45	RxOUT13	IG4
14	TxIN14	G5				46	RxOUT14	IG5
15	TxIN15	B0				47	RxOUT15	IB0
16	TxIN16	B6	TxOUT3− TxOUT3+	17 18	RX3− RX3+	49	RxOUT16	IB6
18	TxIN17	B7				50	RxOUT17	IB7
19	TxIN18	B1	TxOUT1− TxOUT1+	8 9	RX1− RX1+	51	RxOUT18	IB1
20	TxIN19	B2	TxOUT2− TxOUT2+	11 12	RX2− RX2+	53	RxOUT19	IB2
22	TxIN20	B3				54	RxOUT20	IB3
23	TxIN21	B4				55	RxOUT21	IB4
24	TxIN22	B5				1	RxOUT22	IB5
25	TxIN23	RESERVED	TxOUT3− TxOUT3+	17 18	RX3− RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2− TxOUT2+	11 12	RX2− RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	ENAB
50	TxIN27	R6	TxOUT3− TxOUT3+	17 18	RX3− RX3+	7	RxOUT27	IR6
31	TxCLKIN	DCLK	TxCLKOUT− TxCLKOUT+	14 15	RXCLK− RXCLK+	26	RxCLKOUT	DCLK

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10-1-3. LVDS Data Mapping 2

Table 10-1-3 shows the LVDS data mapping 2.(DataMappingSelectInput = Open or Hi)

Table 10-1-3. LVDS Data Mapping 2

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R2	TxOUT0− TxOUT0+	5 6	RX0− RX0+	27	RxOUT0	IR2
52	TxIN1	R3				29	RxOUT1	IR3
54	TxIN2	R4				30	RxOUT2	IR4
55	TxIN3	R5				32	RxOUT3	IR5
56	TxIN4	R6				33	RxOUT4	IR6
2	TxIN5	R1	TxOUT3− TxOUT3+	17 18	RX3− RX3+	34	RxOUT5	IR1
3	TxIN6	R7	TxOUT0−	5	RX0−	35	RxOUT6	IR7
4	TxIN7	G2	TxOUT0+	6	RX0+	37	RxOUT7	IG2
6	TxIN8	G3	TxOUT1−	8	RX1−	38	RxOUT8	IG3
7	TxIN9	G4	TxOUT1+	9	RX1+	39	RxOUT9	IG4
8	TxIN10	G0	TxOUT3−	17	RX3−	41	RxOUT10	IG0
10	TxIN11	G1	TxOUT3+	18	RX3+	42	RxOUT11	IG1
11	TxIN12	G5	TxOUT1− TxOUT1+	8 9	RX1− RX1+	43	RxOUT12	IG5
12	TxIN13	G6				45	RxOUT13	IG6
14	TxIN14	G7				46	RxOUT14	IG7
15	TxIN15	B2				47	RxOUT15	IB2
16	TxIN16	B0	TxOUT3−	17	RX3−	49	RxOUT16	IB0
18	TxIN17	B1	TxOUT3+	18	RX3+	50	RxOUT17	IB1
19	TxIN18	B3	TxOUT1− TxOUT1+	8 9	RX1− RX1+	51	RxOUT18	IB3
20	TxIN19	B4	TxOUT2− TxOUT2+	11 12	RX2− RX2+	53	RxOUT19	IB4
22	TxIN20	B5				54	RxOUT20	IB5
23	TxIN21	B6				55	RxOUT21	IB6
24	TxIN22	B7				1	RxOUT22	IB7
25	TxIN23	RESERVED	TxOUT3− TxOUT3+	17 18	RX3− RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2− TxOUT2+	11 12	RX2− RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	ENAB
50	TxIN27	R0	TxOUT3− TxOUT3+	17 18	RX3− RX3+	7	RxOUT27	IR0
31	TxCLKIN	DCLK	TxCLKOUT− TxCLKOUT+	14 15	RXCLK− RXCLK+	26	RxCLKOUT	DCLK

10-2. Color Data Assignment

Table 10-2 shows the color data assignment.

Table 10-2. Color Data Assignment

Color and Brightness			Input data (0 : Low level, 1 : High level)																							
			R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	2	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	⇩	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇩	254	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	⇧	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	⇩	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	⇩	254	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	⇧	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	⇩	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	⇩	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note.1) Definition of gray scale : Color (n) ---"n" indicates gray scale level.
The gray scale is brighter as the number is larger.

Note.2) Data; 1: High, 0: Low

Note.3) By inputting 8-bit data signal for each red, green and blue, this module can display 256 gray scale independently for each color.
Therefore, the module is able to display 16 million colors. Color data are 24 lines.

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10-3. Input Signal Timing

Table 10-3 and Fig.10-3 shows the input signal timing.

Table 10-3. Timing Characteristics (T=0~50ºC, Vcc=5±0.25V)

Item			Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK signal (Clock)		Period	Tc	12.195	15.382	20.000	ns	
		Frequency	fc	50.00	65.01	82.00	MHz	fc=1/Tc
		Duty	Tch/Tc	45	50	55	%	*1
		High time	TclkH	5.0	—	—	ns	
		Low time	TclkL	5.0	—	—	ns	
		Rise time	Tclkr	—	—	5.0	ns	
		Fall time	Tclkf	—	—	5.0	ns	
DCLK-Data Timing		Setup time	Tset	4	—	—	ns	
		Hold time	Thold	4	—	—	ns	
Data-ENAB timing	Horizontal	Period	Th	1310	1344	1688	DCLK	
		Frequency	fh	35.0	48.3	75.0	kHz	
		Display period	Thd	1280	1280	1280	DCLK	*2,3
	Vertical	Period	Tv	776	806	806	Th	16.67ms
		Frequency	1/Tv	50	60	75	Hz	WXGA mode
		Display period	Tvd	768	768	768	Th	*2,3
	Data-ENAB timing		Tdn	0	0	0	DCLK	*4

*1) DCLK signal input must be valid while power supply is applied.

*2) Display position is specified by the ENAB signal only.

- Horizontal display position is specified by the rise of ENAB signal. The data which is latched by the falling edge of 1st DCLK right after the rise of ENAB, is displayed on the left edge of the screen.
- Vertical display position is specified by the rise of ENAB after a “Low” level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of ENAB is displayed at the top line of screen.

*3) If a period of ENAB “High” is less than 1280 DCLK or less than 768 lines, the rest of the screen displays black.

*4) The display position does not fit to the screen if a period of ENAB “High” and the effective data period do not synchronize with each other.

*5) The specifications of all are not provided individually.

It is necessary that all of those specifications are satisfied at the same time.

*6)The display quality is guaranteed by the Typ timing.

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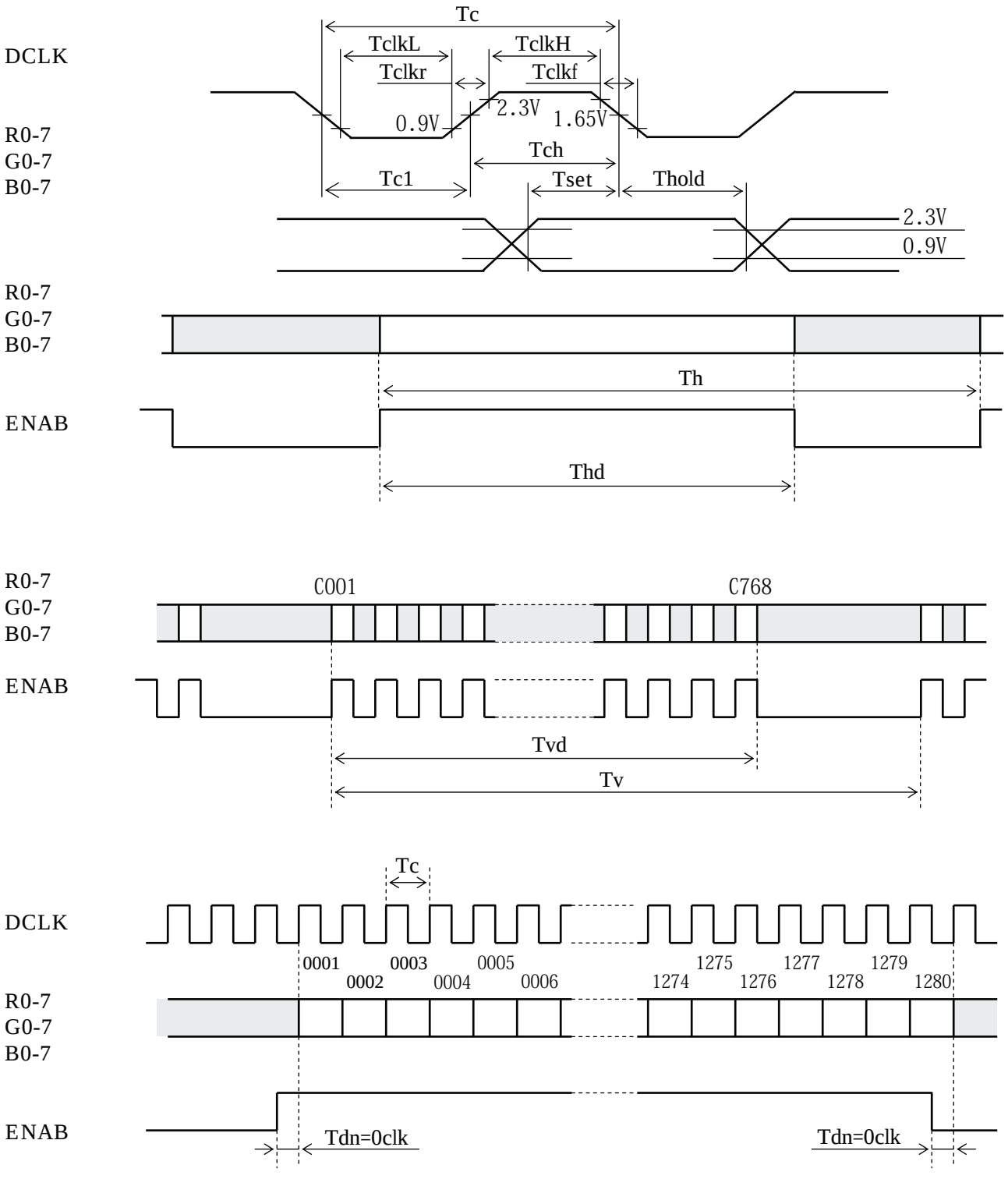

Fig.10-3.Input Signal Timing Chart

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10-4. Correspondence between Data and Display Position

Fig.10-4 shows the Correspondence between Data and Display Position.

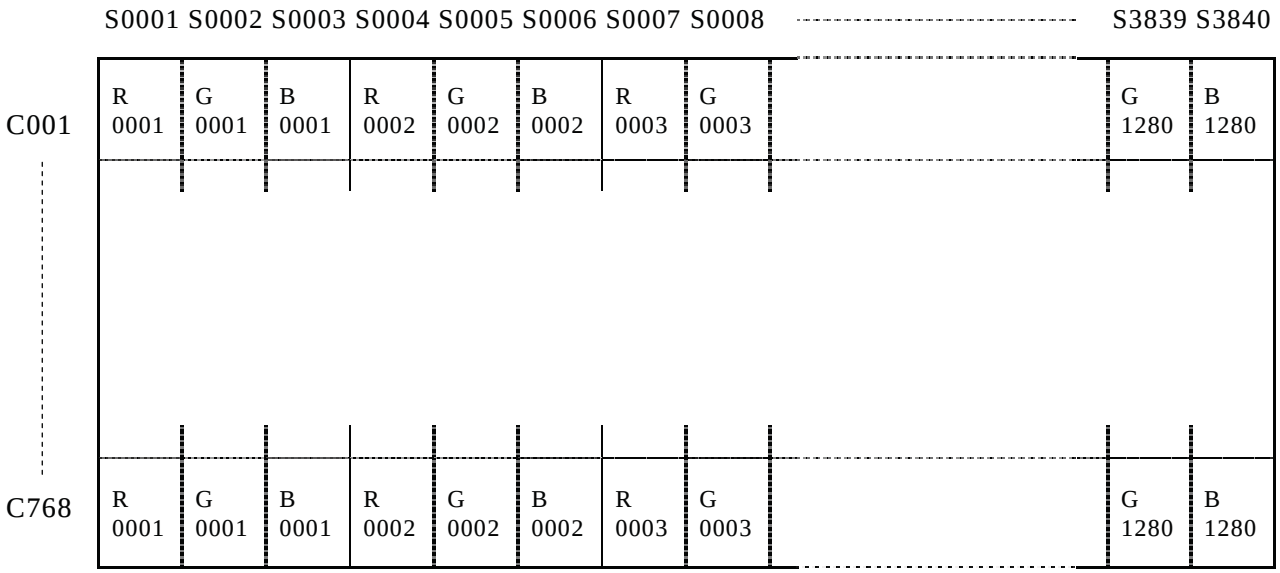

Fig.10-4.Correspondence Data and Display Position

10-5. Power Supply Sequence

The sequence of input signals and On/Off of the power supply of this LCD module should be in the specification shown in Fig.10-5 to prevent latch-up of the driver ICs and DC driving of the LCD panel.

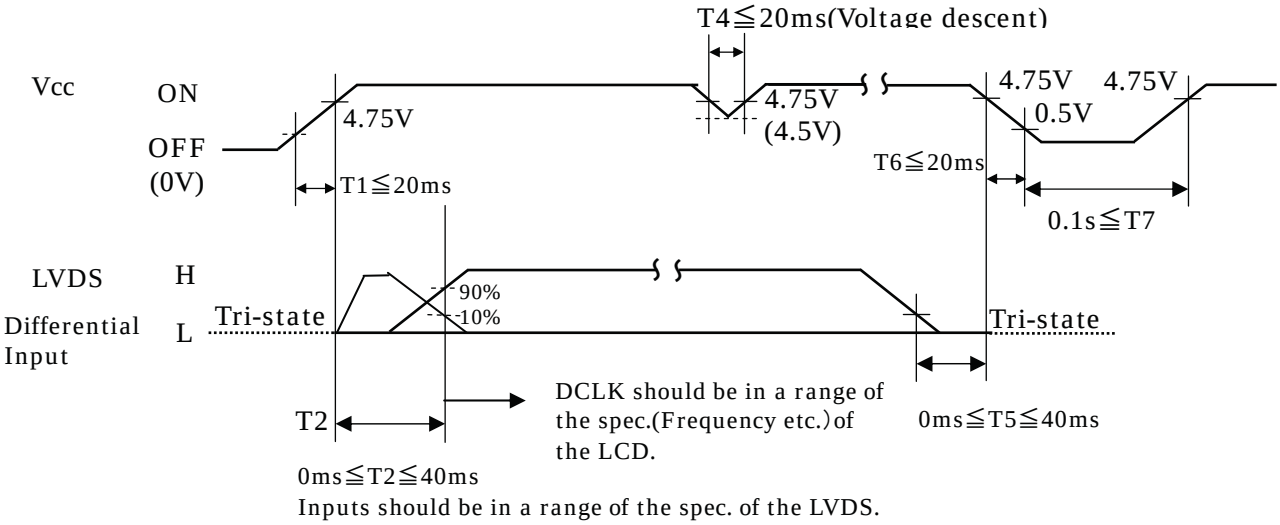

Fig.10-5. Power Supply Sequence

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11. BACKLIGHT SPECIFICATIONS

11-1. Pin Configuration for Backlight

Table 11-1(a) and 11-1(b) shows the description and pin assignment of the connectors (CN-A and B) for the Backlight of this LCD module.

Table 11-1(a) Pin Assignment of CN-A

Pin No.	Signal	Function	Cable color
1	V_L1	Power supply for CCFL 1	Red
2	V_L2	Power supply for CCFL 2	Red
3	NC	——	—
4	GND	Ground (for V_L1, 2)	White

Table11-1(b) Pin Assignment of CN-B

Pin No.	Signal	Function	Cable color
1	V_L3	Power supply for CCFL 3	Red
2	V_L4	Power supply for CCFL 4	Red
3	NC	——	—
4	GND	Ground (for V_L3, 4)	White

Connector : Housing : BHR-04VS-1
: Contact : SBH-001T-P0.5
User's Connector : Post with base : SM04(4.0)B-BHS-1-TB
Supplier : Japan Solderless Terminal Trading Company LTD. (J.S.T.)

11-2. CCFL

Supplier : SANKEN ELECTRIC CO., LTD Part No. SD26E3850E8350B3113000

11-3. Life

The life of the backlight is a minimum of 25,000 hours at the following conditions.

(1) Working conditions
① Ambient temperature : 25±5ºC
② Tube current(I_L) : (10.5mA or less)

(2) Definition of life
① Brightness becomes 50% or below 50% of the minimum brightness value shown in Table 9-1.
② The lamp cannot be lit by the breakdown voltage of 1600Vvms.
③ Lamp is flashing.

11-4. Lamp Assembly set (for replacement)

Lamp Assembly set (with charge) is prepared for maintenance.
This set consists of an upper lamp assembly and a lower lamp assembly.

Type number : FLCL-21

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12. APPEARANCE SPECIFICATIONS

12-1 Appearance

No.	Item	Judgment method and standard	
1	Bright spot (high and Low)	≦ 8 dots (Note 1)	
2	Bright spot connection (high and low)	2 dots connection ≦ 2 pair (Note 1) 3 dots connection ≦ 1 pair	
3	Total of bright spot	≦ 8 dots	
4	Dark spot	≦ 10 dots (Note 2)	
5	Dark spot connection	2 dots connection ≦ 3 pair (Note 2) 3 dots connection ≦ 1 pair	
6	Total of dark spot	≦ 10 dots (Note 2)	
7	Total of dot defect	≦ 18 dots	
8	Distance of dot defect	≧ 2mm	
9	Black / white spot	D≦0.3	Ignore
		0.3<D≦0.6	N≦5
		0.6<D≦0.9	N≦2(Distance≧100mm)
		0.9<D	0
10	Mura	Ignore	

D:Average diameter [mm], W:Width [mm], L:Length [mm], S=(bright spot size)/(dot size)

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13. ENVIRONMENTAL SPECIFICATIONS

Table 13-1 shows the environmental specifications.

Table 13-1. Environmental Specifications

Item	Condition		Remark
Temperature	Operation	0~57ºC (Note1)	Temperature on surface of LCD panel (display area.)
	Storage	-20~60ºC	
Humidity	Operation	20~85%RH	Maximum wet-bulb temperature should not exceed 29ºC. No condensation.
	Storage	5~85%RH	
Vibration	Non-operation	10~500Hz, 1 cycle/20minute, 2G, 1.5mm max, 2hour each X, Y and Z directions	For single module without package.(Note2)
Shock	Non-operation	30G, 6ms, 1time each ±X, ±Y and ±Z directions.	

Note1: Temperature on surface of LCD panel should be under 57℃.

Note2: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged.

Table 13-2. Shock Resistance Standard when Module is Packaged

Dropping location	Dropping height	Count
A ~ J	60cm	1 time

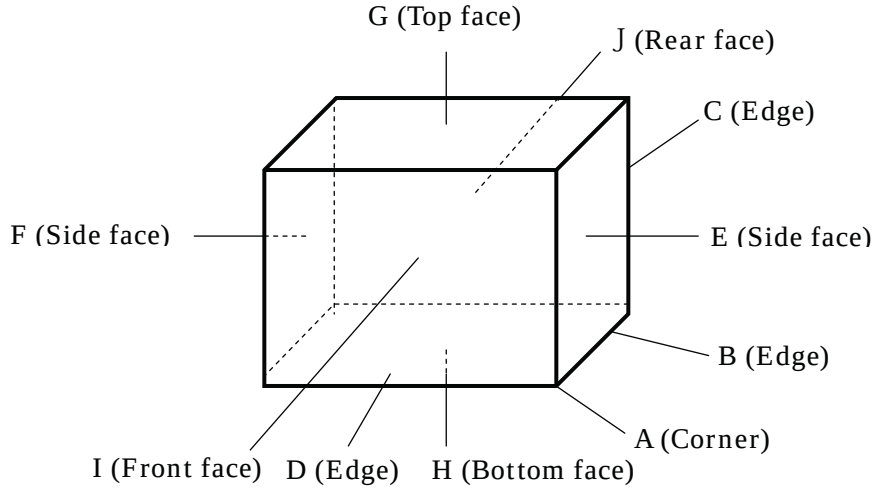

Fig.13-1. Direction to apply shock to package

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14. INDICATIONS

This module has the following indications.

(1) Product name : LCD unit

(2) Model number : NA19026-C081A

(3) Manufacturing number : 3 Y 0 0 0 0 1

- 3 — Last digit of manufacturing year.
- Y — Manufacturing month (Oct. = X, Nov. =Y, Dec. =Z)
- 0 0 0 0 1 — Serial number (To be reset every month on 1st.)

(8) Caution when changing cold-cathode tubes and disposal method of them. (See Fig. 14-1)

・WHEN CHANGING COLD CATHODE FLUORESCENT LAMPS, FOLLOW OPERATING SPECIFICATIONS. ESPECIALLY BE CAREFUL ABOUT THE LAMP'S SIDE-EDGE.
・蛍光管の交換は作業仕様書に従って行って下さい。特に蛍光管ホルダ側面のエッジに気をつけて下さい。

・THIS TFT COLOR LCD CONTAINS COLD CATHODE FLUORESCENT LAMPS. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR ITS DISPOSAL.
・当該液晶ディスプレイユニットには蛍光管が組み込まれていますので、地方自治体の条例または規則に従って廃棄して下さい。

Fig.14-1

LCD unit
2101234
NA19026-C081 A

Fig.14-2 Product label (example)

15. PACKAGING

Separately specified in packaging specifications.

15-1. Packaging specifications

(1) 5 LCD modules / 1 package.

(2) Weight: approximately 13kg / 1 package.

(3) Outline dimensions : 348mm(W)×328(D)×490mm(H)

15-2. Packaging method

Fig.15-2 (a),(b),(c),(d) show the packing method.

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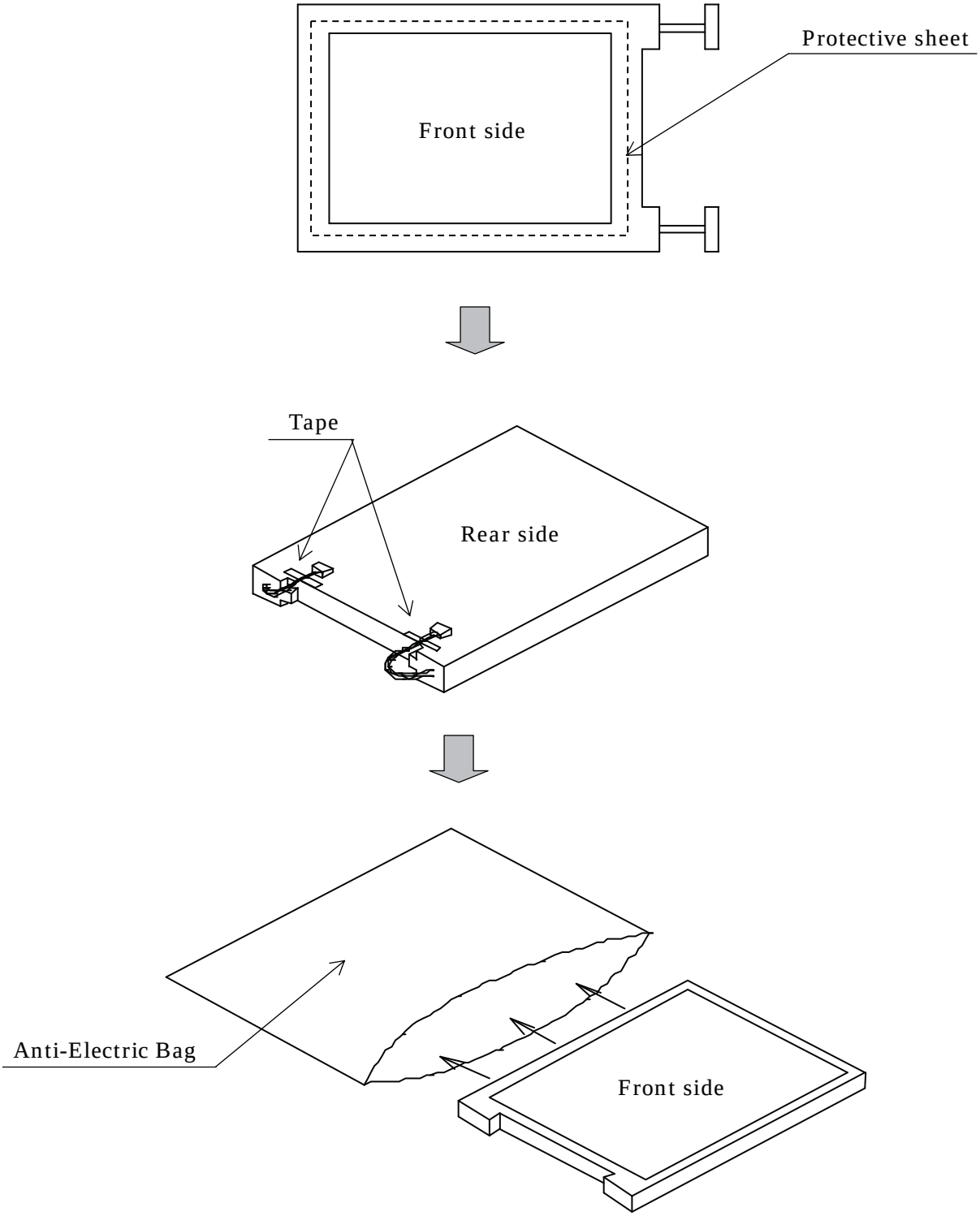

Fig.15-2(a) Packaging Method

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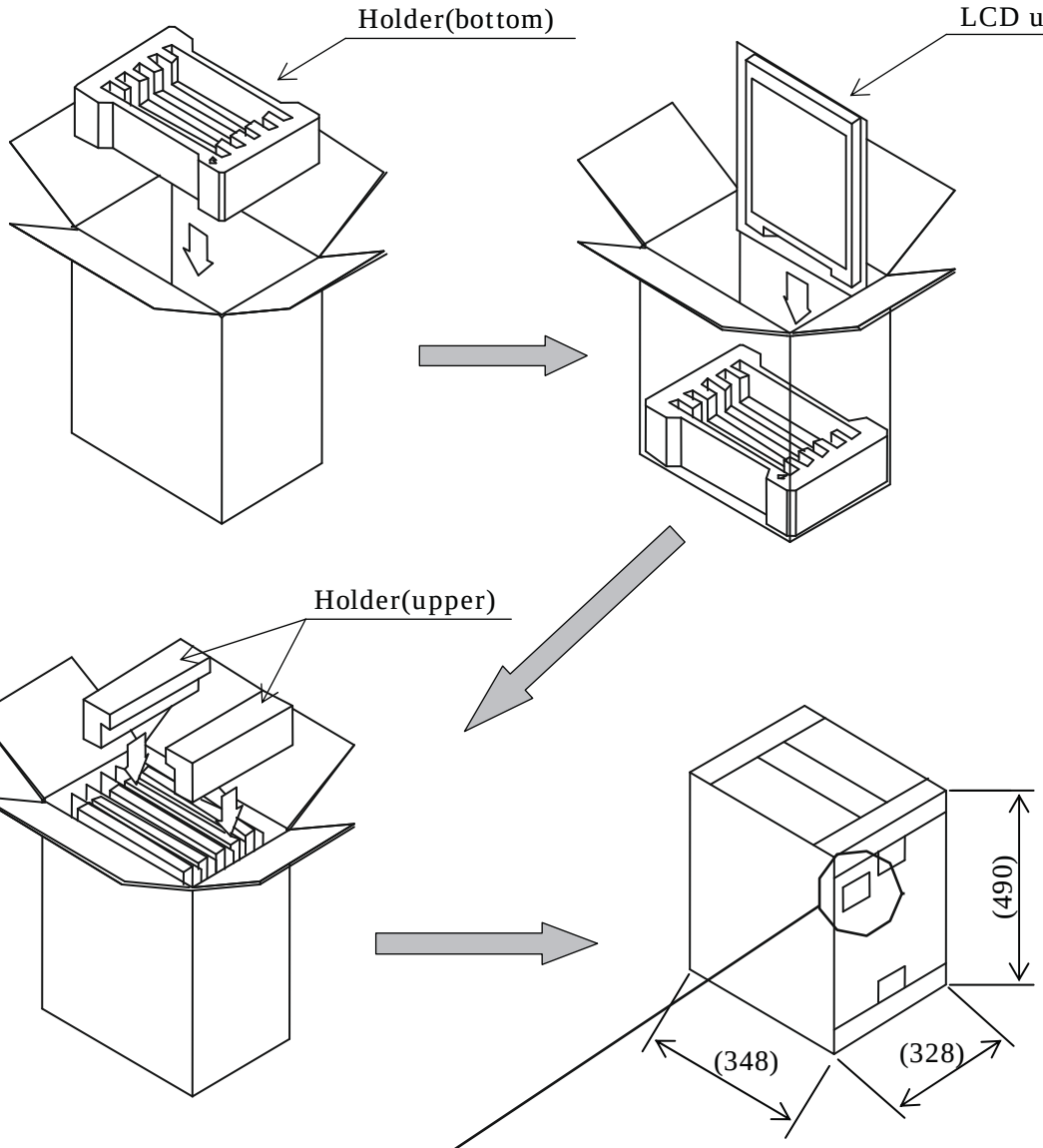

Label(example)

型 格 (TYPE)		数 量 (QTY.)	5
図 番 (DRWG. NO.)	NA19026-C081A	版 数 (REV. NO.)	01A

9X00001
9X00002
9X00003
9X00004
9X00005

バーコード

MADE IN JAPAN

- Taping
  Upper : H or I method
  Bottom : H method
- Upper and bottom holders should be anti-electrostatic type.

Fig.15-2(b) Packaging Method

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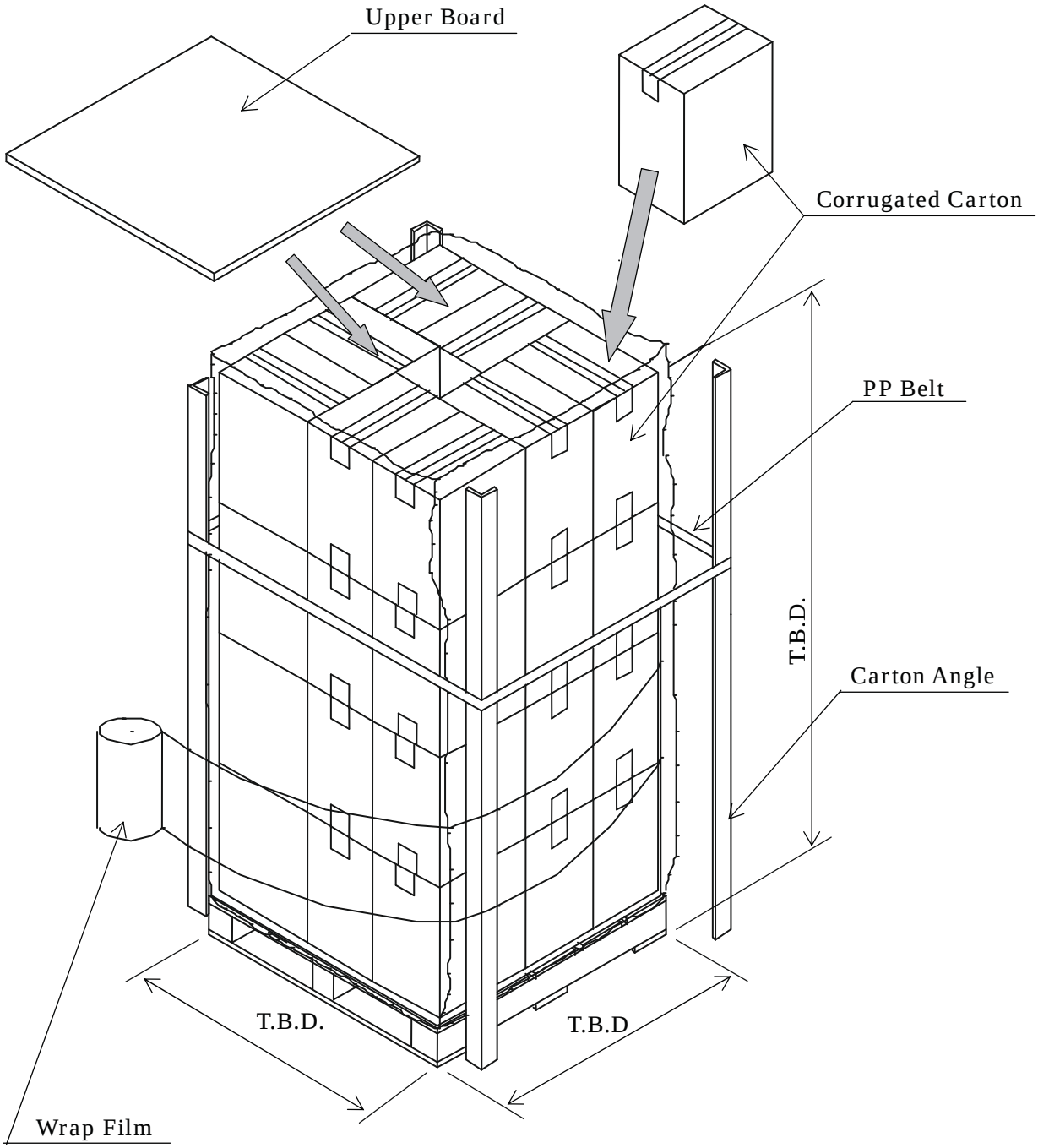

Fig.15-2(c) Packaging Method

DOCUMENT CONTROL SECTION

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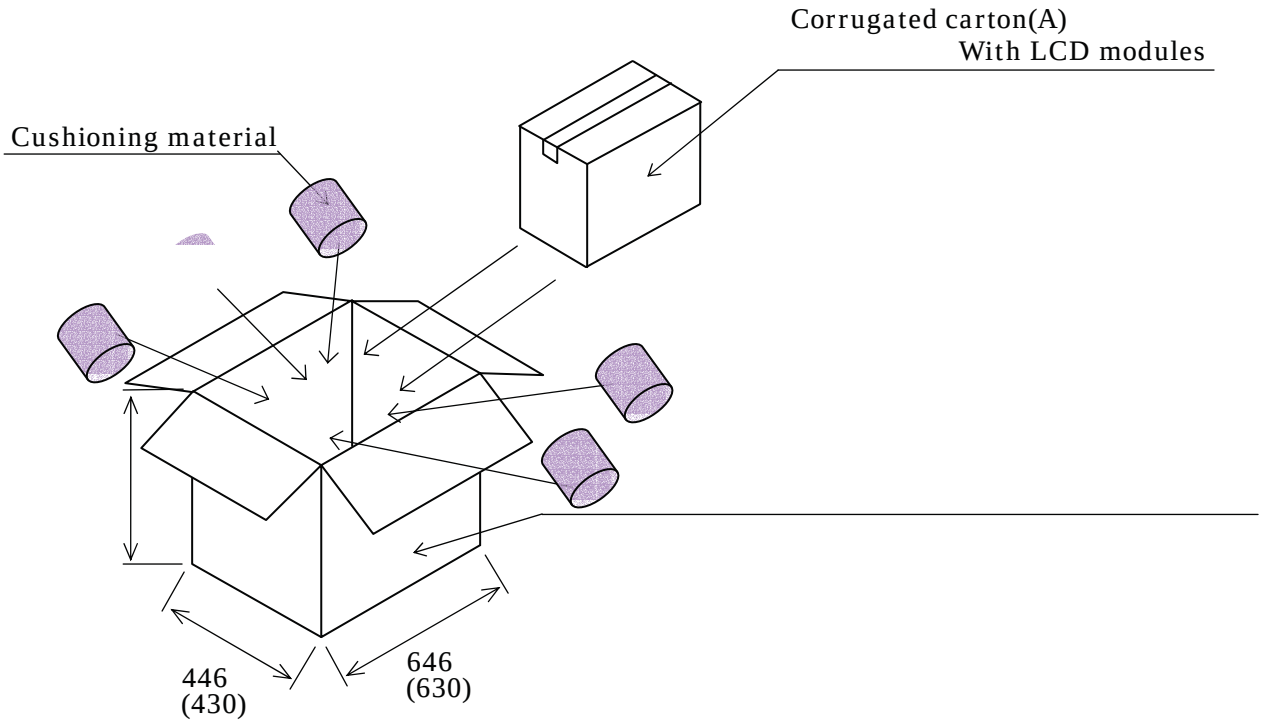

Note 1) The carton (A) should be placed in the middle of the container(B) with enough cushioning materials.

Figure.15-2(d) Packaging Method