

1A, 600V - 900V Glass Passivated High Efficient Rectifiers

FEATURES

- Designed for use in switching power supplies, inverters and as free wheeling diodes
- High efficiency, low VF
- High reliability
- Ultrafast recovery time for high efficiency
- 175°C operating junction temperature
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition







MECHANICAL DATA

Case: DO-204AL (DO-41)

Molding compound, UL flammability classification rating 94V-0 Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Pure tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 1A whisker test **Weight:** 0.33 g (approximately)

DO-204AL (DO-41)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)								
PARAMETER		SYMBOL	MUR160A	MUR190A	UNIT			
Maximum repetitive peak reverse voltage		V_{RRM}	600	900	V			
Maximum RMS voltage		V_{RMS}	420	630	V			
Maximum DC blocking voltage		V _{DC}	600	900	V			
Maximum average forward rectified current		I _{F(AV)}		1	Α			
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	35		А			
Maximum instantaneous forward voltage @ 1 A (Note 1)	T _J =25°C T _J =125°C	V _F	1.25 1.05	1.70 1.50	V			
Maximum reverse current @ rated V _R	T _J =25°C T _J =125°C	I _R	5 150		μΑ			
Maximum reverse recovery time (Note 2)		t _{rr}	50	75	ns			
Typical junction capacitance (Note 3)		CJ	27	15	pF			
Typical thermal resistance		$R_{\theta JA}$	5	50	°C/W			
Operating junction temperature range		T _J	- 55 to +175		°C			
Storage temperature range		T _{STG}	- 55 to +175		°C			

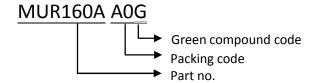
Note 1: Pulse Test with PW=300µs, 1% Duty Cycle

Note 2: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

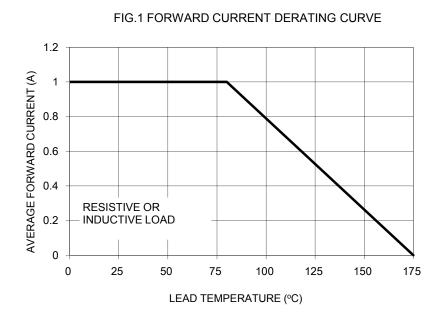


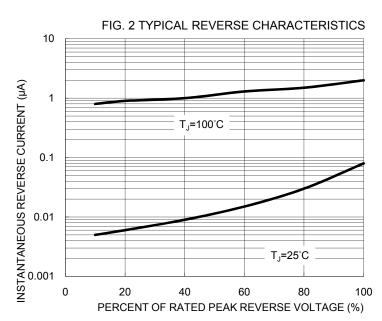
ORDER INFORMATION (EXAMPLE)

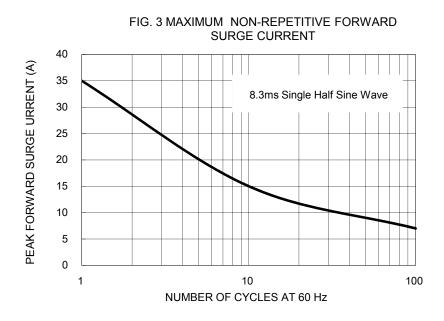


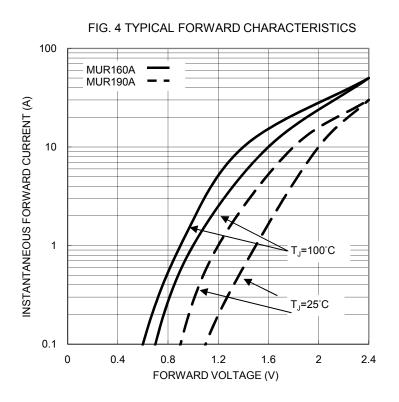
RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)









Document Number: DS_D0000067



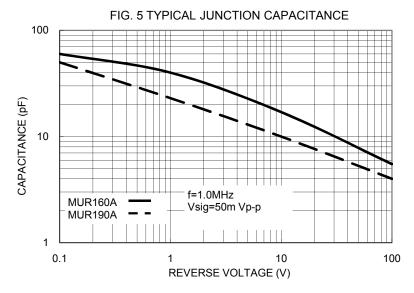
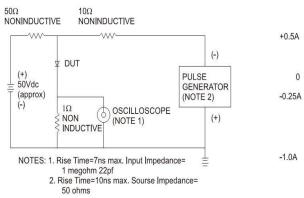
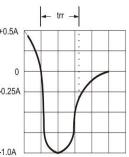
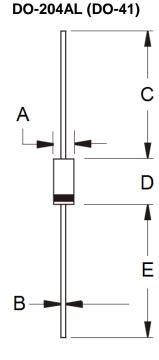


FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)		
	Min	Max	Min	Max	
Α	2.00	2.70	0.079	0.106	
В	0.71	0.86	0.028	0.034	
С	25.40	-	1.000	-	
D	4.20	5.20	0.165	0.205	
Е	25.40	-	1.000	-	

MARKING DIAGRAM



P/N = Specific Device Code G = Green Compound

YWW = Date Code F = Factory Code





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Document Number: DS_D0000067 Version: E15