MT3243B

NChannel Enhancement Mode MOSFET

Feature Description

- 30V/140A
 - $R_{DS(ON)}=1.9m\Omega(typ.)@V_{GS}=10V$ $R_{DS(ON)}=3.0m\Omega(typ.) @V_{GS}=4.5V$
- 100% avalanche tested
- Excellent CdV/dt effect decline
- Halogen Free Device Available

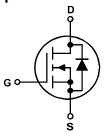
Applications

- Systems High Frequency Synchronous Buck Converters for Computer Processor Power
- High Frequency Isolated DC-DC
 Converters with Synchronous Rectification
 for Telecom and Industrial Use

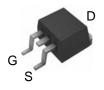


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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



TO-263-2L

Absolute Maximum Ratings(T_A = 25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit	
Common Ra	tings (Tc=25°C Unless Otherwise Noted)			
Voss	Drain-Source Voltage	30	V	
Vgss	Gate-Source Voltage	±20	V	
TJ	Maximum Junction Temperature	175	°C	
Тѕтс	Storage Temperature Range		-55 to 175	°C
Is	Drain Current-Continuous	Tc=25°C	140	Α
Mounted on	Large Heat Sink	*	39	*
IDM	Pulsed Drain Current *	Tc=25°C	560	Α
	Continuous Brain Commant	Tc=25°C	140	Α
lD	Continuous Drain Current	Tc=100°C	101	Α
-	M	Tc=25°C	115	W
Pb	Maximum Power Dissipation	Tc=100°C	57.5	W
Reuc	Thermal Resistance, Junction-to-Case		1.3	°CM
ReuA	Thermal Resistance, Junction-to-Ambient **		62.5	°CM
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	186	mJ

Note: * Repetitive rating ; pulse width limited by max junction temperature.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Cumbal	Parameter	Test Conditions				l lmi4
Symbol	Parameter	rest Conditions	Min	Тур	Max	Unit
Static Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _{DS} =250uA	30	-	-	V
lana	Drain-to-Source LeakageCurrent	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
IDSS		TJ=55°C	-	-	5	uA
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	1.5	3	V
Igss	Gate-Source Leakage Current	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
D*	Drain-Source On-state Resistance	V _{GS} =10V,I _{DS} =70A	-	1.9	2.2	mΩ
Rds(on)*		V _{GS} =4.5V,I _{DS} =70A	-	3.0	3.5	mΩ
Diode Cha	racteristics					
V _{SD} *	Diode Forward Voltage	I _{SD} =70A,V _{GS} =0V	-	0.8	1.3	V
trr	Reverse Recovery Time	lon=704 dlon/dt=1004/up	-	23	-	ns
Qrr	Reverse Recovery Charge	IsD=70A,dIsD/dt=100A/us	-	58	_	nC

Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

0	B	To al O and little and				11.24
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Dynamic	Characteristics	·				
Rg	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1 MHz	-	1.9	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	4726	-	
Coss	Output Capacitance	V _{DS} =25V,	-	469	-	pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	322	-	
t d(ON)	Turn-on Delay Time		-	13	-	
Tr	Turn-on Rise Time	V_{DD} =15 V , R_{G} =4 Ω ,	-	11	-	ns
td(OFF)	Turn-off Delay Time	I _{DS} =70A,V _{GS} =10V	-	41	-	
Tf	Turn-off Fall Time		-	14	-	
Gate Cha	rge Characteristics					•
Qg	Total Gate Charge	\/ -24\/\\ -40\/	-	120	-	
Qgs	Gate-Source Charge	$V_{DS} = 24V, V_{GS} = 10V,$	-	9	-	nC
Qgd	Gate-Drain Charge	I _D =70A,	-	26	-	

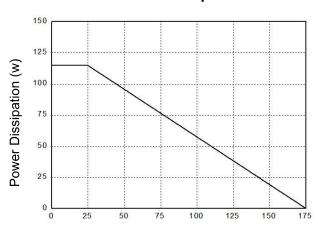
Note: *Pulse test; pulse width ≤ 300us, duty cycle ≤ 2%

^{**} Surface mounted on FR-4 board.

^{***} Limited by TJmax , starting TJ=25°C, L = 0.3mH, Ro= 25 Ω , V_{GS} =10V.

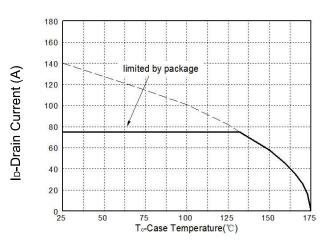
Typical Operating Characteristics

Power Dissipation



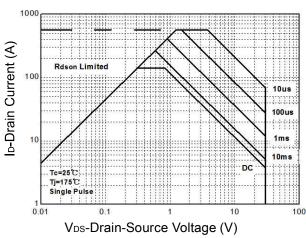
Tc-Case Temperture (°C)

Drain Current

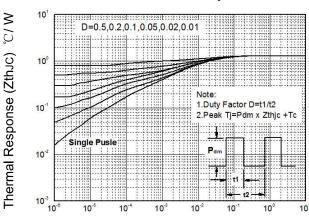


Tc-Case Temperture (°C)

Safe Operation Area

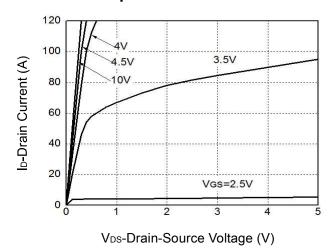




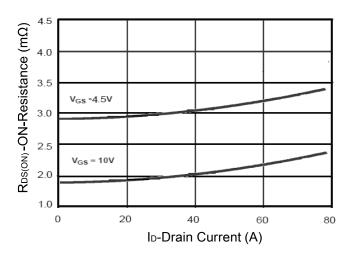


Maximum Effective Transient Thermal Impedance, Junction-to-Case

Output Characteristics

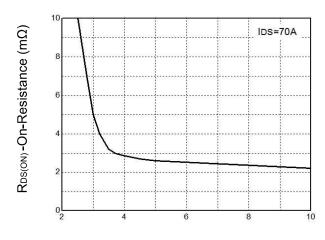


Drain-Source On Resistance



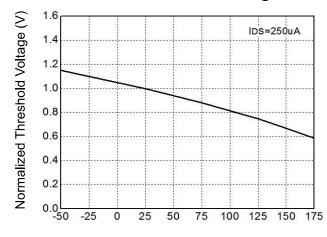
Typical Operating Characteristics(Cont.)

Gate-Source On Resistance



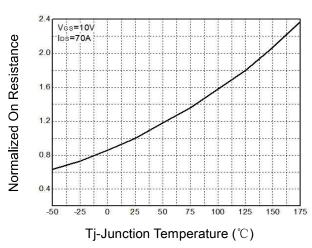
Vgs-Gate-Source Voltage (V)

Gate Threshold Voltage

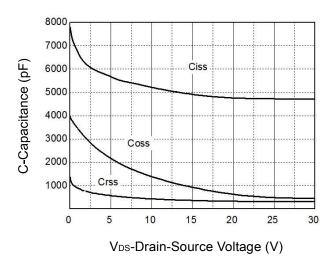


Tj-Junction Temperature (°C)

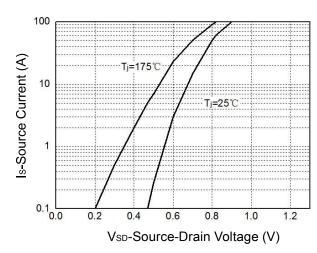
On-Resistance vs. Temperature



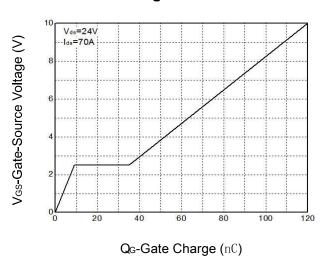
Capacitance Characteristics



Source- Drain Diode Forward



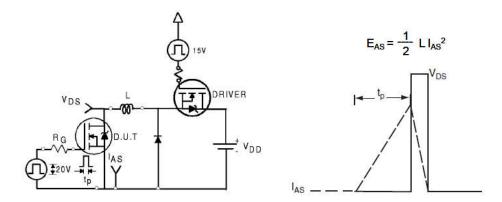
Gate Charge Characteristics



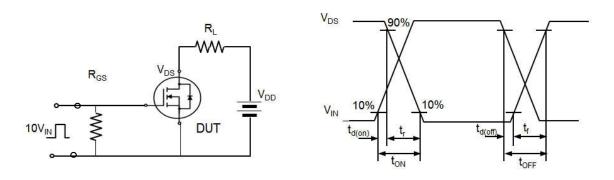
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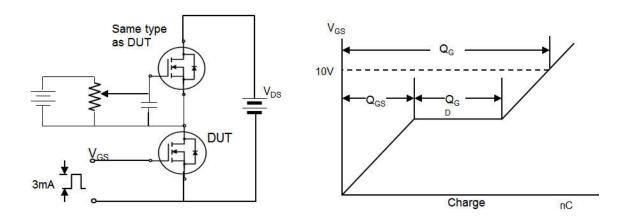
Avalanche Test Circuit and Waveforms



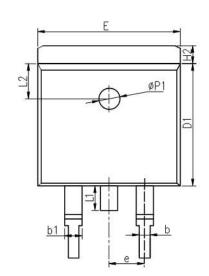
Switching Time Test Circuit and Waveforms

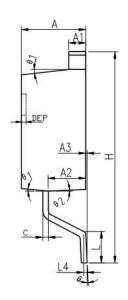


Qate Charge Test Circuit and Waveforms

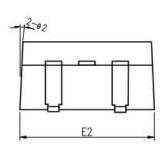


TO-263-2L





COMMON DIMENSIONS



SYMBOL		MM	ote	e	INCH		
STIVIBUL	MIN	NOM	MAX	MIN	NOM	MAX	
Α	4.40	4.57	4.70	0.173	0.180	0.185	
A1	1.22	1.27	1.32	0.048	0.050	0.052	
A2	2.59	2.69	2.79	0.102	0.106	0.110	
A3	0.00	0.10	0.20	0.000	0.004	0.008	
b	0.77	0.813	0.90	0.030	0.032	0.035	
b1	1.20	1.270	1.36	0.047	0.050	0.054	
С	0.34	0.381	0.47	0.013	0.015	0.019	
D1	8.60	8.70	8.80	0.339	0.343	0.346	
Е	10.00	10.16	10.26	0.394	0.400	0.404	
E2	10.00	10.10	10.20	0.394	0.398	0.402	
е		2.54 BSC			0.100 BSC		
Н	14.70	15.10	15.50	0.579	0.594 0.61		
H2	1.17	1.27	1.40	0.046	0.050	0.055	
L	2.00	2.30	2.60	0.079	0,091	0.102	
L1	1.45	1.55	1.70	0.057	0.061	0.067	
L2	2.50 REF			0.098 REF			
L4	0.25 BSC			0.010 BSC			
	0°	5°	8°	0°	5°	8°	
1	5°	7°	9°	5°	7°	9°	
2	10	3°	5°	1°	3°	5°	
ФР1	1.40	1.50	1.60	0.055	0.059	0.063	
DEP	0.05	0.10	0.20	0.002	0.004	0.008	

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