

MT11G035B

N-Channel Enhancement Mode Field Effect Transistor

Product Summary

- $V_{DS} = 100V$
- $I_D = 160A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} = 4.5 m\Omega @ V_{GS} = 10V$

The MT11G035B **SGT MOSFET** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating

Application

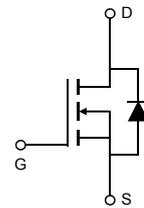
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



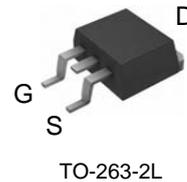
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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



TO-263-2L

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MT11G035B	MT11G035B	TO-263-2L		-	50

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous (Package Limited)	I_D	160	A
Drain Current-Continuous($T_C=100^\circ C$)	$I_D(100^\circ C)$	120	A
Pulsed Drain Current	I_{DM}	480	A
Maximum Power Dissipation	P_D	277	W
Single pulse avalanche energy ^(Note 5)	E_{AS}	480	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	0.55	$^{\circ}C/W$
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Electrical Characteristics ($T_C=25^{\circ}C$ unless otherwise noted)

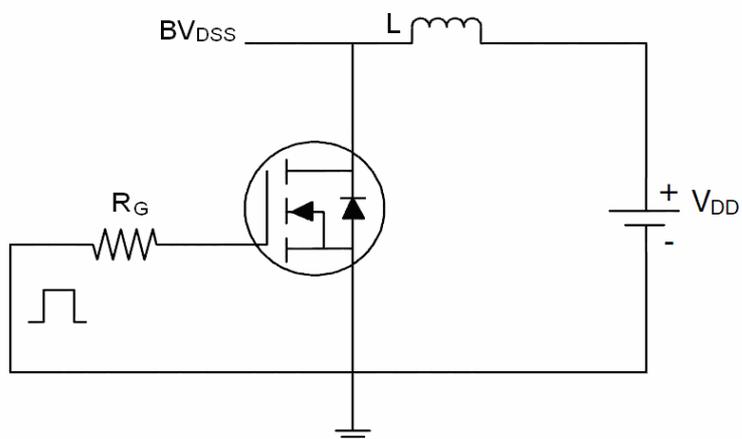
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.6	2	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=30A$	-	4.0	4.8	m Ω
		$V_{DS}=4.5V, I_D=30A$	-	5.0	5.3	
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ $F=1.0MHz$	-	4200	-	PF
Output Capacitance	C_{oss}		-	2400	-	PF
Reverse Transfer Capacitance	C_{rss}		-	100	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=50V, I_D=30A$ $V_{GS}=10V, R_G=4.7\Omega$	-	48	-	nS
Turn-on Rise Time	t_r		-	56	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	75	-	nS
Turn-Off Fall Time	t_f		-	33	-	nS
Total Gate Charge	Q_g	$V_{DS}=50V, I_D=30A,$ $V_{GS}=10V$	-	118	-	nC
Gate-Source Charge	Q_{gs}		-	42	-	nC
Gate-Drain Charge	Q_{gd}		-	38	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$V_{GS}=0V, I_S=60A$	-	-	1.3	V
Diode Forward Current ^(Note 2)	I_S		-	-	160	A
Reverse Recovery Time	t_{rr}	$T_J=25^{\circ}C, I_F=I_S$	-	60	-	nS
Reverse Recovery Charge	Q_{rr}	$di/dt=100A/\mu s$ ^(Note 3)	-	560	-	nC

Notes:

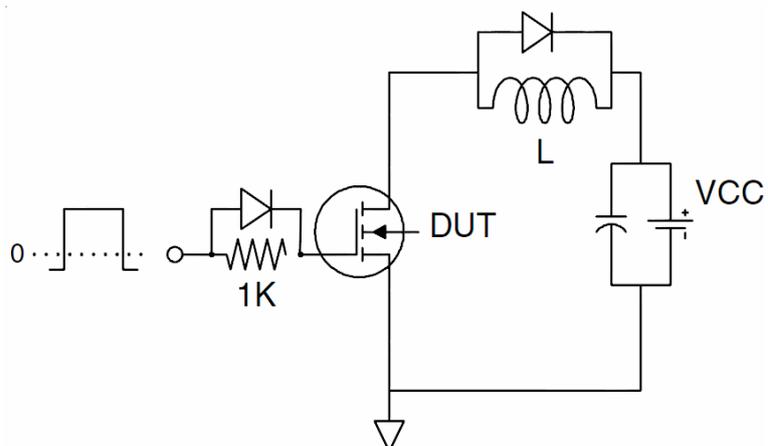
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^{\circ}C, V_{DD}=50V, V_G=10V, L=0.5mH, R_G=25\Omega$

Test Circuit

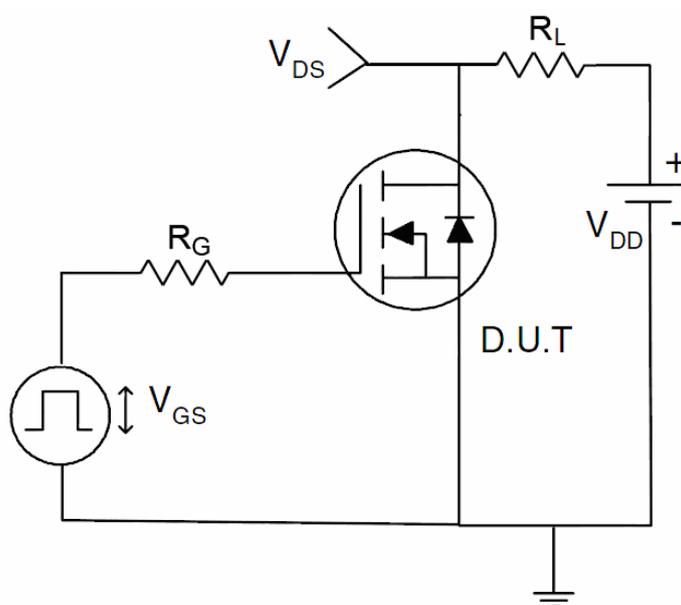
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics

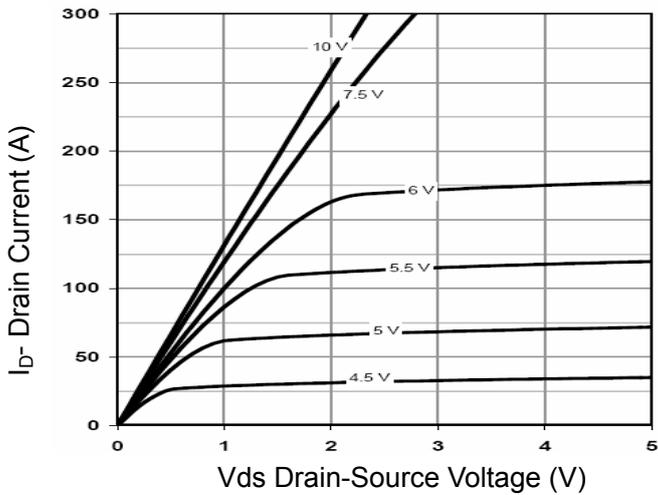


Figure 1 Output Characteristics

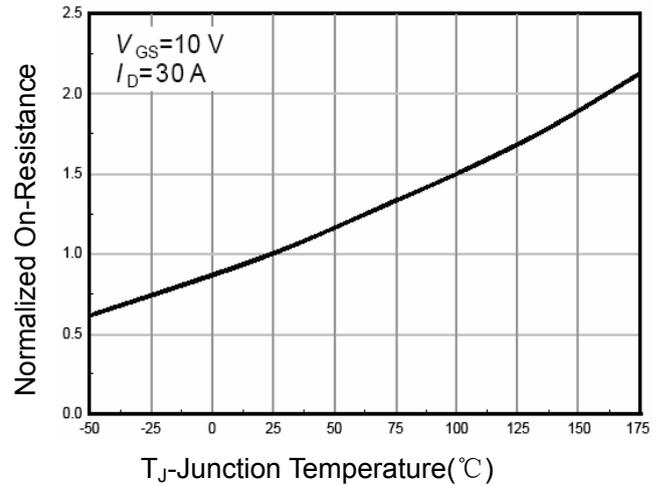


Figure 4 Rdson-Junction Temperature

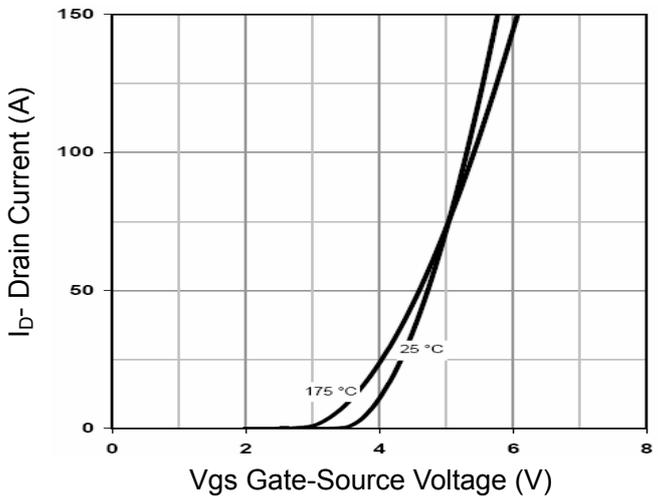


Figure 2 Transfer Characteristics

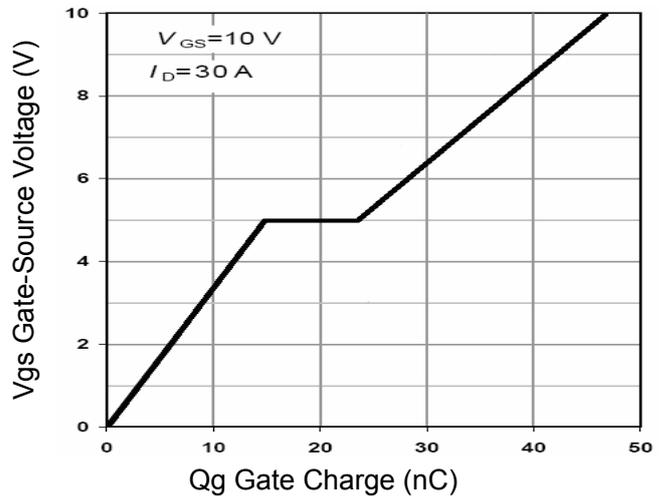


Figure 5 Gate Charge

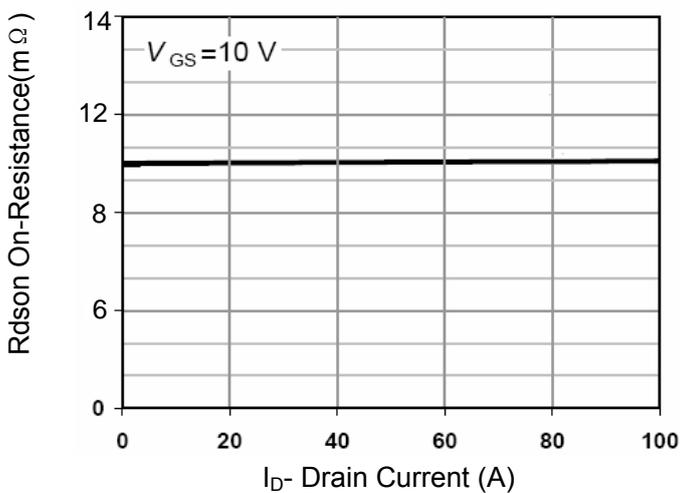


Figure 3 Rdson- Drain Current

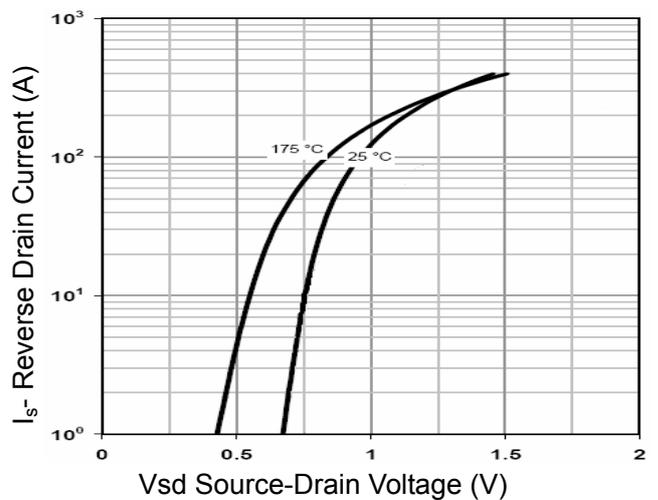


Figure 6 Source- Drain Diode Forward

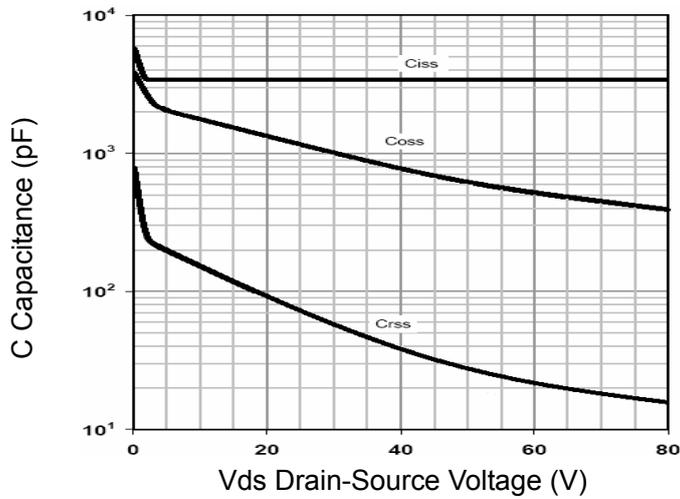


Figure 7 Capacitance vs Vds

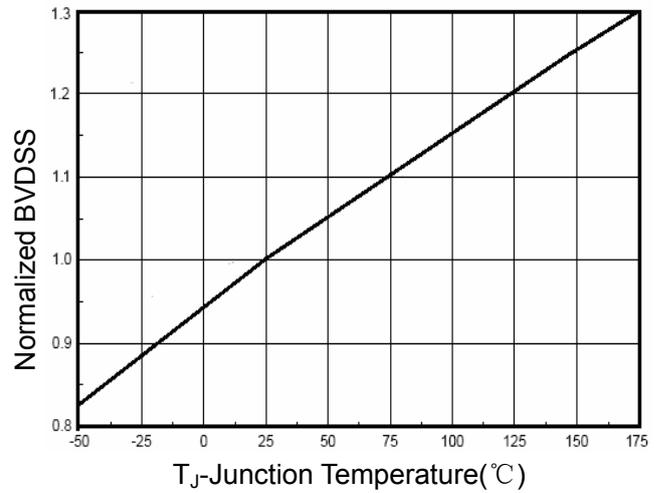


Figure 9 BV_{DSS} vs Junction Temperature

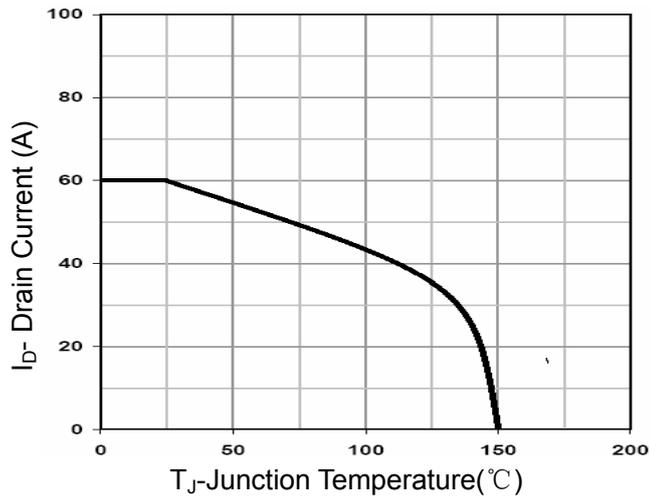


Figure 8 Current De-rating

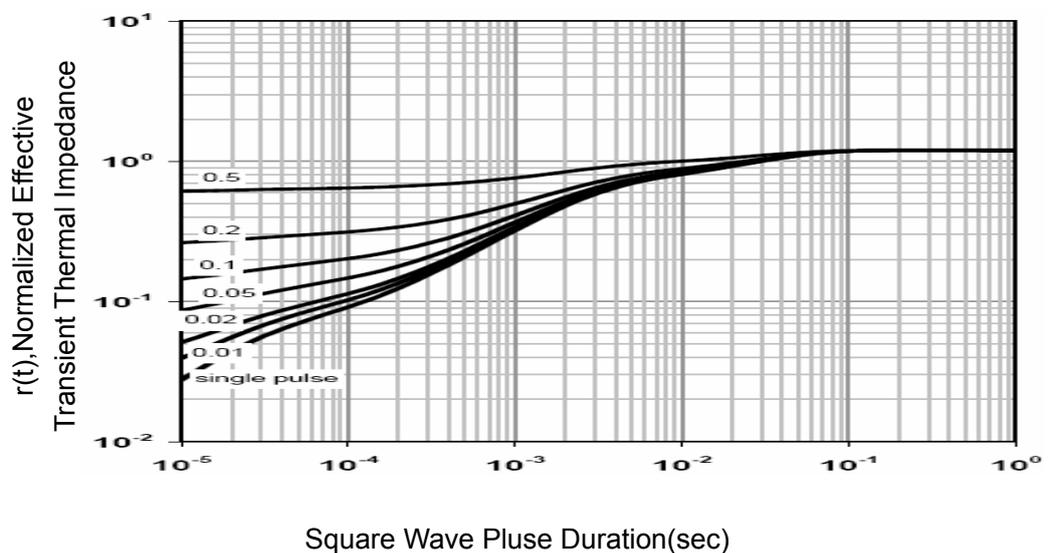
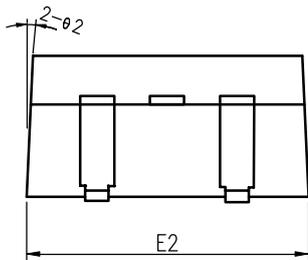
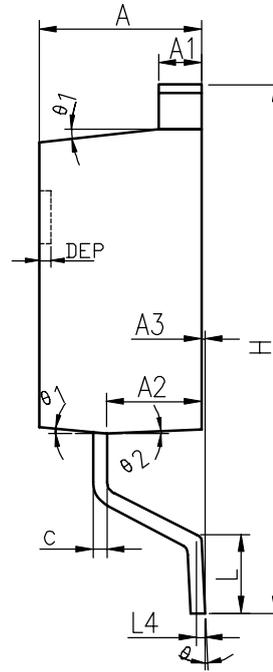
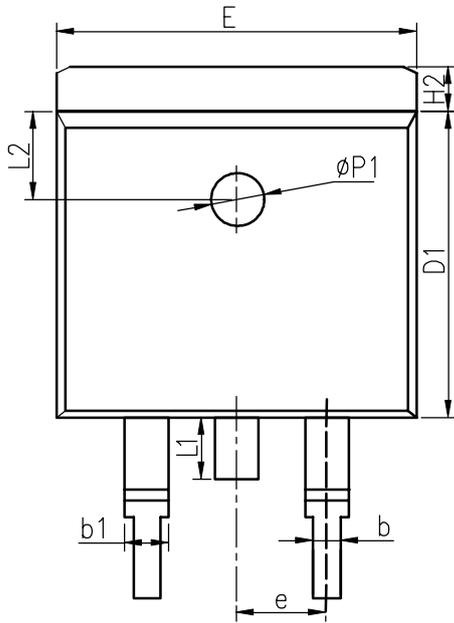


Figure 10 Normalized Maximum Transient Thermal Impedance

TO-263-2L



COMMON DIMENSIONS

SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.22	1.27	1.32	0.048	0.050	0.052
A2	2.59	2.69	2.79	0.102	0.106	0.110
A3	0.00	0.10	0.20	0.000	0.004	0.008
b	0.77	0.813	0.90	0.030	0.032	0.035
b1	1.20	1.270	1.36	0.047	0.050	0.054
c	0.34	0.381	0.47	0.013	0.015	0.019
D1	8.60	8.70	8.80	0.339	0.343	0.346
E	10.00	10.16	10.26	0.394	0.400	0.404
E2	10.00	10.10	10.20	0.394	0.398	0.402
e	2.54 BSC			0.100 BSC		
H	14.70	15.10	15.50	0.579	0.594	0.610
H2	1.17	1.27	1.40	0.046	0.050	0.055
L	2.00	2.30	2.60	0.079	0.091	0.102
L1	1.45	1.55	1.70	0.057	0.061	0.067
L2	2.50 REF			0.098 REF		
L4	0.25 BSC			0.010 BSC		
	0°	5°	8°	0°	5°	8°
1	5°	7°	9°	5°	7°	9°
2	1°	3°	5°	1°	3°	5°
$\phi P1$	1.40	1.50	1.60	0.055	0.059	0.063
DEP	0.05	0.10	0.20	0.002	0.004	0.008

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