

# 1200V/100A SiC HALF BRIDGE

4804

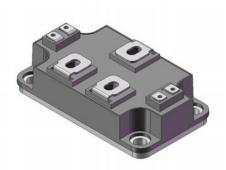
(315) 701-6751

M.S.KENNEDY CORP.

4707 Dey Road Liverpool, N.Y. 13088

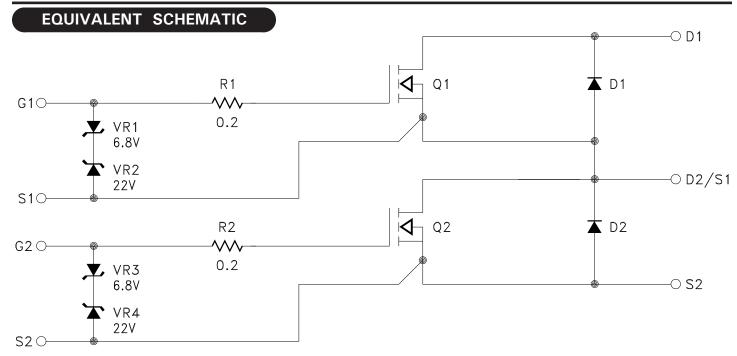
#### **FEATURES:**

- · Half Bridge Configuration
- · Silicon Carbide Mosfet Provides Ultra Fast Switching
- · Silicon Carbide Diode Provides Near Zero Recovery
- 1200V Rated Voltage
- 100A Continuous Output Current
- · Internal Zener Clamps on Gates
- · HI-REL Screening Available (Modified 38534)
- · Light Weight Domed AISiC Baseplate
- · Robust Mechanical Design for Hi-Rel Applications
- · Ultra-Low Inductance Internal Layout
- Withstands 96 Hours HAST and Thermal Cycling (-55°C to +125°C)
- · Contact MSK for MIL-PRF-38534 (Modified) Qualification Status



#### **DESCRIPTION:**

The MSK 4804 is one of a family of plastic encapsulated modules (PEM) developed specifically for use in military, aerospace and other severe environment applications. The SiC(Silicon Carbide) technology has superior switching performance compared to Si-Based modules. The half bridge configuration and 1200V/100A rating make it ideal for use in high current motor drive and inverter applications. The Aluminum Silicon Carbide (AlSiC) baseplate offers superior flatness and light weight; far better than copper or copper alloys found in most high power plastic modules. The high thermal conductivity materials used to construct the MSK 4804 allow high power outputs at elevated baseplate temperatures.



#### TYPICAL APPLICATIONS

- Motor Drives
- Inverters

### **ABSOLUTE MAXIMUM RATING**



VDS	Collector to Emitter Voltage	Tst	Storage Temperature Range55°C to +125°C
Vgs	Gate to Emitter Voltage + 25/-10V	TJ	Junction Temperature
Iout	Current (Continuous)	Tc	Case Operating Temperature Range
IOUTP	Current Pulsed (1mS) 200A		MSK 4804H55°C to +125°C
VCASE	Case Isolation Voltage		MSK 480440°C to +85°C

#### **ELECTRICAL SPECIFICATIONS**

Parameter ⑥		Test Conditions	Group A	MSK 4804 H			MSK 4804			Units
			Subgroup	Min.	Тур.	Max.	Min.	Typ.	Max.	Units
Drain-Source Voltage		ID = 100A, VGS = 20V	1	-	1.44	2.10	-	1.44	2.20	V
			2	-	1.78	2.40	-	-	-	V
			3	-	1.82	2.45	-	-	-	V
Dunia Causa Lank	Lankana Commant	\/pa	1	-	0.006	1	-	0.006	1.2	mA
Drain-Source Leakage Current		VDS = 1000V, VGS = 0V	2	-	0.060	5	-	-	-	mA
			1	1.0	1.66	3.5	0.8	1.6	3.5	V
Gate Threshold Voltage		ID = 1  mA, VDS = VGS	2	1.0	1.19	3.5	-	-	-	V
			3	1.0	2.47	3.5	-	-	-	V
		ID = 100A	1	-	1.88	2.20	-	1.88	2.30	V
Diode Forward Vo	ltage		2	-	2.28	2.60	-	-	-	V
	-		3	-	1.67	2.20	-	-	-	V
Total Gate Charge	1	V = 600V, ID = 100A	4	-	500	570	-	500	590	nC
	V = 600V, ID =	100A, RG = $5\Omega$ , VGs = $-5/ + 20V$	4	-	1.99	3.3	-	1.99	3.4	mJ
E(on) (1)	V = 600V, ID = !	50A, RG = $5\Omega$ , VGs = $-5/+20V$	4	-	1.50	-	-	1.50	-	mJ
E(On) (I)	V = 600V, ID =	100A, RG = $5\Omega$ , VGs = $-5/ + 20V$	5	-	2.08	-	-	-	-	mJ
	V = 600V, ID = !	50A, RG = $5\Omega$ , VGs = $-5/+20V$	5	-	1.66	-	-	-	-	mJ
	V = 600V, ID =	100A, RG = $5\Omega$ , VGs = $-5/+20V$	4	-	1.72	2.4	-	1.72	2.5	mJ
E(off) (1)	V = 600V, ID = !	50A, RG = $5\Omega$ , VGs = $-5/+20V$	4	-	637	-	-	637	-	uJ
E(011) (I)	V = 600V, ID =	100A, RG = $5\Omega$ , VGs = $-5/+20V$	5	-	1.91	-	-	-	-	mJ
	V = 600V, ID = !	50A, RG = $5\Omega$ , VGs = $-5/+20V$	5	-	700	-	-	-	-	uJ
		Is = 100, di/dt = 2200A/uS	4	-	47	-	-	47	-	nS
Diode Reverse Red	Ti (1)	Is = 50, $di/dt = 2200A/uS$	4	-	52	-	-	52	-	nS
Diode neverse nec	covery rime (i)	Is = 100, di/dt = 2200A/uS	5	-	56	-	-	-	-	nS
		Is=50, di/dt=2200A/u\$	5	-	63	-	-	-	-	nS
Diode Reverse Energy ① $\frac{Is = 50,  di/dt}{Is = 100,  di/dt}$		Is = 100, $di/dt = 2200A/uS$	4	-	460	790	-	460	800	uJ
		Is = 50, di/dt = 2200A/uS	4	-	470	-	-	470	-	uJ
		Is = 100, di/dt = 2200A/uS	5	-	776	_	-	-	-	uJ
		Is = 50, di/dt = 2200A/u\$	5	-	824	-	-	-	-	uJ
Thermal Resistance ①		MOSFET @ TJ = 125°C	-	-	0.160	0.190	-	0.160	0.190	°C/W
		DIODE @ TJ=125°C	-	-	0.350	0.370	-	0.350	0.370	°C/W

#### **NOTES:**

- ① Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only.
- 2 Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
  3 HI-REL grade devices ("H" suffix) shall be 100% tested to subgroups 1, 2 and sample tested to subgroup 3.
  4 Subgroup 4 testing available upon request.
  5 Subgroup 1, 4 TA = +25°C
- - 2, 5 TA = +125 °C
  - TA = -55°C
- 6 All specifications apply to both the upper and lower sections of the half bridge. 7 VGs = 20V unless otherwise specified.
- 8 Continuous operation at or above absolute maximum ratings may adversly effect the device performance and/or life cycle.

#### APPLICATION NOTES

#### THERMAL CALCULATIONS

Power dissipation and maximum allowable temperature rise involve many variables working together. Drain current, PWM duty cycle and switching frequency all factor into power dissipation. DC losses or "ON-TIME" losses are simply VDs x Drain Current x PWM duty cycle. For the MSK 4804, VDs = 2.4V max., and at 100 amps and a PWM duty cycle of 30%, DC losses equal 240 watts. Switching losses vary proportionally with switching frequency. The MSK 4804 typical switching losses at VDs = 600V and IDs = 100A are about 3.99mJ, which is simply the sum of the turn-on switching loss and the turn-off switching loss. Multiplying the switching frequency times the switching losses will result in a power dissipation number for switching. The MSK 4804, at 5KHz, will exhibit switching power dissipation of 19.95 watts. The total losses are the sum of DC losses plus switching losses, or in this case, 91.95 watts total.

91.95 watts x 0.190°C/W thermal resistance equals 17.47 degrees of temperature rise between the case and the junction. Subtracting 17.47°C from the maximum junction temperature of 150°C equals 132.53°C maximum case temperature for this example.

VDS x ID x PWM duty cycle = 2.4V x 100 amps x 30% = 72 watts DC losses

Turn-on switching loss + Turn-off switching loss = Total switching losses = 2.08mJ + 1.91mJ = 3.99mJ

Total switching loss x PWM frequency = Total switching power dissipation = 3.99mJ x 5KHz = 19.95 watts

Total power dissipation = DC losses + switching losses = 72W + 19.95W = 91.95 watts

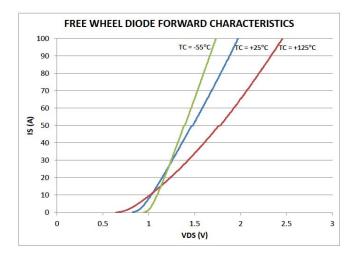
Junction temperature rise above case = Total power dissipation x thermal resistance

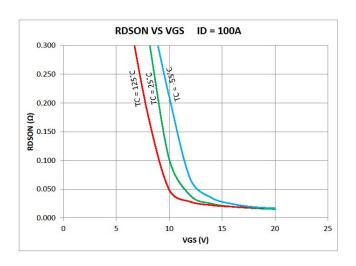
 $91.95 \text{ watts x } 0.190 ^{\circ}\text{C/W} = 17.47 ^{\circ}\text{C}$  temperature rise above case

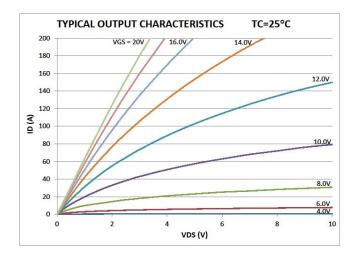
Maximum junction temperature - junction temperature rise = maximum baseplate temperature

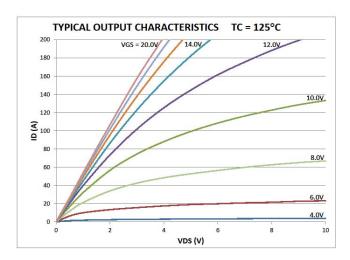
 $150^{\circ}\text{C} - 17.47^{\circ}\text{C} = 132.53^{\circ}\text{C}$ 

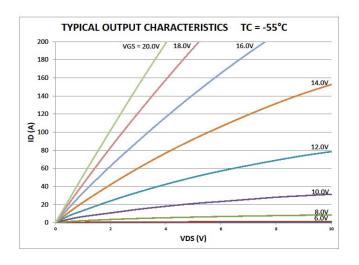
## **TYPICAL PERFORMANCE CURVES**









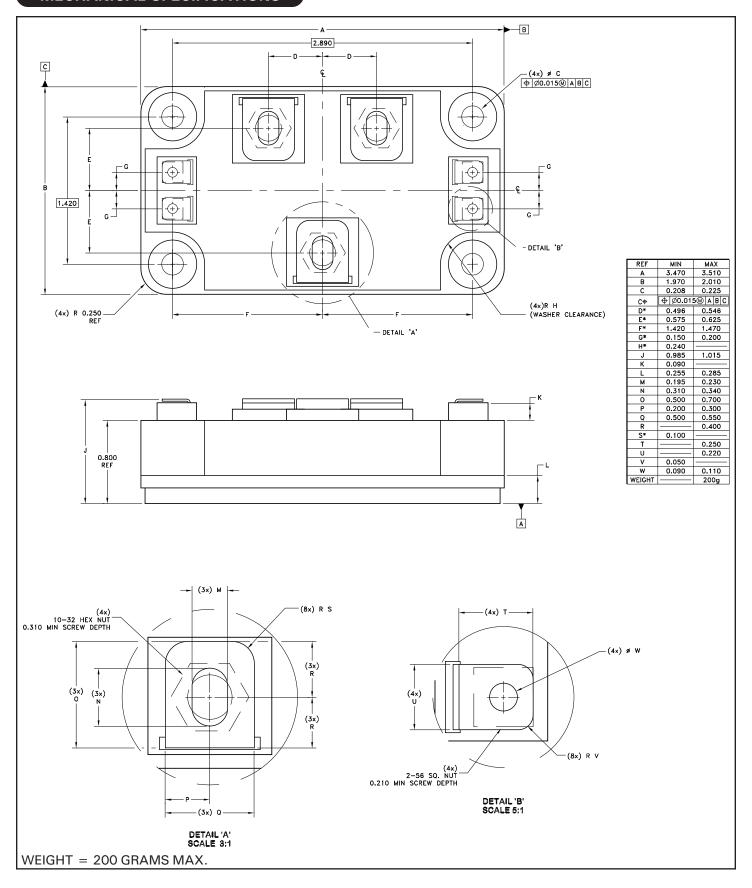


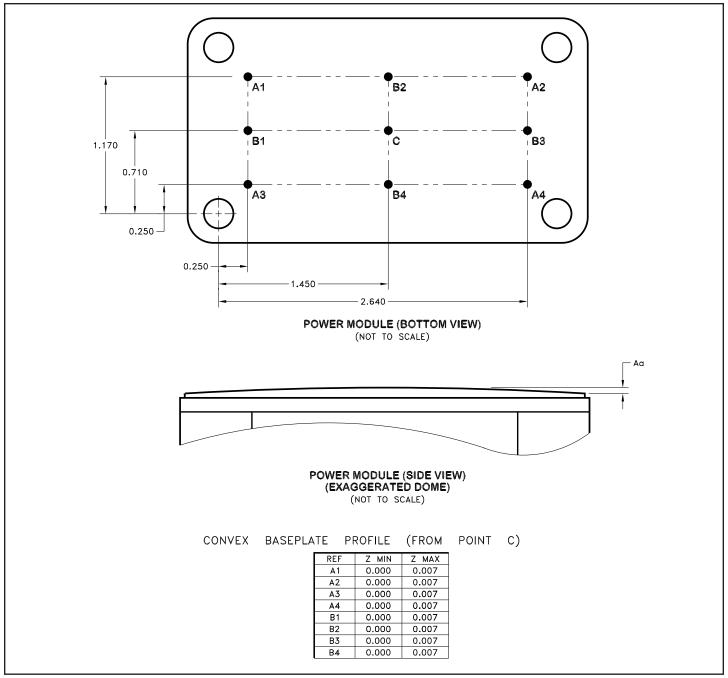
## **SCREENING CHART**

OPERATION	INDUSTRIAL	H SUFFIX
QUALIFICATION (MODIFIED)	NO	YES
ELEMENT EVALUATION	NO	YES
CLEAN ROOM PROCESSING	YES	YES
NON DESTRUCT BOND PULL SAMPLE	YES	YES
CERTIFIED OPERATORS	NO	YES
MIL LINE PROCESSING	YES	YES
MAX REWORK SPECIFIED	NO	YES
PRE-CAP VISUAL	YES - INDUSTRIAL	YES - CLASS H
TEMP CYCLE (-55°C TO +125°C)	NO	YES
BURN-IN	NO	YES - 160 HOURS
ELECTRICAL TESTING	YES - 25°C	YES - FULL TEMP
EXTERNAL VISUAL	YES - SAMPLE	YES
XRAY	NO	NO
PIN FINISH	NI	NI

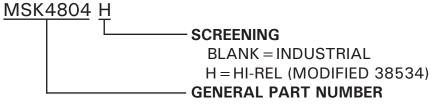
NOTE: ADDITIONAL SCREENING IS AVAILABLE SUCH AS XRAY, CSAM, MECHANICAL SHOCK, ETC. CONTACT FACTORY FOR QUAL STATUS.

## **MECHANICAL SPECIFICATIONS**





## ORDERING INFORMATION



THE ABOVE EXAMPLE IS A MILITARY SCREENED MODULE.

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