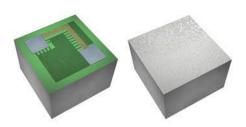


# **Thin Film, Top-Contact Miniature Resistor**



Product may not be to scale

The MSFM series of single-value precision resistors chips offer a small size, wide ohmic value range and high power rating. The MSFM tantalum nitride resistor material offers excellent resistance to high moisture environments. The MSFMs are manufactured using Vishay Electro-Films (EFI) sophisticated thin-film equipment and manufacturing technology. The MSFMs are 100 % electrically tested and visually inspected to MIL-STD-883, method 2032, class H or K

#### **FEATURES**

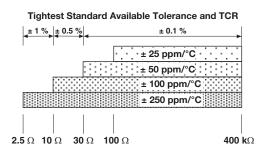
- Wire bondable
- Small size: 0.015" square
- Case: 0101
- Wide value range: 2.5  $\Omega$  to 400 k $\Omega$
- DC power rating up to 125 mW
- Self passivating tantalum nitride film
- · Oxidized silicon substrate
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>



### **APPLICATIONS**

Vishay EFI MSFM top-contact 0.015" square resistor chips are designed for hybrid (chip and wire) assemblies. They are ideally suited for compact designs due to their ultra small form factor.

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES		
PARAMETER	VALUE	UNIT
Total Resistance Range	2.5 to 400K	Ω
Standard Tolerances	± 0.1, ± 0.5, ± 1	%
TCR	± 25, ± 50, ± 100, ± 250	ppm/°C

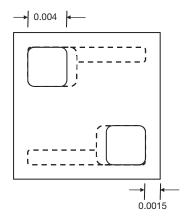


STANDARD ELECTRICAL SPECIFICATIONS		
PARAMETER	VALUE	UNIT
Noise, MIL-STD-202, Method 308 $>$ 100 $\Omega$ $<$ 100 $\Omega$	-35 -20	dB
Moisture Resistance, MIL-STD-202 Method 106	± 0.5 max. Δ <i>R/R</i>	%
Stability, 1000 h, +70 °C, 125 mW	± 0.5 max. Δ <i>R/R</i>	%
Operating Temperature Range	-55 to + 125	°C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	± 0.2 max. Δ <i>R</i> / <i>R</i>	%
High Temperature Exposure, +150 °C, 100 h	± 0.5 max. Δ <i>R/R</i>	%
Dielectric Voltage Breakdown	200	V
Insulation Resistance	10 <sup>12</sup> min.	Ω
Operating Voltage	100 max.	V
DC Power Rating up to +70 °C (Linear de-erated to zero at +175 °C)	0.125	W
5x Rated Power Short-Time Overload, +25 °C, 5 s	± 0.25 max. Δ <i>R/R</i>	%

Revision: 26-Jan-15 **1** Document Number: 61096 For technical questions, contact: efi@vishay.com

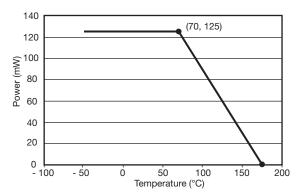


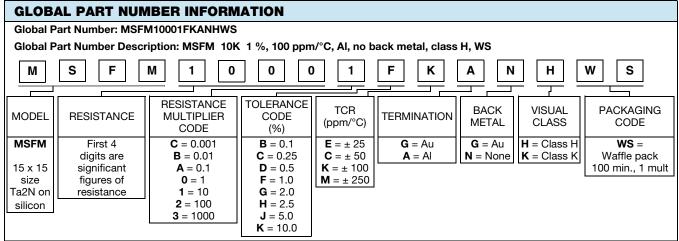
### **DIMENSIONS** in inches



MECHANICAL SPECIFICATIONS	
PARAMETER	VALUE
Chip Size	0.015" x 0.015" ± 0.002" (0.381 mm x 0.381 mm ± 0.05 mm)
Chip Thickness	0.010" ± 0.002" (0.254 mm ± 0.05 mm)
Chip Substrate Material	Oxidized silicon, 10 kÅ minimum oxide
Resistor Material	Tantalum nitride, self-passivating
Bonding Pad Size	0.004" x 0.004" (0.1 mm x 0.1 mm)
Number of Pads	2
Pad Material	10 kÅ minimum aluminum (Au optional)
Backing	None, lapped silicon

#### **POWER DERATING CURVE**







## **Legal Disclaimer Notice**

Vishay

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Revision: 02-Oct-12 Document Number: 91000