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#### **MSC1600M**

# RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

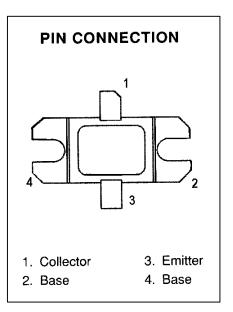
#### **Features**

- 1090 MHz
- 25:1 VSWR CAPABILITY
- P<sub>OUT</sub> = 600 WATTS
- $G_P = 6.0 \text{ dB MINIMUM}$
- GOLD METALIZATION
- INPUT MATCHING
- COMMON BASE CONFIGURATION

# .400 x .500 2LFL (M216) hermetically sealed

#### **DESCRIPTION:**

The MSC1600M is a high power pulsed transistor specifically designed for IFF avionics applications. It is designed for operation under short pulse width and low duty cycle pulse conditions and is capable of withstanding a minimum 25:1 load mismatch at rated RF conditions. Internal impedance matching and gold metallization ensure high product reliability and consistency.



## ABSOLUTE MAXIMUM RATINGS (Tcase = 25°C)

Symbol	Parameter	Value	Unit
P <sub>DISS</sub>	Power Dissipation* (T <sub>C</sub> ≤ 100°C)	1670	W
Ic	Device Current*	43	Α
V <sub>cc</sub>	Collector - Base Voltage*	55	V
TJ	Junction Temperature	+250	°C
T <sub>STG</sub>	Storage Temperature	-65 to +200	°C

#### **Thermal Data**

R <sub>TH(J-C)</sub>	Junction-case Thermal Resistance*	0.09	°C/W
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<sup>\*</sup>Applies only to rated RF amplifier operation



# **ELECTRICAL SPECIFICATIONS (Tcase = 25°C) STATIC**

Symbol	Test Conditions			Value		
Syllibol		rest conditions		Typ.	Typ. Max.	Unit
BV <sub>CBO</sub>	I <sub>C</sub> = 25mA	I <sub>E</sub> = 0mA	65			V
BV <sub>EBO</sub>	$I_E = 5mA$	$I_C = 0mA$	3.5			V
BV <sub>CER</sub>	$I_C = 50mA$	$R_{BE} = 10\Omega$	65			V
I <sub>CES</sub>	$V_{CB} = 50V$				60	mA
h <sub>FE</sub>	V <sub>CE</sub> = 5V	I <sub>C</sub> = 2A	10		250	

#### **DYNAMIC**

Symbol	Test Conditions			Value			
		rest Conditi	Ulis	Min.	Min. Typ. M	Max.	Unit
P <sub>out</sub>	f = 1090 MHz	$P_{IN} = 150 \text{ W}$	$V_{CC} = 50 \text{ V}$	600	700		W
ης	f = 1090 MHz	P <sub>IN</sub> = 150 W	V <sub>CC</sub> = 50 V	35	40		%
G₽	f = 1090 MHz	P <sub>IN</sub> = 150 W	V <sub>CC</sub> = 50 V	6.0	6.7		dB
Conditions	Pulse Width =	10uS Duty Cv	cle = 1%				



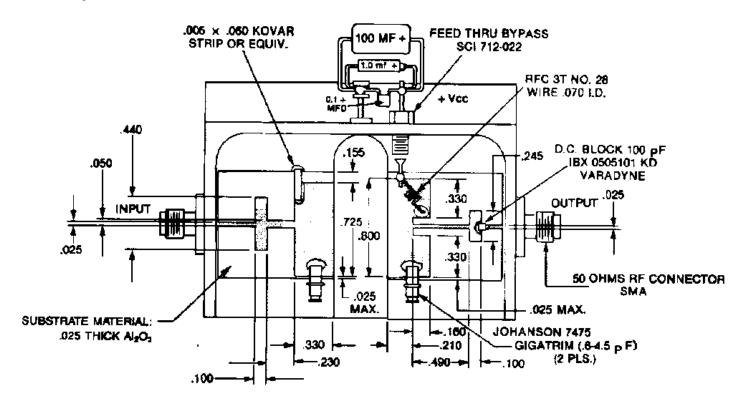
#### **IMPEDANCE DATA:**

FREQ	$Z_{IN}(\Omega)$	$Z_{\mathtt{CL}}\!(\Omega)$
1025 MHz	3.7 + j4.5	0.9 - j1.6
1090 MHz	4.3 + j1.6	1.0 - j2.3
1150 MHz	2.8 + j1.6	0.8 - j2.0

 $P_{IN} = 150 \text{ W}$   $V_{CC} = 50 \text{ V}$ 

#### **TEST CIRCUIT**

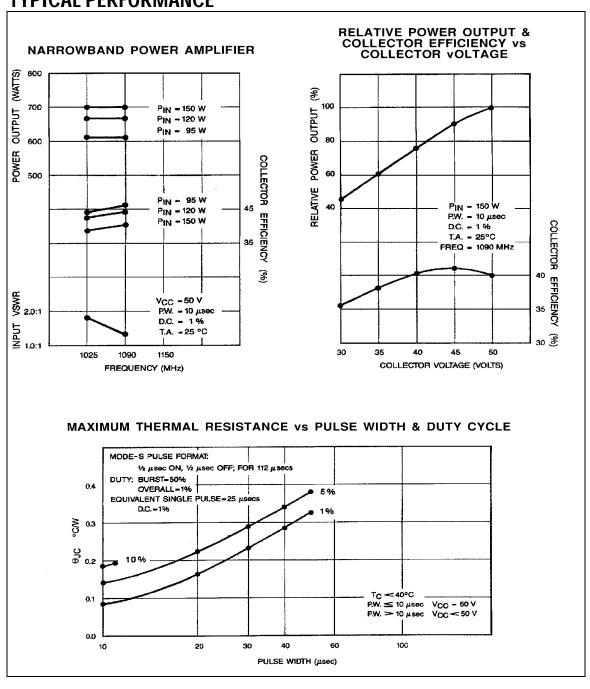
Ref.: Dwg. No. C125410



All dimensions are in inches.



#### **TYPICAL PERFORMANCE**





#### PACKAGE MECHANICAL DATA

