

N-Channel 100-V (D-S) MOSFET

Description

The MSB90N10 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-263 package is universally preferred for all commercial-industrial applications

Features

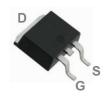
- · Low rDS(on) trench technology
- · Low thermal impedance
- · Fast switching speed
- · RoHS compliant package

Application

- · White LED boost converters
- · Automotive Systems
- · Industrial DC/DC Conversion Circuits

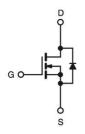
Packing & Order Information

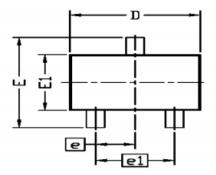
3,000/Reel

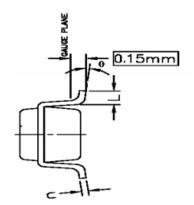


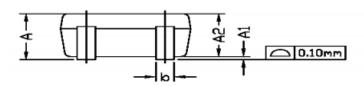
RoHS COMPLIANT

Graphic symbol









SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SIMBULS	MIN	NOM	MAX	MIN	NOM	MAX	
A			1.10			0.043	
A1	0.00		0.10	0.00		0.004	
A2	0.7	0.9	1.00	0.028	0.035	0.039	
ь	0.15		0.30	0.006		0.012	
C	0.08		0.22	0.003		0.009	
D	1.85	2.10	2,15	0.073	0.083	0.085	
Е	1.80	2.30	2.40	0.071	0.091	0.094	
e		0.65 BSC			0.026 BSC		
el	1.30 BSC			0.051 BSC			
E1	1.1	1.30	1.4	0.043	0.051	0.055	
L	0.26	0.36	0.46	0.010	0.014	0.018	
θ	0°	4°	8°	0°	4°	8°	



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (T _A =25°C unless otherwise specified)						
Symbol	Parameter	Value	Unit			
V_{DS}	Drain-Source Voltage	100	V			
V_{GS}	Gate-Source Voltage	±20	V			
I _D	Continuous Drain Current ^a (T _C =25°C)	90	Α			
	Continuous Drain Current _a (T _C =70°C)	90	Α			
I _{DM}	Pulsed Drain Current ^b	360	Α			
I _S	Continuous Source Current (Diode Conduction) ^a	90	А			
P _D	Power Dissipation ^a (T _C =25°C)	300	W			
	Power Dissipation ^a (T _C =70°C)	150	W			
T _J /T _{STG}	Operating Junction and Storage Temperature	-55 to +175	°C			

Thermal Resistance Ratings					
Symbol	Parameter	Maximum	Units		
$R_{\theta JA}$	Maximum Junction-to-Ambient ^a	62.5	°C/W		
$R_{ heta JC}$	Maximum Junction-to-Case	0.5	C/VV		

Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Static						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
V_{GS}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	1			V
I _{GSS}	Gate-Body Leakage	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			1 25	uA
I _{D(on)}	On-State Drain Current ^A	V _{DS} = 5 V, V _{GS} = 10 V	120			Α
R _{DS(on)}	Drain-Source On-Resistance ^A	$V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$ $V_{GS} = 5.5 \text{ V}, I_D = 44 \text{ A}$			7 9	mΩ
g fs	Forward Tranconductance ^A	V _{DS} = 15 V , I _D = 20 A		22		S
V _{SD}	Diode Forward Voltage	I _S = 45 V , V _{GS} = 0 V		1.1		V

Dynamic ^b							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 50 \text{ V}, R_L = 2.5 \Omega,$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$ $I_D = 20 \text{ A}$		30		ns	
t _r	Rise Time			58		ns	
t _{d(off)}	Turn-Off Delay Time			230		ns	
tf	Fall Time			87		ns	



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Dynamic ^b							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
Q_g	Total Gate Charge	$V_{DS} = 50 \text{ V}$, $I_{D} = 20 \text{ A}$ $V_{GS} = 5.5 \text{ V}$		114		nC	
Q_{gs}	Gate-Source Charge			28		nC	
Q_{gd}	Gate-Drain Charge			72		nC	
C _{ISS}	Input Capacitance	$V_{GS} = 0 \text{ V}$, $V_{DS} = 15 \text{ V}$, $f = 1 \text{MHz}$		9235		pF	
Coss	Output Capacitance			811		pF	
C _{RSS}	Reverse Transfer Capacitance			752		pF	

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.



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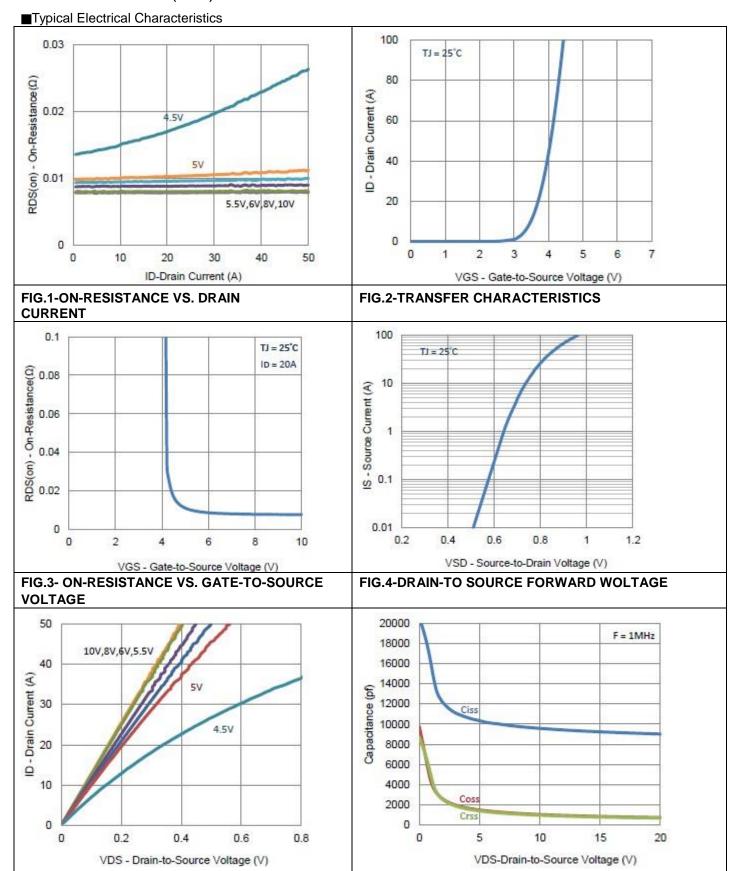


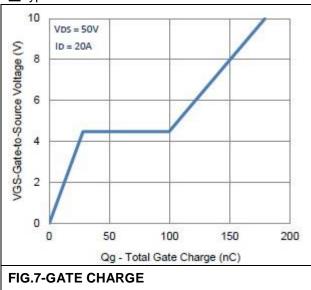
FIG.6-CAPACITANCE

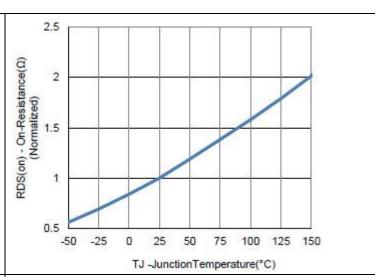
FIG.5-OUTPUT CHARACTERISTICS



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■Typical Electrical Characteristics





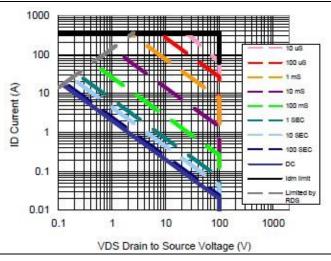


FIG.8-NORMALIZED ON-RESISTANCE VS JUNCTION TEMPERATURE

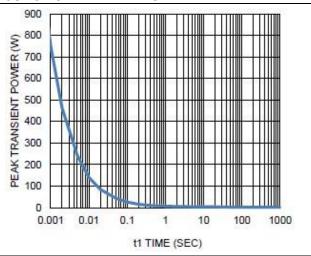


FIG.9-SAFE OPERATING AREA



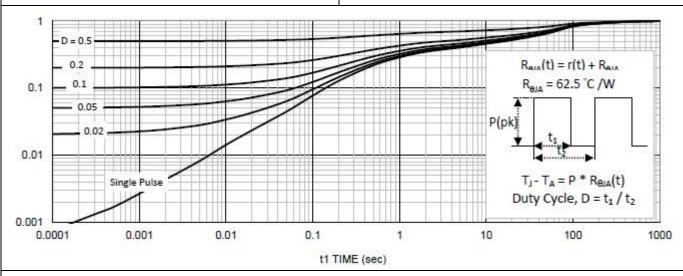


FIG.11-NORMALIZED THERMAL TRANSIENT JUNCTION TO AMBIENT



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