

MS6N90

900V N-Channel MOSFET

Absolute Maximum Ratings (Tc=25°C unless otherwise specified)

Symbol	Parameter	Value	Unit
P _D	Power Dissipation (TC=25°C)	165	W
	- Derate above 25°C	1.4	W/°C
T _J /T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	°C

- Drain current limited by maximum junction temperature

Thermal Resistance Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{θJC}	Junction-to-Case	--	0.75	°C/W
R _{θJA}	Junction-to-Ambient	--	62.5	

On Characteristics

Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
V _{GS}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	3.0	--	5.0	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 3 A	--	1.95	2.4	Ω

Off Characteristics

Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250μA	900	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	--	0.6	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 900 V, V _{GS} = 0 V V _{DS} = 720 V, V _C = 125°C	--	--	10 100	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
C _{ISS}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0MHz	--	1500	--	pF
C _{OSS}	Coss Output Capacitance		--	120	--	pF
C _{RSS}	Crss Reverse Transfer Capacitance		--	12	--	pF

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Switching Characteristics

Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
$t_{d(on)}$	Turn-On Time	$V_{DS} = 450\text{ V}$, $I_D = 6\text{ A}$, $R_G = 25\ \Omega$	--	50	--	ns
t_r	Turn-On Rise Time		--	100	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	50	--	ns
t_f	Turn-Off Fall Time		--	60	--	ns
Q_g	Total Gate Charge	$V_{DS} = 720\text{ V}$, $I_D = 6\text{ A}$, $V_{GS} = 10\text{ V}$	--	33	--	nC
Q_{gs}	Gate-Source Charge		--	10	--	nC
Q_{gd}	Gate-Drain Charge		--	13	--	nC
t_{rr}	Reverse Recovery Time	$I_S = 6\text{ A}$, $V_{GS} = 0\text{ V}$ $diF/dt = 100\text{ A}/\mu\text{s}$	--	0.65	--	ns
Q_{rr}	Reverse Recovery Charge		--	7.0	--	μC

Source-Drain Diode Maximum Ratings and Characteristics

Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
I_S	Continuous Source-Drain Diode Forward Current	$I_S = 6\text{ A}$, $V_{GS} = 0\text{ V}$	--	--	6	A
I_{SM}	ISM Pulsed Source-Drain Diode Forward Current		--	--	24	
V_{SD}	Source-Drain Diode Forward Voltage		--	--	1.4	V

Notes:

1. Repeativity rating : pulse width limited by junction temperature
2. $L = 34.0\text{mH}$, $I_{AS} = 6.0\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 6.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq \text{BVDSS}$, Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
5. Essentially independent of operating temperature.

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■ Characteristics Curve

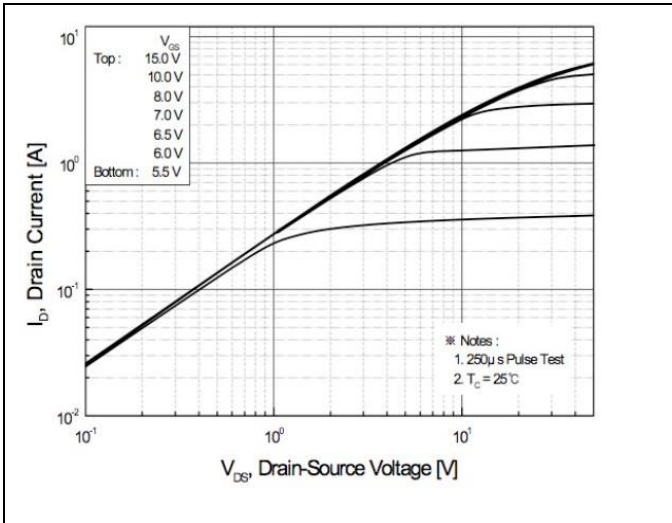


FIG.1-ON REGION CHARACTERISTICS

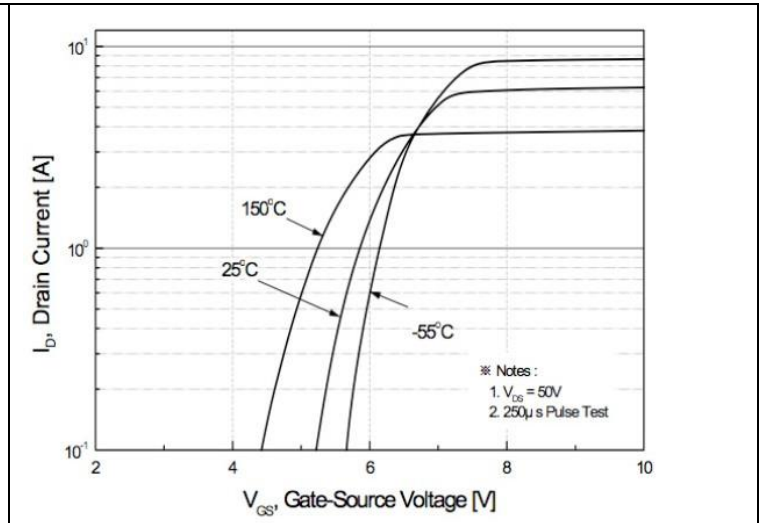


FIG.2-TRANSFER CHARACTERISTICS

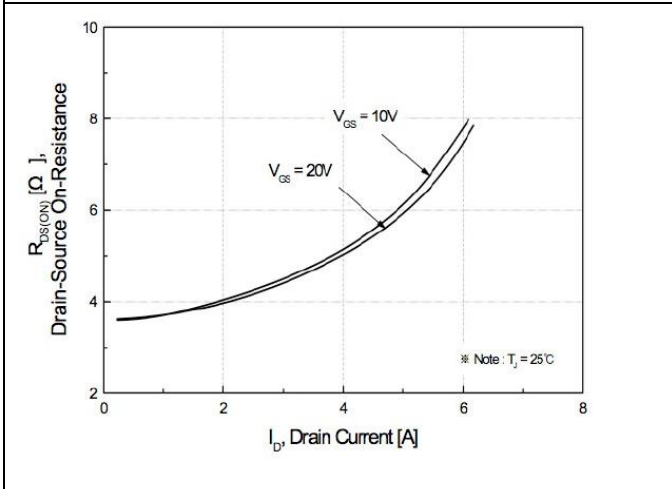


FIG.3-ON RESISTANCE VARIATION VS DRAIN CURRENT AND GATE VOLTAGE

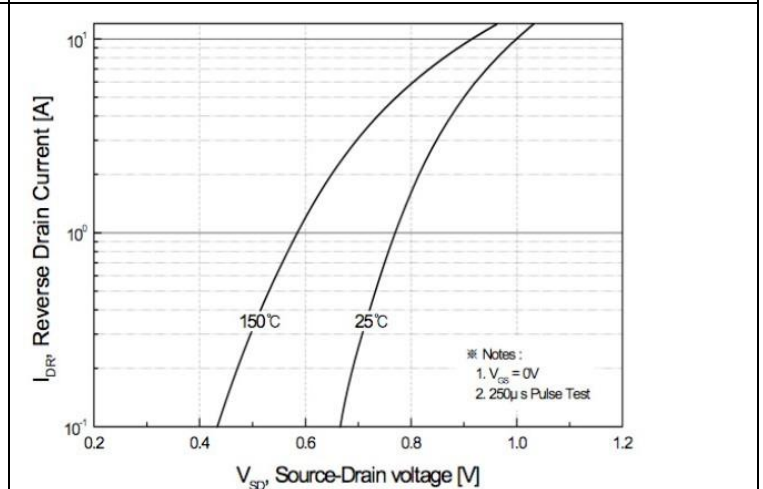


FIG.4-BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE

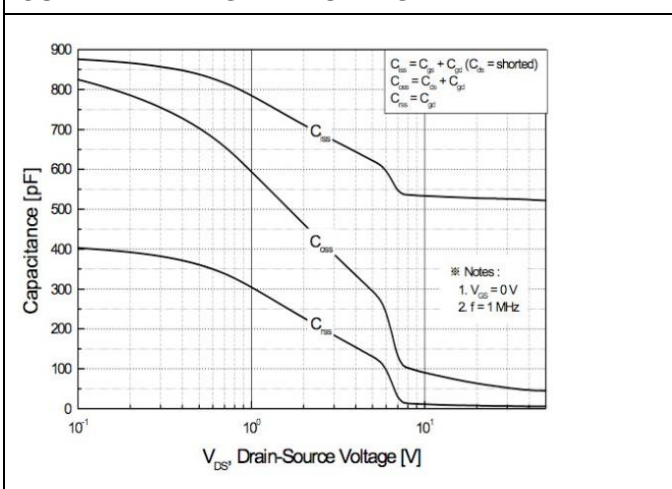


FIG.5-CAPACITANCE CHARACTERISTICS

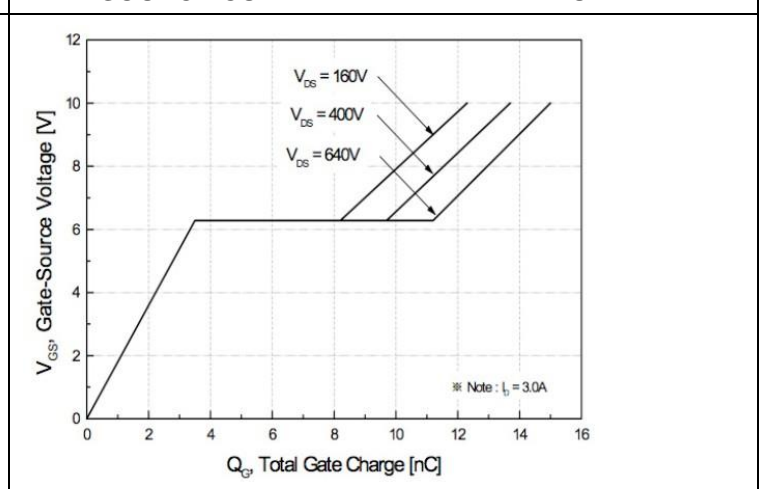


FIG.6-GATE CHARGE CHARACTERISTICS

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■ Characteristics Curve

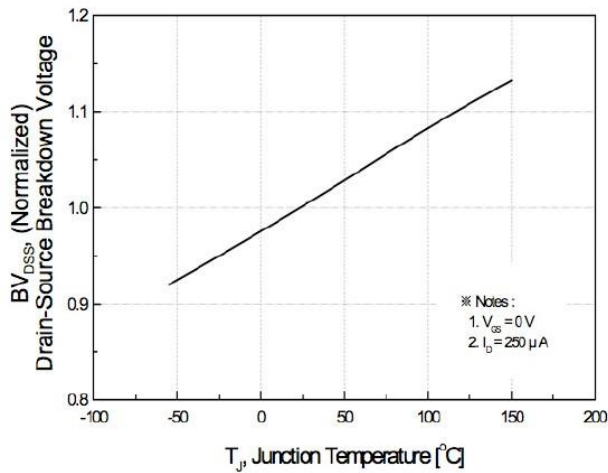


FIG.7-BREAKDOWN VOLTAGE VARIATION VS TEMPERATURE

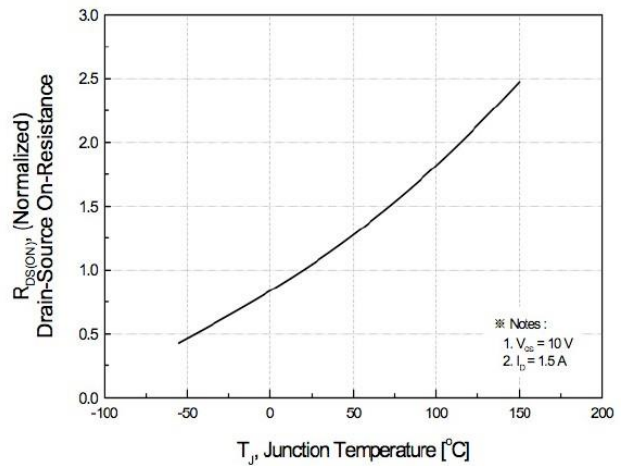


FIG.8-ON-RESISTANCE VARIATION VS TEMPERATURE

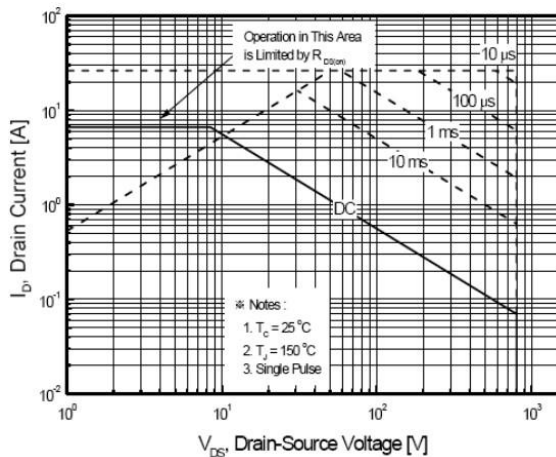


FIG.9-MAXIMUM SAFE OPERATING AREA

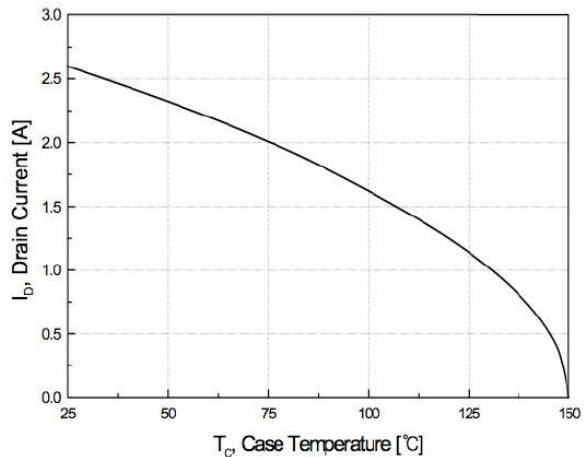


FIG.10-MAXIMUM DRAIN CURRENT VS CASE TEMPERATURE

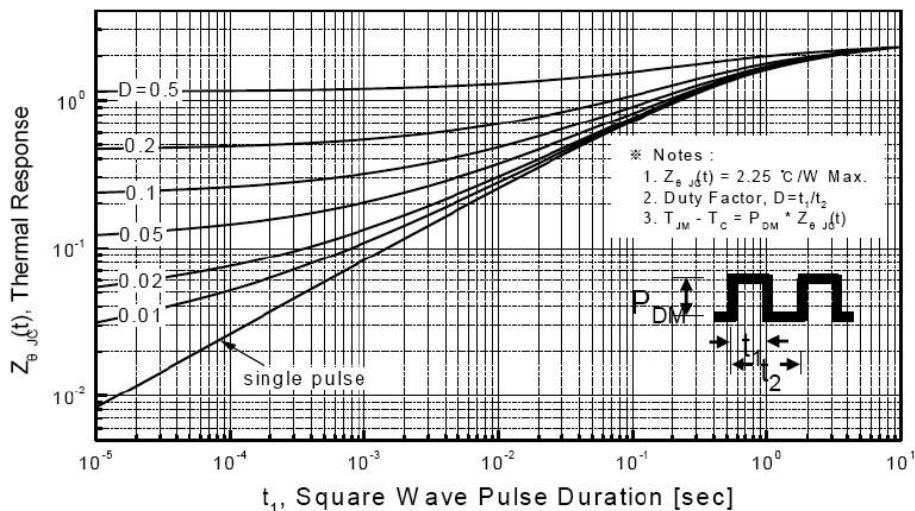


FIG.11-TRANSIENT THERMAL RESPONSE CURVE

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■ Characteristics Test Circuit & Waveform

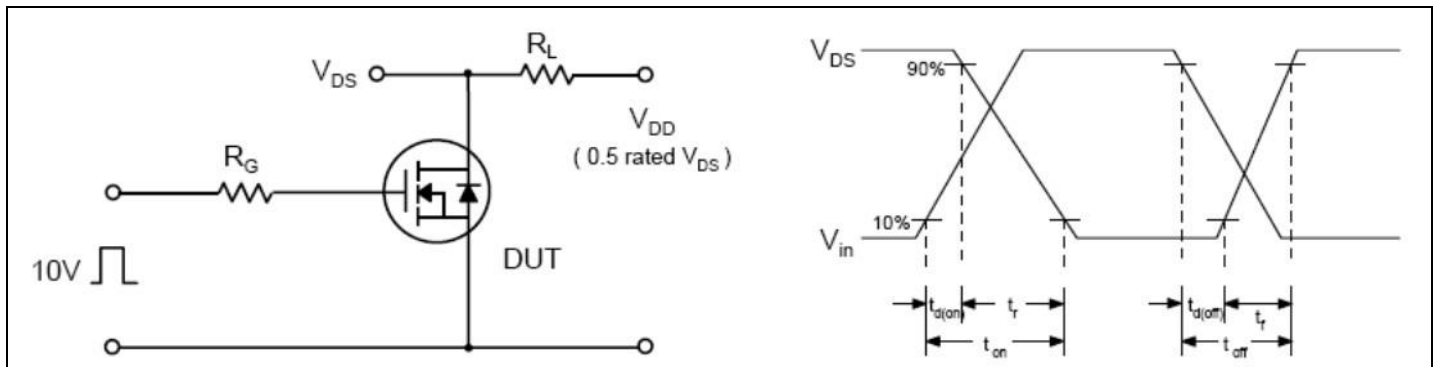


FIG.12-RESISTIVE SWITCHING TEST CIRCUIT & WAVEFORMS

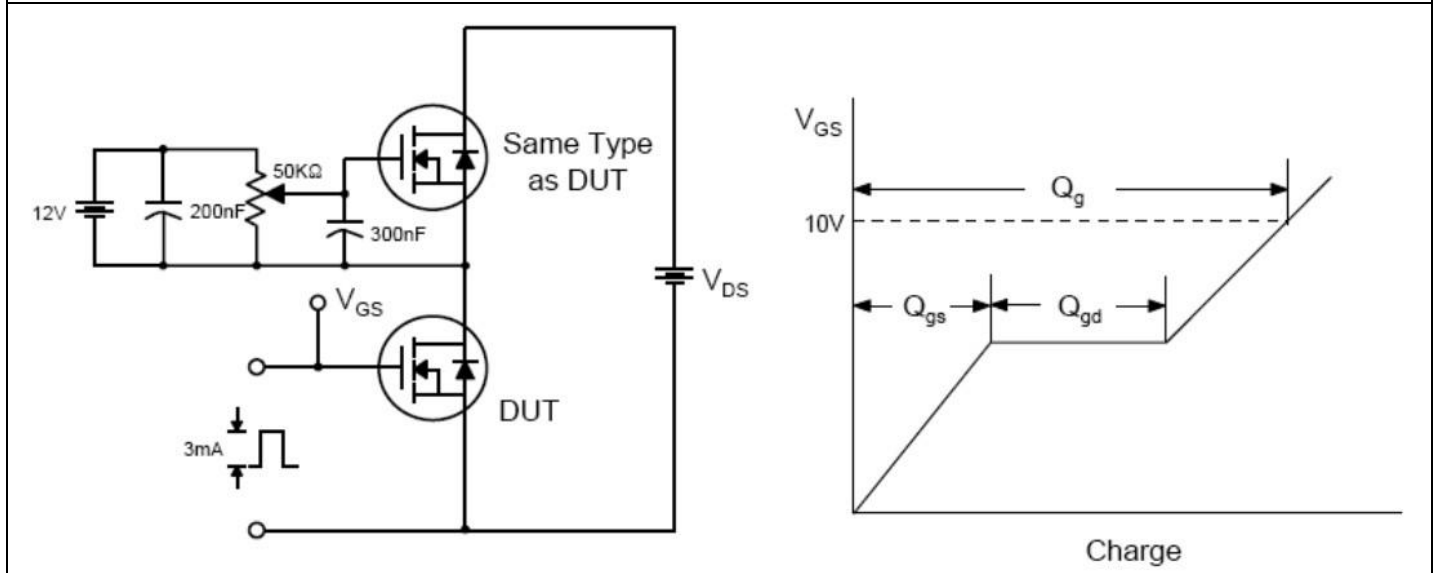


FIG.13-GATE CHARGE TEST CIRCUIT & WAVEFORM

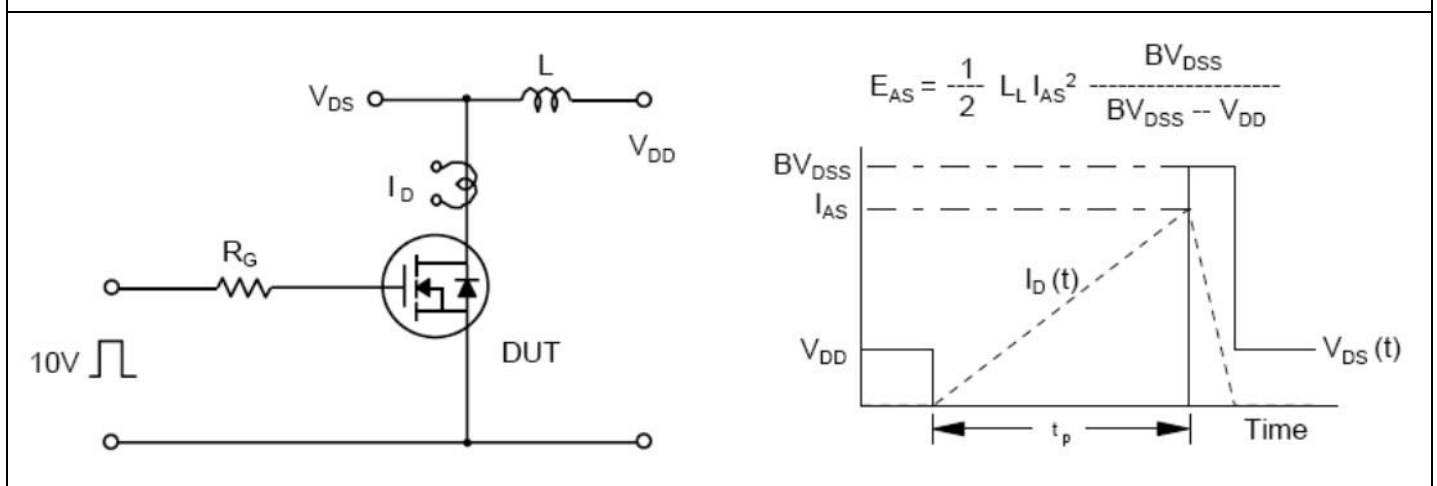


FIG.14-UNCLAMPED LINDUCTIVE SWITCHING TEST CIRCUIT & WAVEFORMS

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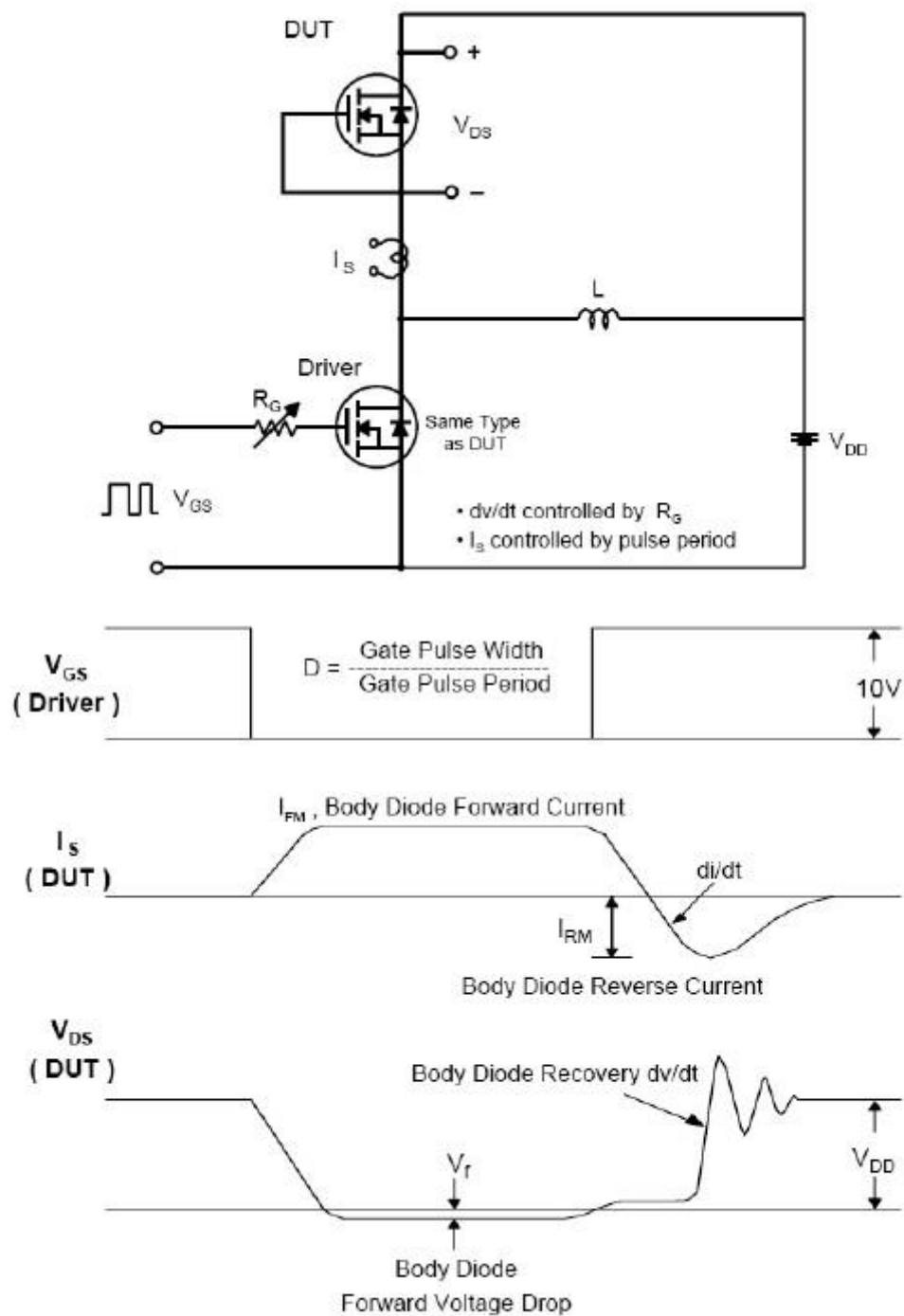


Fig 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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