The RF Line NPN Silicon RF Power Transistor

. . . designed primarily for wideband large—signal output amplifier stages in the 100 to 500 MHz frequency range.

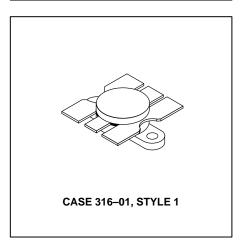
- Guaranteed Performance @ 400 MHz, 28 Vdc
 Output Power = 80 Watts over 225 to 400 MHz Band
 Minimum Gain = 7.3 dB @ 400 MHz
- Built–In Matching Network for Broadband Operation Using Double Match Technique
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR
- Gold Metallization System for High Reliability Applications
- Characterized for 100 to 500 MHz

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	33	Vdc
Collector-Base Voltage	VCBO	60	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous — Peak	lC	9.0 12	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	250 1.43	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

MRF327

80 W, 100 to 500 MHz CONTROLLED "Q" BROADBAND RF POWER TRANSISTOR NPN SILICON



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.7	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			•	•	
Collector–Emitter Breakdown Voltage (I _C = 80 mAdc, I _B = 0)	V(BR)CEO	33	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 80 mAdc, V _{BE} = 0)	V(BR)CES	60	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 8.0 mAdc, I _C = 0)	V(BR)EBO	4.0	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 80 mAdc, I _C = 0)	V _(BR) CBO	60	_	_	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	ІСВО	_	_	5.0	mAdc
ON CHARACTERISTICS					
DC Current Gain (I _C = 4.0 Adc, V _{CE} = 5.0 Vdc)	hFE	20	_	80	_
DYNAMIC CHARACTERISTICS			-		-
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	95	125	pF

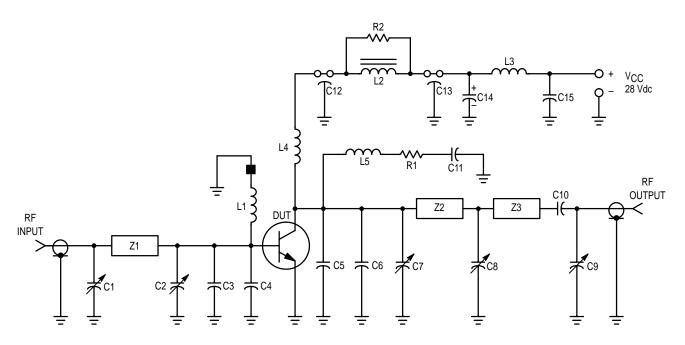
NOTE:

(continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.

ELECTRICAL CHARACTERISTICS – continued (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
FUNCTIONAL TESTS (Figure 1)					
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{Out} = 80 W, f = 400 MHz)	G _{PE}	7.3	9.0	_	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 80 W, f = 400 MHz)	η	50	60	_	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 80 W, f = 400 MHz, VSWR = 30:1 All Phase Angles)	Ψ	No Degradation in Output Power			



C1, C2, C7, C8, C9 — 1.0-20 pF Piston Trimmer (Johanson JMC 5501)

C3, C4 — 36 pF ATC 100 mil Chip Capacitor

C5, C6 — 43 pF ATC 100 mil Chip Capacitor

C10 - 100 pF UNELCO

C11, C15 — 0.1 μF Erie Redcap

C12, C13 — 680 pF Feedthru

C14 — 1.0 µF 50 V Tantalum

L1 — 4 Turns #22 AWG Enameled, 3/16" ID Closewound with Ferroxcube Bead (#56-590-65/4B) on Ground End of Coil

L2 — Ferroxcube VK200-19/4B Ferrite Choke

L3 — 7 Turns #18 AWG, 11/16" Long, Wound on a 100 k Ω 2.0 Watt Resistor

L4 — 6 Turns #20 AWG Enameled, 3/16" ID Closewound

L5 — 4 Turns #22 AWG Enameled, 1/8" ID Closewound

Z1 — Microstrip 0.2" W x 1.5" L

Z2 — Microstrip 0.17" W x 1.16" L Z3 — Microstrip 0.17" W x 0.63" L

R1, R2 — 10 Ω 2.0 Watt

Board — Glass Teflon ε_r = 2.56, t = 0.062"

Input/Output Connectors Type N

DUT Socket Lead Frame Etched from 80-mil-Thick Copper

Figure 1. 400 MHz Test Circuit

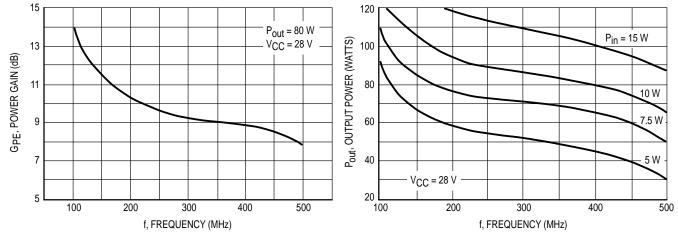


Figure 2. Power Gain versus Frequency

Figure 3. Output Power versus Frequency

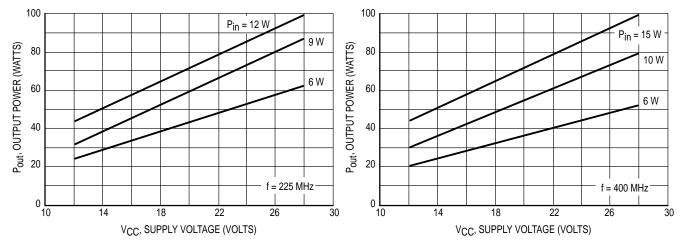


Figure 4. Output Power versus Supply Voltage

Figure 5. Output Power versus Supply Voltage

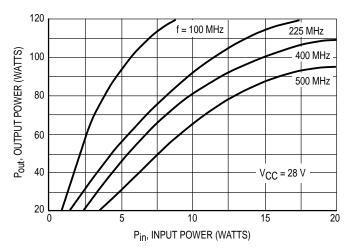
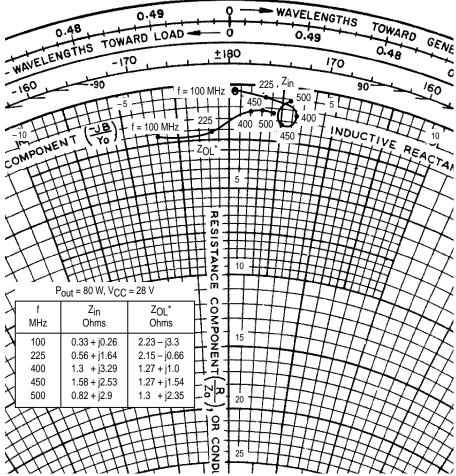


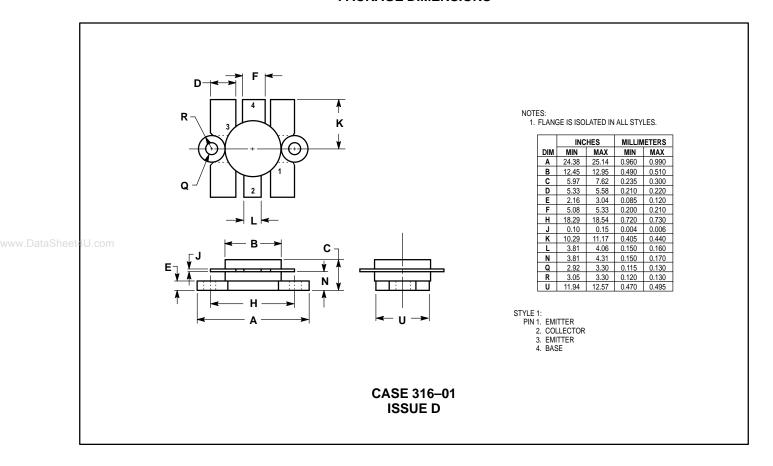
Figure 6. Output Power versus Input Power



 Z_{OL}^* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 7. Series Equivalent Input-Output Impedance

PACKAGE DIMENSIONS



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