The RF Line **NPN Silicon RF Power Transistor**

... designed primarily for wideband large-signal driver and predriver amplifier stages in the 200-500 MHz frequency range.

- Guaranteed Performance at 400 MHz, 28 V Output Power = 20 Watts Power Gain = 10 dB Min Efficiency = 50% Min
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR
- · Gold Metallization System for High Reliability
- Computer–Controlled Wirebonding Gives Consistent Input Impedance

MRF323

20 W, 400 MHz

RF POWER

TRANSISTOR

NPN SILICON

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	33	Vdc
Collector–Base Voltage	VCBO	60	Vdc
Emitter-Base Voltage	VEBO	4.0	Vdc
Collector Current — Continuous — Peak	ΙC	2.2 3.0	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	55 310	Watts mW/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C



THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _{θJC}	3.2	°C/W

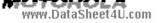
ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Мах	Unit
OFF CHARACTERISTICS			•		
Collector–Emitter Breakdown Voltage ($I_C = 20 \text{ mAdc}, I_B = 0$)	V(BR)CEO	33	-	—	Vdc
Collector–Emitter Breakdown Voltage ($I_C = 20 \text{ mAdc}, V_{BE} = 0$)	V(BR)CES	60	-	—	Vdc
Collector–Base Breakdown Voltage ($I_C = 20 \text{ mAdc}, I_E = 0$)	V(BR)CBO	60	-	—	Vdc
Emitter–Base Breakdown Voltage (I _E = 2.0 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	-	—	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	ІСВО	—	-	2.0	mAdc

DC Current Gain (I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)	hFE	20		80	—
NOTE:					(continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.





ELECTRICAL CHARACTERISTICS — continued ($T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS					
Output Capacitance ($V_{CB} = 28 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C _{ob}	_	20	24	pF
FUNCTIONAL TESTS (Figure 1)		•			
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 20 W, f = 400 MHz)	G _{PE}	10	11	—	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 20 W, f = 400 MHz)	η	50	60	—	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 20 W, f = 400 MHz, VSWR = 30:1 all phase angles)	Ψ	No	Degradation	in Output Pow	ver

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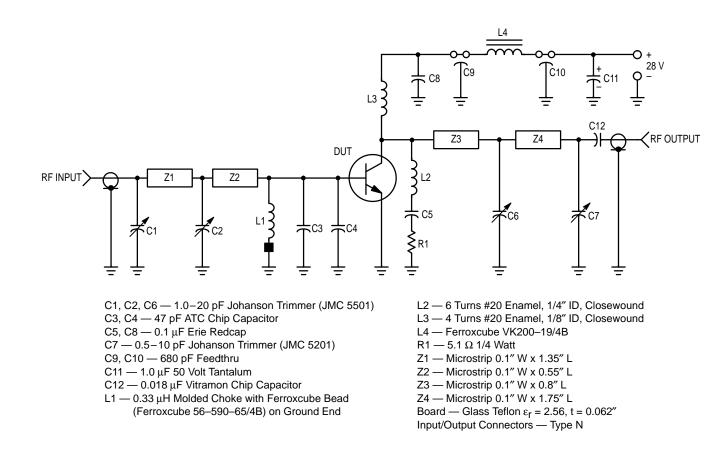


Figure 1. 400 MHz Test Circuit Schematic

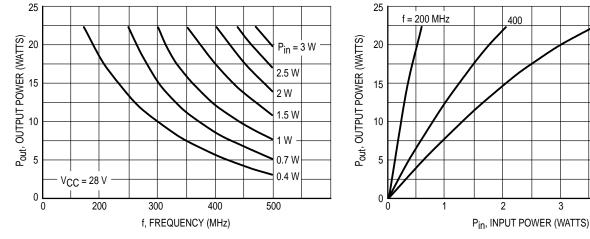


Figure 2. Output Power versus Frequency



500

V_{CC} = 28 V

5

4

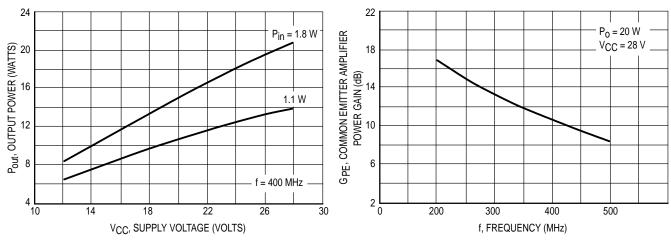
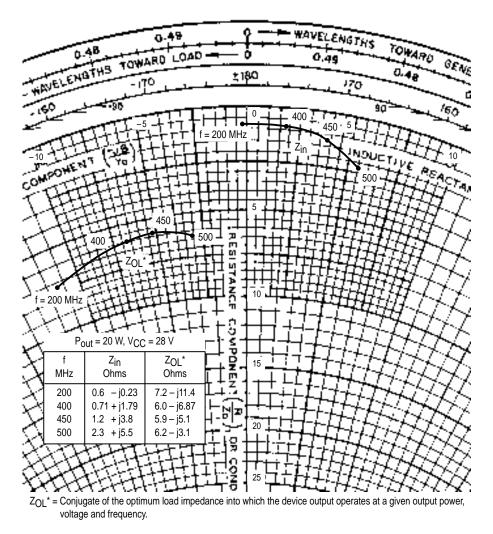


Figure 4. Output Power versus Supply Voltage

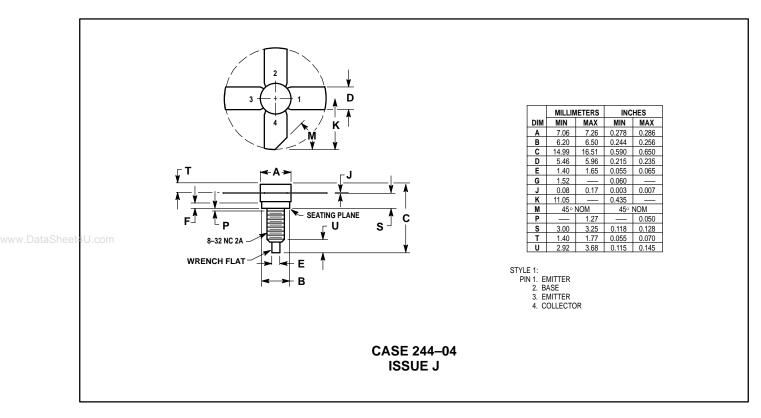
Figure 5. Power Gain versus Frequency



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Figure 6. Series Equivalent Impedance

PACKAGE DIMENSIONS



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