High Volume Sensor for Low Pressure Applications

Freescale Semiconductor has developed a low cost, high volume, miniature pressure sensor package which is ideal as a sub-module component or a disposable unit. The unique concept of the Chip Pak allows great flexibility in system design while allowing an economic solution for the designer. This new chip carrier package uses Freescale Semiconductor's unique sensor die with its piezoresistive technology, along with the added feature of on-chip, thin-film temperature compensation and calibration.

NOTE: Freescale Semiconductor is also offering the Chip Pak package in application-specific configurations, which will have an "SPX" prefix, followed by a four-digit number, unique to the specific customer.

Features:

- · Low Cost
- Integrated Temperature Compensation and Calibration
- Ratiometric to Supply Voltage
- Polysulfone Case Material (Medical, Class V Approved)
- Provided in Easy-to-Use Tape and Reel

Application Examples

- Respiratory Diagnostics
- Air Movement Control
- Controllers
- · Pressure Switching

NOTE: The die and wire bonds are exposed on the front side of the Chip Pak (pressure is applied to the backside of the device). Front side die and wire protection must be provided in the customer's housing. Use caution when handling the devices during all processes.

Freescale Semiconductor's MPXC2011DT1/ MPXC2012DT1 Pressure Sensor has been designed for medical usage by combining the performance of Freescale Semiconductor's shear stress pressure sensor design and the use of biomedically approved materials. Materials with a proven history in medical situations have been chosen to provide a sensor that can be used with confidence in applications, such as invasive blood pressure monitoring. It can be sterilized using ethylene oxide. The portions of the pressure sensor that are required to be biomedically approved are the rigid housing and the gel coating.

The rigid housing is molded from a white, medical grade polysulfone that has passed extensive biological testing including: tissue culture test, rabbit implant, hemolysis, intracutaneous test in rabbits, and system toxicity, USP.

MPXC2011DT1 MPXC2012DT1

Freescale Semiconductor Preferred Device

PRESSURE SENSORS 0 to 75 mmHg (0 to 10 kPa)

CHIP PAK PACKAGE



MPXC2011DT1/MPXC2012DT1 CASE 423A

PIN NUMBER					
1	Gnd	3	Vs		
2	S+	4	S-		

The MPXC2011DT1 contains a silicone dielectric gel which covers the silicon piezoresistive sensing element. The gel is a nontoxic, nonallergenic elastomer system which meets all USP XX Biological Testing Class V requirements. The properties of the gel allow it to transmit pressure uniformly to the diaphragm surface, while isolating the internal electrical connections from the corrosive effects of fluids, such as saline solution. The gel provides electrical isolation sufficient to withstand defibrillation testing, as specified in the proposed Association for the Advancement of Medical Instrumentation (AAMI) Standard for blood pressure transducers. A biomedically approved opaque filler in the gel prevents bright operating room lights from affecting the performance of the sensor.

The MPXC2012DT1 is a no-gel option.

Preferred devices are Freescale Semiconductor recommended choices for future use and best overall value.



MAXIMUM RATINGS(NOTE)

Rating	Symbol	Value	Unit
Maximum Pressure (Backside)	P _{max}	75	kPa
Storage Temperature	T _{stg}	-25 to +85	°C
Operating Temperature	T _A	+15 to +40	°C

NOTE: Exposure beyond the specified limits may cause permanent damage or degradation to the device.

OPERATING CHARACTERISTICS (V_S = 10 Vdc, T_A = 25°C unless otherwise noted, P1 > P2)

Characteristic	Symbol	Min	Тур	Max	Unit
Pressure Range ⁽¹⁾	P _{OP}	0	_	10	kPa
Supply Voltage ⁽²⁾	V _S	_	3	10	Vdc
Supply Current	Io	_	6.0	_	mAdc
Full Scale Span ⁽³⁾	V _{FSS}	24	25	26	mV
Offset ⁽⁴⁾	V _{off}	-1.0	_	1.0	mV
Sensitivity	ΔV/ΔΡ	_	2.5	_	mV/kPa
Linearity ⁽⁵⁾	_	-1.0	_	1.0	%V _{FSS}
Pressure Hysteresis ⁽⁵⁾ (0 to 10 kPa)	_	_	±0.1	_	%V _{FSS}
Temperature Hysteresis ⁽⁵⁾ (+15°C to +40°C)	_	_	±0.1	_	%V _{FSS}
Temperature Effect on Full Scale Span ⁽⁵⁾	TCV _{FSS}	-1.0	_	1.0	%V _{FSS}
Temperature Effect on Offset ⁽⁵⁾	TCV _{off}	-1.0	_	1.0	mV
Input Impedance	Z _{in}	1300	_	2550	Ω
Output Impedance	Z _{out}	1400	_	3000	Ω
Response Time ⁽⁶⁾ (10% to 90%)	t _R	_	1.0	_	ms
Warm-Up	_	_	20	_	ms
Offset Stability ⁽⁷⁾	_	_	±0.5	_	%V _{FSS}

NOTES:

- 1. 1.0 kPa (kiloPascal) equals 0.145 psi.
- 2. Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- 3. Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 4. Offset (Voff) is defined as the output voltage at the minimum rated pressure.
- 5. Accuracy (error budget) consists of the following:
 - Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified pressure range.
 - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure
 - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
 - TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.
 TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
- 6. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 7. Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

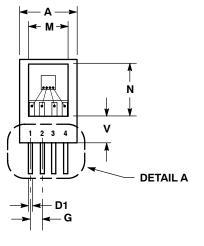
ORDERING INFORMATION

The MPXC2011DT1/MPXC2012DT1 silicon pressure sensors are available in tape and reel.

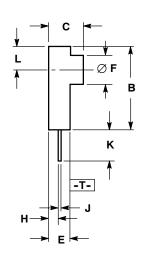
Device Type/Order No.	Case No.	Device Description	Marking
MPXC2011DT1	423A	Chip Pak, 1/3 Gel	Date Code, Lot ID
MPXC2012DT1	423A	Chip Pak, No Gel	Date Code, Lot ID

Packaging Information	Reel Size	Tape Width	Quantity
Tape and Reel	330 mm	24 mm	1000 pc/reel

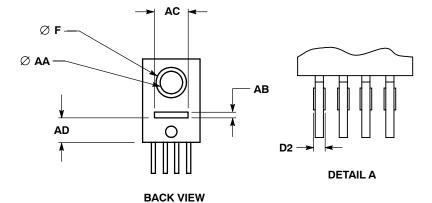
PACKAGE DIMENSIONS



FRONT VIEW



END VIEW



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- NOTES:

 8. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 9. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.240	0.260	6.10	6.60
В	0.350	0.370	8.89	9.40
С	0.140	0.150	3.56	3.81
D1	0.012	0.020	0.30	0.51
D2	0.014	0.022	0.36	0.56
Е	0.088	0.102	2.24	2.59
F	0.123	0.128	3.12	3.25
G	0.045	0.055	1.14	1.40
Н	0.037	0.047	0.94	1.19
J	0.007	0.011	0.18	0.28
K	0.120	0.140	3.05	3.56
L	0.095	0.105	2.41	2.67
M	0.165	0.175	4.19	4.45
N	0.223	0.239	5.66	6.07
٧	0.105	0.115	2.67	2.92
AA	0.095	0.107	2.41	2.72
AB	0.015	0.035	0.38	0.89
AC	0.120	0.175	3.05	4.45
AD	0.100	0.115	2.54	2.92

CASE 423A-03 ISSUE C

MPXC2011DT1 MPXC2012DT1

How to Reach Us:

Home Page:

www.freescale.com

E-mail:

support@freescale.com

USA/Europe or Locations Not Listed:

Freescale Semiconductor Technical Information Center, CH370 1300 N. Alma School Road Chandler, Arizona 85224 +1-800-521-6274 or +1-480-768-2130 support@freescale.com

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) support@freescale.com

Japan:

Freescale Semiconductor Japan Ltd. Technical Information Center 3-20-1, Minami-Azabu, Minato-ku Tokyo 106-0047, Japan 0120 191014 or +81 3 3440 3569 support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd. Technical Information Center 2 Dai King Street Tai Po Industrial Estate Tai Po, N.T., Hong Kong +800 2666 8080 support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217

1-800-441-2447 or 303-675-2140

Fax: 303-675-2150

LDCF or Free scale Semiconductor @hibbert group.com

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