MAXIMUM RATINGS

| Rating | Symbol Value | | Unit | |
|---|----------------------|-------------|-------------|--|
| Collector-Emitter Voltage | VCEO | 45 | Vdc | |
| Collector-Base Voltage | VCBO | 45 | Vdc | |
| Emitter-Base Voltage | VEBO | 4.0 | Vdc | |
| Collector Current — Continuous | IC | 100 | mAdc | |
| Total Device Dissipation @ T _A = 25°C Derate above 25°C | PD | 350 2.8 | mW mW/°C | |
| Operating and Storage Junction Temperature Range | TJ, T _{stg} | -55 to +135 | °C | |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Symbol Max | | |
|---|------------------|------------|------|--|
| Thermal Resistance, Junction to Ambient | R _{0JA} | 357 | °C/W | |

CASE 29-02, STYLE 2 TO-92 (TO-226AA)

MPSH34

IF TRANSISTOR

NPN SILICON

Refer to MPSH24 for graphs.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted.)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|-----------------------|----------|------|------|------|
| OFF CHARACTERISTICS | | | | | |
| Collector-Emitter Breakdown Voltage (I _C = 1.0 mAdc, I _B = 0) | V(BR)CEO | 45 | _ | _ | Vdc |
| Collector-Base Breakdown Voltage ($I_C = 100 \ \mu Adc, I_E = 0$) | V(BR)CBO | 45 | _ | | Vdo |
| Emitter-Base Breakdown Voltage ($I_E = 10 \ \mu Adc, I_C = 0$) | V _{(BR)EBO} | 4.0 | - | - | Vdc |
| Collector Cutoff Current ($V_{CB} = 30 Vdc, I_E = 0$) | Ісво | - | - | 50 | nAdc |
| ON CHARACTERISTICS | | | | | |
| DC Current Gain (I _C = 7.0 mAdc, V _{CE} = 15 Vdc) (I _C = 20 mAdc, V _{CE} = 2.0 Vdc) | hFE | 40 15 | - | - | _ |
| Collector-Emitter Saturation Voltage (IC = 20 mAdc, IB = 2.0 mAdc) | V _{CE} (sat) | - | - | 0.5 | Vdc |
| Base-Emitter On Voltage (I _C = 7.0 mAdc, V _{CE} = 15 Vdc) | V _{BE(on)} | | | 0.95 | Vdc |
| SMALL-SIGNAL CHARACTERISTICS | | | | | |
| Current-Gain — Bandwidth Product ($I_C = 15 \text{ mAdc}, V_{CE} = 15 \text{ Vdc}, f = 100 \text{ MHz}$) | fT | 500 | 720 | - | MHz |
| Collector-Base Capacitance ($V_{CB} = 10 Vdc$, $I_E = 0$, $f = 1.0 MHz$) | C _{cb} | | 0.25 | 0.32 | pF |
| Current-Gain — Bandwidth Ratio (I _C = 15 mAdc to I _C = 20 mAdc, V _{CE} = 15 Vdc) | <u>fτ15</u> fτ20 | - | | 1.6 | - * |