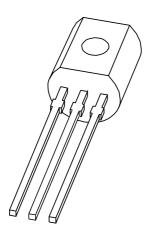
DISCRETE SEMICONDUCTORS

DATA SHEET



MPSA44 NPN high-voltage transistor

Product data sheet Supersedes data of 1999 Apr 27 2004 Oct 11



NXP Semiconductors Product data sheet

NPN high-voltage transistor

MPSA44

FEATURES

- Low current (max. 300 mA)
- High voltage (max. 400 V).

APPLICATIONS

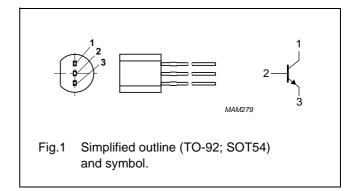
• Telecommunication applications.

DESCRIPTION

NPN high-voltage transistor in a TO-92; SOT54 plastic package.

PINNING

PIN	DESCRIPTION
1	collector
2	base
3	emitter



ORDERING INFORMATION

TYPE NUMBER			
ITPE NUMBER	NAME	DESCRIPTION	VERSION
MPSA44	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	500	V
V _{CEO}	collector-emitter voltage	open base	-	400	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)		_	300	mA
I _{CM}	peak collector current		_	600	mA
I _{BM}	peak base current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	-	625	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	200	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 400 V; I _E = 0 A	Ī-	100	nA
		$V_{CB} = 400 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	-	10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 1 mA	40	_	
		$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA}$	50	200	
		V _{CE} = 10 V; I _C = 50 mA; note 1	45	_	
		V _{CE} = 10 V; I _C = 100 mA; note 1	40	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 1 mA; I _B = 0.1 mA	-	400	mV
		$I_C = 10 \text{ mA}; I_B = 1 \text{ mA}$	_	500	mV
		$I_C = 50 \text{ mA}$; $I_B = 5 \text{ mA}$; note 1	_	750	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 1 mA; note 1	-	850	mV
C _c	collector capacitance	$V_{CB} = 20 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	-	7	pF
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_C = I_C = 0 \text{ A}; f = 1 \text{ MHz}$	_	180	pF
f _T	transition frequency	$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz}$	20	_	MHz

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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NXP Semiconductors Product data sheet

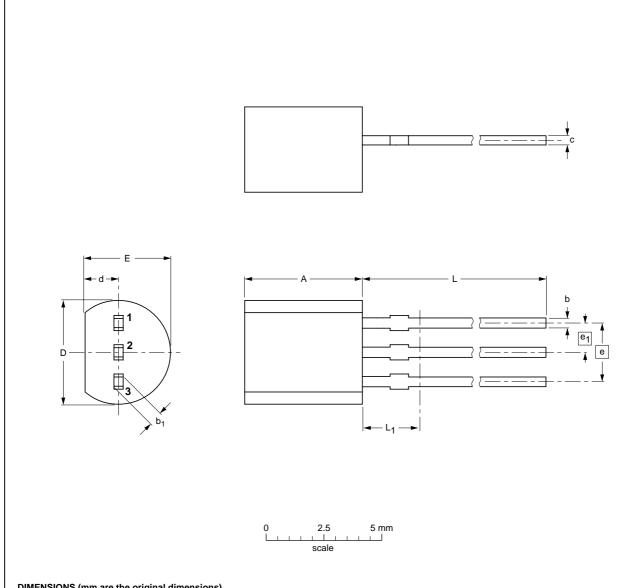
NPN high-voltage transistor

MPSA44

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max.	
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5	

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT54		TO-92	SC-43A			-04-06-28 04-11-16

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NPN high-voltage transistor

MPSA44

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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