

SILICON NPN TRANSISTORS



# Central semiconductor Corp.

www.centralsemi.com

# DESCRIPTION:

The CENTRAL SEMICONDUCTOR MPS6520 and MPS6521 are silicon NPN epitaxial transistors designed for complementary amplifier applications requiring low noise and high DC current gains. The PNP complementary devices are MPS6522 and MPS6523 respectively.

## MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V <sub>CBO</sub>	40	V
Collector-Emitter Voltage	V <sub>CEO</sub>	25	V
Emitter-Base Voltage	V <sub>EBO</sub>	4.0	V
Continuous Collector Current	۱ <sub>C</sub>	100	mA
Power Dissipation	PD	625	mW
Operating and Storage Junction Temperature	TJ, Tstg	-65 to +150	°C

### **ELECTRICAL CHARACTERISTICS**: (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
ICBO	V <sub>CB</sub> =30V			50	nA
I <sub>CBO</sub>	V <sub>CB</sub> =30V, T <sub>A</sub> =60°C			1.0	μΑ
BVCEO	Ι <sub>C</sub> =500μΑ	25			V
BVEBO	Ι <sub>Ε</sub> =10μΑ	4.0			V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =50mA, I <sub>B</sub> =5.0mA			0.5	V
fT	V <sub>CE</sub> =10V, I <sub>C</sub> =2.0mA		300		MHz
fT	V <sub>CE</sub> =10V, I <sub>C</sub> =10mA		400		MHz
Cob	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=100kHz			3.5	pF
NF	V <sub>CE</sub> =5.0V, I <sub>C</sub> =10μA,				
	R <sub>S</sub> =10KΩ, BW=15.7kHz,				
	3.0dB points @ 10Hz and 10kHz			3.0	dB

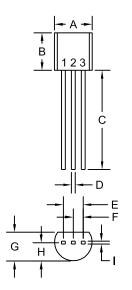
		MPS6520		MPS6521	
SYMBOL	TEST CONDITIONS	MIN	MAX	MIN	MAX
h <sub>FE</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =100µA	100	-	150	-
h <sub>FE</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =2.0mA	200	400	300	600

R1 (11-November 2014)





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# **TO-92 CASE - MECHANICAL OUTLINE**

DIMENSIONS				
	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
A (DIA)	0.175	0.205	4.45	5.21
В	0.170	0.210	4.32	5.33
С	0.500	-	12.70	-
D	0.016	0.022	0.41	0.56
E	0.100		2.54	
F	0.050		1.27	
G	0.125	0.165	3.18	4.19
Н	0.080	0.105	2.03	2.67
	0.015		0.38	

TO-92 (REV: R1)

LEAD CODE:

1) Emitter

2) Base
3) Collector

R1

MARKING: FULL PART NUMBER

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MPS6520 MPS6521



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## SERVICES

- · Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM<sup>™</sup>)

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R1 (11-November 2014)

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