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TOSHIBA Power Transistor Module Silicon NPN Epitaxial Type (Four Darlington Power Transistors in One)

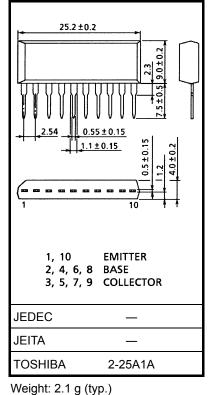
MP4101

High Power Switching Applications Hammer Drive, Pulse Motor Drive Inductive Load Switching

- Small package by full molding (SIP 10 pins)
- High collector power dissipation (4-device operation) : $P_T = 4 W (Ta = 25^{\circ}C)$
- High collector current: IC (DC) = 4 A (max)
- High DC current gain: $h_{FE} = 2000 \text{ (min)} (V_{CE} = 2 \text{ V}, I_C = 1 \text{ A})$
- Zener diode included between collector and base.

Absolute Maximum Ratings (Ta = 25°C)

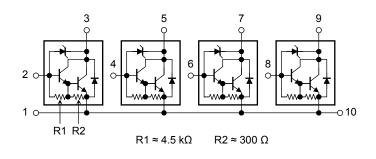
Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	60 ± 10	V	
Collector-emitter voltage		V _{CEO}	60 ± 10	V	
Emitter-base voltage		V _{EBO}	6	V	
Collector current	DC	Ι _C	4	A	
	Pulse	I _{CP}	6		
Continuous base current		Ι _Β	0.5	А	
Collector power dissipation (1-device operation)		PC	2.0	W	
Collector power dissipation (4-device operation)		PT	4.0	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

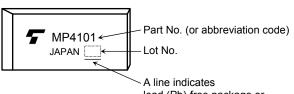
Array Configuration



Industrial Applications

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Marking



A line indicates lead (Pb)-free package or lead (Pb)-free finish.

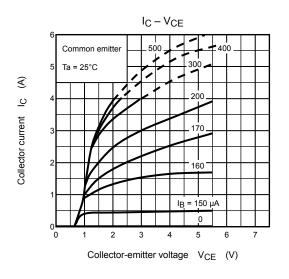
Thermal Characteristics

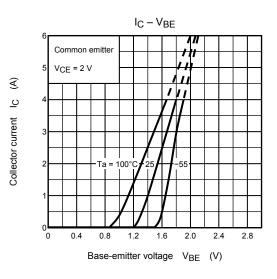
Characteristics	Symbol	Max	Unit	
Thermal resistance from junction to ambient	ΣR _{th (j-a)}	31.3	°C/W	
(4-device operation, $Ta = 25^{\circ}C$)	· · · ·			
Maximum lead temperature for soldering purposes	TL	260	°C	
(3.2 mm from case for 10 s)				

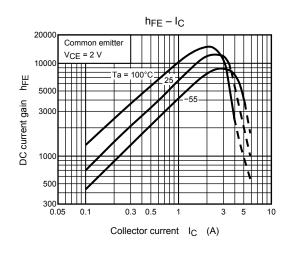
Electrical Characteristics (Ta = 25°C)

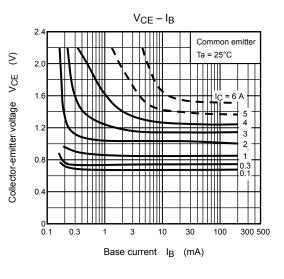
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I _{CBO}	V _{CB} = 45 V, I _E = 0 A	_	—	10	μA	
Collector cut-off current		ICEO	V _{CE} = 45 V, I _B = 0 A	_	_	10	μA	
Emitter cut-off current		I _{EBO}	V _{EB} = 6 V, I _C = 0 A	0.6	_	20	mA	
Collector-base breakdown voltage		V (BR) CBO	I _C = 10 mA, I _E = 0 A	50	60	70	V	
Collector-emitter breakdown voltage		V (BR) CEO	I _C = 10 mA, I _B = 0 A	50	60	70	V	
DC current gain		h _{FE (1)}	V _{CE} = 2 V, I _C = 1 A	2000	_	15000		
		h _{FE (2)}	V _{CE} = 2 V, I _C = 3 A	1000	_	—		
Saturation voltage	Collector-emitter	V _{CE (sat)}	I _C = 3 A, I _B = 10 mA	_	_	1.5	v	
	Base-emitter	V _{BE (sat)}	I _C = 3 A, I _B = 10 mA	_	_	2.0		
Transition frequency		fT	V _{CE} = 2 V, I _C = 0.5 A	_	60	_	MHz	
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0 A, f = 1 MHz	_	30	—	pF	
Switching time	Turn-on time	ton	Output 1000000000000000000000000000000000000	_	0.2	_		
	Storage time	t _{stg}		_	3.0	_	μs	
	Fall time	t _f		_	0.5	_		

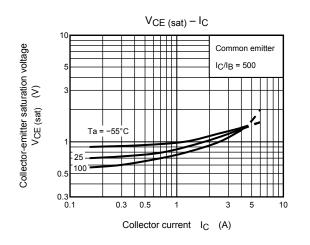
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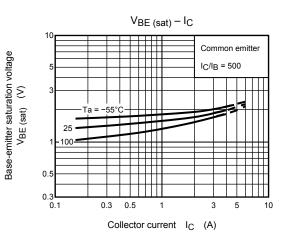


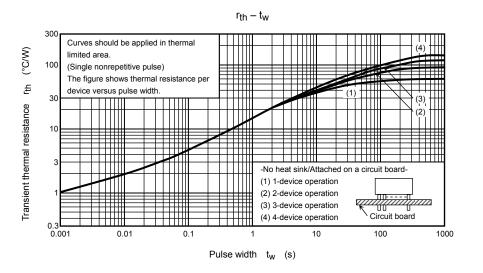


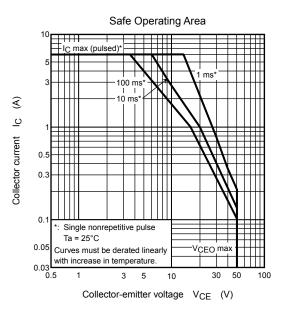




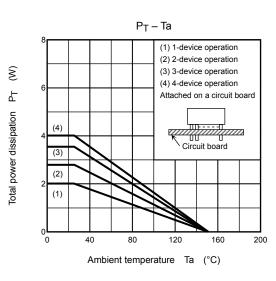








 $\Delta T_{i} - P_{T}$ 160 () 0 (1) (2) (3) (4) $\Delta \mathsf{T}_j$ 120 Junction temperature increase 80 7000 111 Circuit board Attached on a circuit board 4 (1) 1-device operation (2) 2-device operation (3) 3-device operation (4) 4-device operation 0 2 3 4 5 1 Total power dissipation PT (W)



RESTRICTIONS ON PRODUCT USE

20070701-EN

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