



Dual Rectifier Diode Water Cooled Module

Preliminary Information

DS5286-2.1 June 2001

FEATURES

- Dual Device Module
- Electrically Isolated Package
- Pressure Contact Construction
- International Standard Footprint
- Alumina (Non-toxic) Isolation Medium

APPLICATIONS

- Power Supplies
- Large IGBT Circuit 'Front Ends'
- Rectifiers
- Battery Chargers

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V _{DRM} V _{RRM} V	Conditions
MP04DD810-30 MP04DD810-28 MP04DD810-26 MP04DD810-24	3000 2800 2600 2400	$T_{v_j} = -40^{\circ} \text{ to } 150^{\circ}\text{C},$ $V_{RSM} = V_{RRM} + 100V$

Lower voltage grades available

ORDERING INFORMATION

Order As:

MP04DD810-XX-W2 1/4 - 18 NPT connection
MP04DD810-XX-W3 1/4 - 18 NPT connection
MP04DD810-XX-W3A 1/4 - 18 NPT water connection
thread

XX shown in the part number about represents $V_{\text{DRM}}/100$ selection required, eg. MP04DD810-28-W2

Note: When ordering, please use the complete part number. Please quote full part number in all correspondance.

KEY PARAMETERS

V_{RRM}	3000V
I _{F(AV)}	812A
FSM (per arm)	20000A
F(RMS)	1276A
V	3000V
ISOI	

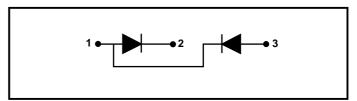


Fig.1 DD circuit configuration

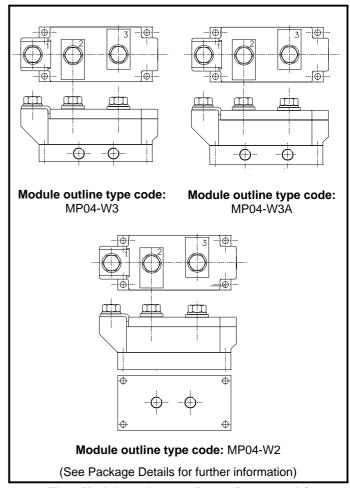


Fig. 2 Module package variants - (not to scale)



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.

Symbol	Parameter	Conditions		Max.	Units
I _{F(AV)}	Mean forward current	Half wave resistive load	T _{water (in)} = 25°C	885	А
		4.5 Ltr/min	T _{water (in)} = 40°C	812	А
I _{F(RMS)}	RMS value	T _{water (in)} = 25°C, 4.5 Ltr/min		1392	Α
		T _{water (in)} = 40°C, 4.5 Ltr/min		1276	А
I _{FSM}	Surge (non-repetitive) forward current	10ms half sine; T _j = 150°C		20	kA
l²t	I ² t for fusing	$V_R = 0$		2.0 x 10 ⁶	A²s
I _{FSM}	Surge (non-repetitive) forward current	10ms half sine; T _j = 150°C		16	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM}$		1.28 x 10 ⁶	A ² s
V _{isol}	Isolation voltage	Commoned terminals to base plate AC RMS, 1 min, 50Hz		3000	V

THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions	Min.	Max.	Units
$R_{\text{th(j-w)}}$	R _{th(j-w)} Thermal resistance - junction to water (per diode)	dc, 4.5 Ltr/min	-	0.102	°C/W
		Halfwave, 4.5 Ltr/min	-	0.106	°C/W
		3 Phase, 4.5 Ltr/min	-	0.112	°C/W
T _{vj}	Virtual junction temperature	Reverse (blocking)	-	150	°C
T _{stg}	Storage temperature range	-	-40	150	°C
-	Screw torque	Mounting - M6	6 (53)	-	Nm (lb.ins)
		Electrical connections - M10	-	12 (106)	Nm (lb.ins)
-	Weight (nominal)	-	-	Refer to Drawing	g



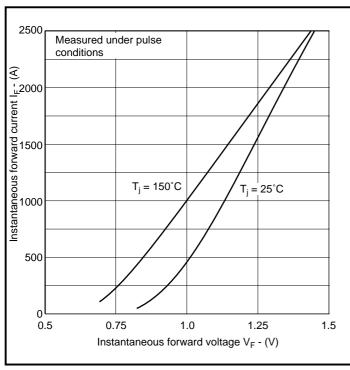
CHARACTERISTICS

Symbol	Parameter	Conditions	Min.	Max.	Units
I _{RRM}	Peak reverse current	At V _{RRM} , T _{case} = 150°C	-	50	mA
Q _s	Total stored charge	$I_{F} = 1000A$, $dI_{RR}/dt = 3A/\mu s$	-	1600	μС
I _{RR}	Peak recovery current	$T_{\text{case}} = 150^{\circ}\text{C}, V_{\text{R}} = 100\text{V}$	-	85	А
V _{TO}	Threshold voltage. See Note 1.	At $T_{v_j} = 150^{\circ}C$	-	0.7	V
r _⊤	Slope resistance. See Note 1.	At $T_{vj} = 150^{\circ}C$	-	0.29	mΩ

Note 1: The data given in this datasheet with regard to forward voltage drop is the for the calculation of the power dissipation in the semiconductor elements only. Forward voltage drops measured at the power terminals will be in excess of these figures due to the impedance of the busbars from the terminals to the semiconductor.



CURVES



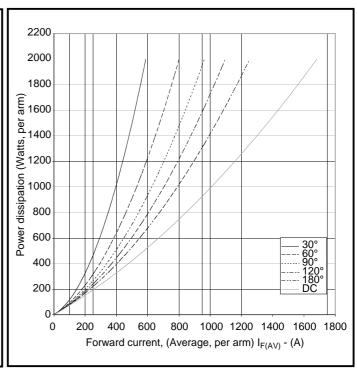


Fig.3 Maximum (limit) forward characteristics



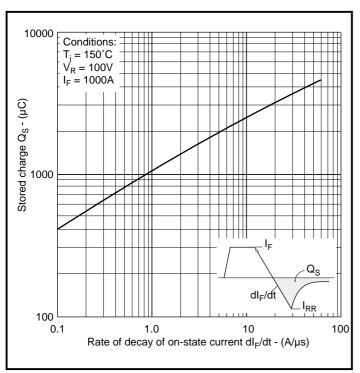


Fig.5 Maximum stored charge

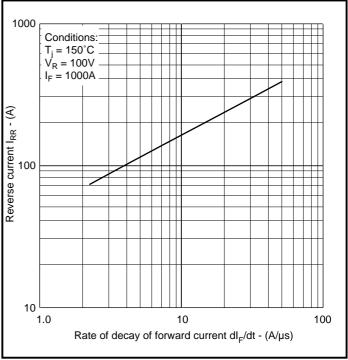


Fig.6 Maximum reverse recovery current



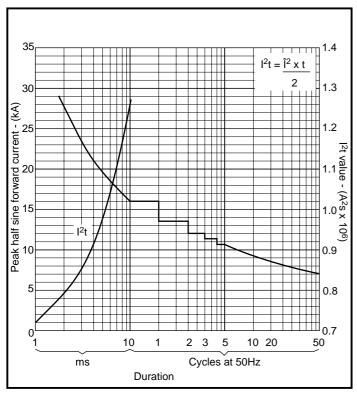


Fig.7 Surge (non-repetitive) forward current vs time (with 50% $V_{RRM} @ T_c - 150^{\circ}C$)

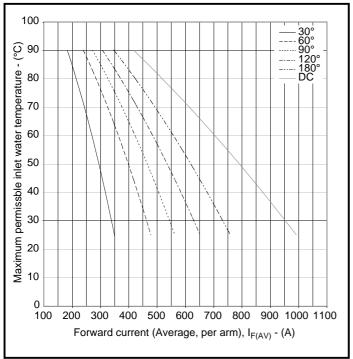


Fig. 9 Maximum permissible water inlet temperature vs on-state current at specified conduction angles, sine wave 50/60Hz

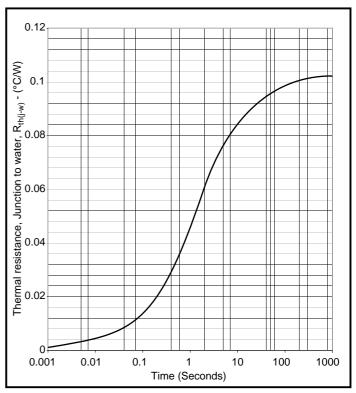
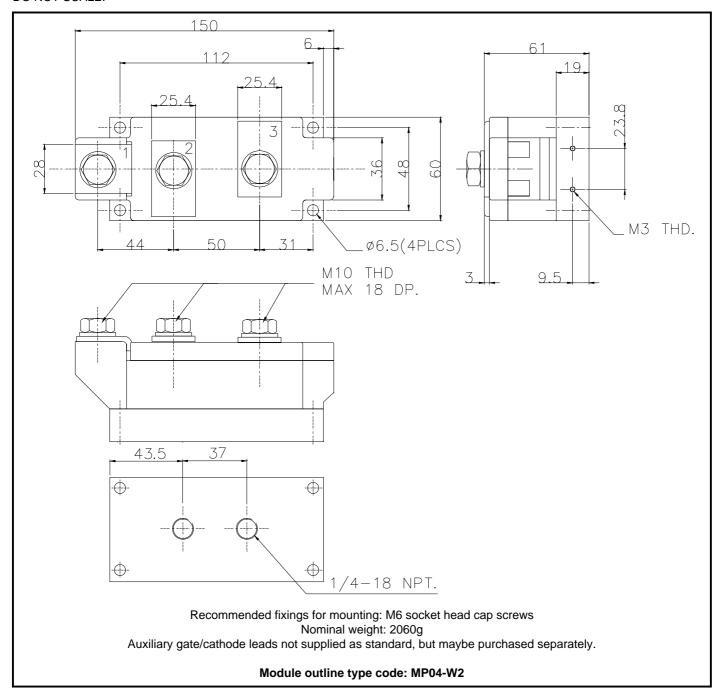


Fig.8 Transient thermal impedance - dc



PACKAGE DETAILS

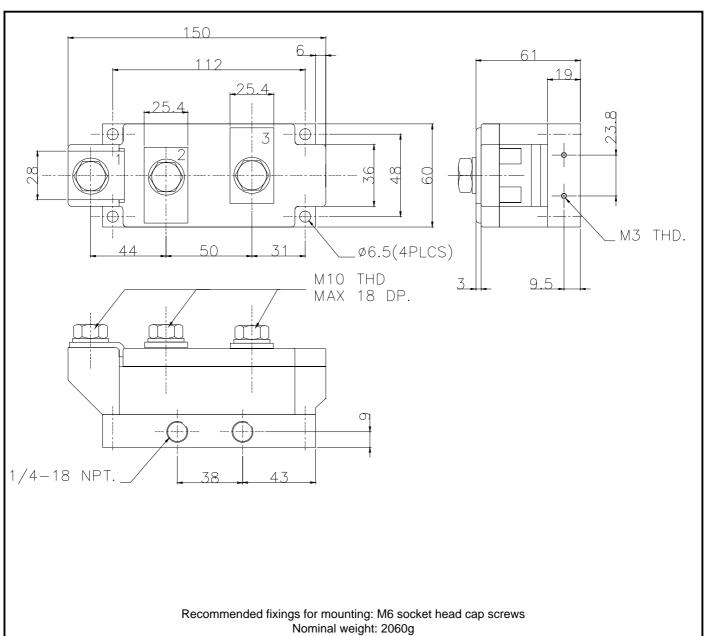
For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





PACKAGE DETAILS

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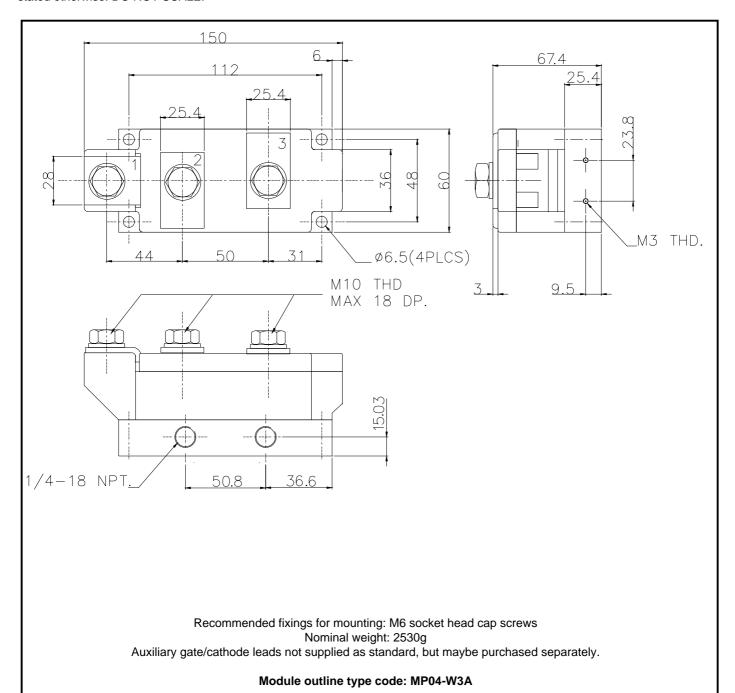
Auxiliary gate/cathode leads not supplied as standard, but maybe purchased separately.

Module outline type code: MP04-W3



PACKAGE DETAILS

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We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

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