

MICROCOMPUTER and PERIPHERAL LSI's

Peripheral LSI's

Type No.	Function	Maximum Ratings (Ta=25°C)	Electrical Characteristics (Ta=25°C)						
			Item	Symbol	Condition	min.	typ.	max.	Unit
MN1201	CMOS Dual 4-Bit Data Latches	$V_{DD} = -0.3 \sim +8V$	"H" Level Input Voltage	V_{IH}		2.4			V
		$V_I = -0.3 \sim V_{DD} + 0.3V$	"L" Level Input Voltage	V_{IL}				0.8	V
		$V_O = -0.3 \sim V_{DD} + 0.3V$	"H" Level Output Voltage	V_{OH}		4.0			V
		$T_{opr} = -30 \sim +70^\circ C$	"L" Level Output Voltage	V_{OL}				0.5	V
		$T_{stg} = -55 \sim +100^\circ C$							
		Operating Condition							
		$V_{DD} = +5V$ $V_{SS} = 0V$ $T_a = 25^\circ C$							

MN1201 (Package L—15,18—Lead Plastic DIL)

