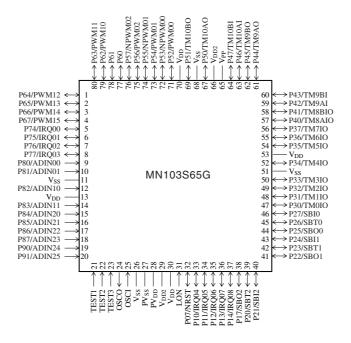
## ■ MN103S65G

Туре		MN103S65G		
Internal ROM		128 K-byte 4 K-byte SRAM		
Internal RAM	_			
Package		LQFP080-P-1414A *Lead-free		
Minimum Instruction Execution Time		25 ns (at 4.3 V to 5.5 V, 10 MHz internal regulator used) 25 ns (at 3.0 V to 3.6 V, 10 MHz) *at internal 4 times oscillation used)		
Interrupts		<ul> <li>9 external interrupts</li> <li>42 internal interrupts (watch dog timer, timer, serial I/F, PWM, A/D, system error)</li> </ul>		
Timer Counter		Eight 8-bit timers Interval timer, Event counter, Cascading  Four 16-bit timers Interval timer, Event counter, PWM output, Double buffer		
		Watch dog timer		
Serial Interface		UART (full duplex) / synchronous interfaces selective: 3		
I/O Pins I/O	50	exclusive: 2, selective: 48		
Input	10	selective: 10		
•		10-bits, 2 inputs: 2, 6 inputs: 1 Minimum conversion time 1.5us		
PWM		3-phase PWM output 16-bit counter, triangular waveform or jigsaw waveform dead time setup, Double buffer		
Electrical Characteristics		T.B.D.		

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## Pin Assignment



LQFP080-P-1414A \*Lead-free

## **Support Tool**

In-circuit Emulator	PX-ICE-103S52 PX-ODB103S-DO		
On board Debugger			
Flash Memory Built-in Type	Туре	MN103SF65G	
	ROM	128 K-byte	
	RAM	4 K-byte SRAM	
	Minimum instruction execution time	25 ns (at 4.3 V to 5.5 V, 10 MHz internal regulator used)	
		25 ns (at 3.0 V to 3.6 V, 10 MHz)	
	Package	LQFP080-P-1414A *Lead-free	

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