BCD-TO-SEVEN SEGMENT LATCH/ DECODER/DRIVER

GENERAL DESCRIPTION

The MMC 4511 is a monolithic integrated circuit available in 16-lead dual in-line plastic or ceramic package
The MMC 4511 type is a BCD-to-7-segment latch decoder driver constructed with COS/MOS logic and n-p-n bipolar transistor output devices on a single monolithic structure. This device combines the low quiescent power dissipation and high noise immunity features of COS/MOS with n-p-n bipolar output capable of sourcing up to 25 mA. This capability allows the MMC 4511 type to drive LED's and other displays directly. Lamp Test (LT), Blanking (BL) and Latch Enable or Strobe inputs are provided to test the display, shut off or intensity-modulate it, and store or strobe a BCD code, respectively. Several different signals may be multiplexed and displayed when external multiplexing circuitry is used.

FEATURES

- High-output-sourcing capability (up to 25mA)
- Input latches for BCD code storage
- Lamp test and blanking capability
- 7-segment outputs blanked for BCD input codes>1001

APPLICATION

- Interfacing with various displays
- Driving common-cathode 7-segment LED displays
- Driving low-voltage fluorescent displays
- Driving incandescent displays.

ABSOLUTE MAXIMUM RATINGS

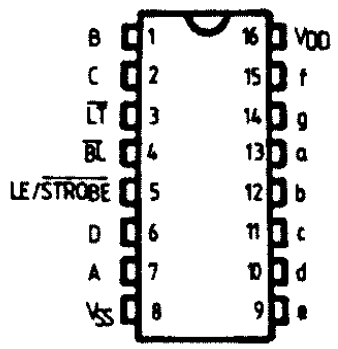
V_{DD}*	Supply voltage:	G and H types	-0.5 to	20	V
		E and F types	-0.5 to	18	V
V_I	Input voltage		-0.5 to	V_{DD}+0.5	V
I_I	DC input current (any one input)			±10	mA
P_{tot}	Total power dissipation (per package)			200	mW
	Dissipation per output transistor for T_A = full package-temperature range			100	mW
T_A	Operating temperature :	G and H types	-55 to	125	°C
		E and F types	-40 to	85	°C
T_{stg}	Storage temperature		-65 to	150	°C

* All voltage values are referred to V_{SS} pin voltage

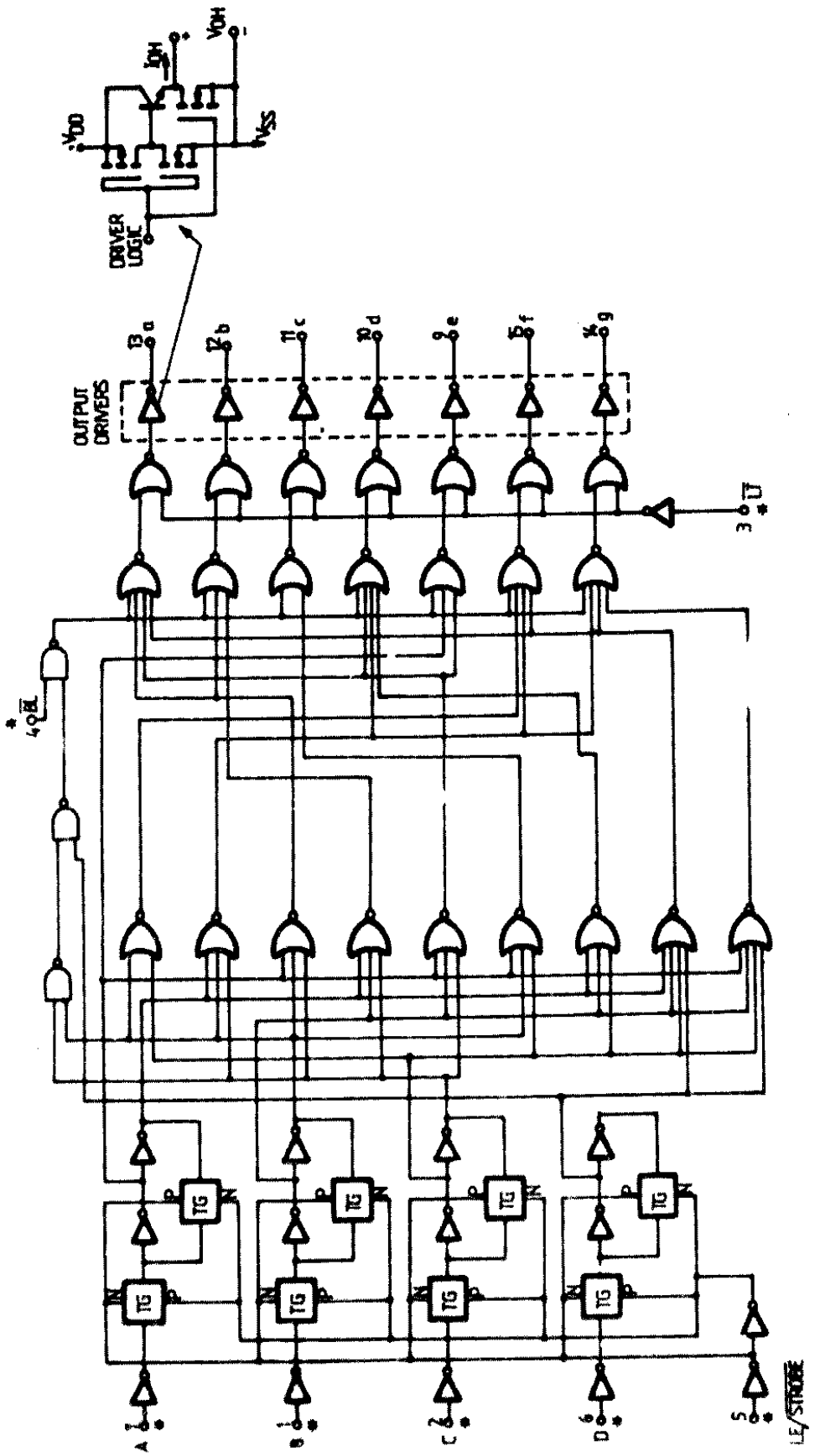
RECOMMENDED OPERATING CONDITIONS

V_{DD}*	Supply voltage:	G and H types	3 to	18	V
		E and F types	3 to	15	V
V_I	Input voltage		0 to	V_{DD}	V
T_A	Operating temperature :	G and H types	-55 to	125	°C
		E and F types	-40 to	85	°C

CONNECTION DIAGRAM



LOGIC DIAGRAM

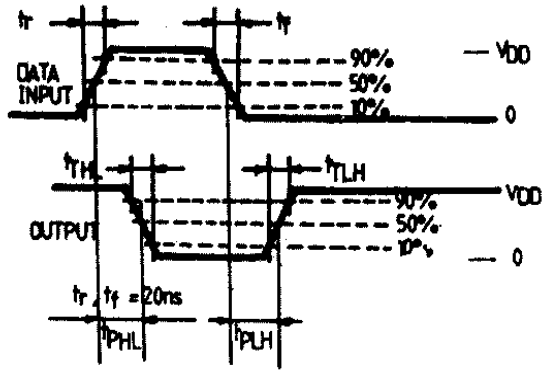


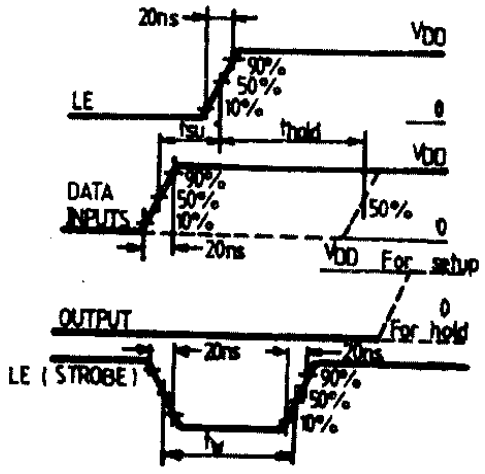
TRUTH TABLE

LE	BI	LT	D	C	B	A	a	b	c	d	e	f	g	Display
X	X	0	X	X	X	X	1	1	1	1	1	1	1	8
X	0	1	X	X	X	X	0	0	0	0	0	0	0	Blank
0	1	1	0	0	0	0	1	1	1	1	1	1	0	0
0	1	1	0	0	0	1	0	1	1	0	0	0	0	1
0	1	1	0	0	1	0	1	1	0	1	1	0	1	2
0	1	1	0	0	1	1	1	1	1	1	0	0	1	3
0	1	1	0	1	0	0	0	1	1	0	0	1	1	4
0	1	1	0	1	0	1	1	0	1	1	0	1	1	5
0	1	1	0	1	1	0	0	0	1	1	1	1	1	6
0	1	1	0	1	1	1	1	1	1	0	0	0	0	7
0	1	1	1	0	0	0	1	1	1	1	1	1	1	8
0	1	1	1	0	0	1	1	1	1	0	0	1	1	9
0	1	1	1	0	1	0	0	0	0	0	0	0	0	Blank
0	1	1	1	0	1	1	0	0	0	0	0	0	0	Blank
0	1	1	1	1	0	0	0	0	0	0	0	0	0	Blank
0	1	1	1	1	0	1	0	0	0	0	0	0	0	Blank
0	1	1	1	1	1	0	0	0	0	0	0	0	0	Blank
0	1	1	1	1	1	1	0	0	0	0	0	0	0	Blank
1	1	1	X	X	X	X				✲				✲

X = Don't care
✲ = Depends on BCD code previously applied when LE = 0
Note : Display is blank for all illegal input codes (BCD > 1001)

WAVEFORMS





STATIC ELECTRICAL CHARACTERISTICS

(over recommended operating conditions)

PARAMETER			TEST CONDITIONS				VALUES							UNIT
			V_I (V)	V_O (V)	$\|I_O\|$ (μA)	V_{DD} (V)	T^*_{LOW} min.	T^*_{LOW} max.	25°C min.	25°C typ	25°C max.	T^*_{HIGH} min.	T^*_{HIGH} max.	
I_L	Quiescent current	G, H types	0/ 5			5		5		0.04	5		150	μA
			0/10			10		10		0.04	10		300	
			0/15			15		20		0.04	20		600	
			0/20			20		100		0.08	100		3000	
		E, F types	0/ 5			5		20		0.04	20		150	
			0/10			10		40		0.04	40		300	
			0/15			15		80		0.04	80		600	
V_{OH}	Output high voltage		0/ 5			5	4		4.1	4.55		4.2		V
			0/10			10	9		9.1	9.55		9.2		
			0/15			15	14		14.1	14.55		14.2		
V_{OL}	Output low voltage		5 /0		< 1	5		0.05			0.05		0.05	V
			10/0		< 1	10		0.05			0.05		0.05	
			15/0		< 1	15		0.05			0.05		0.05	
V_{IH}	Input high voltage			0.5/4.5	< 1	5	3.5		3.5			3.5		V
				1/9	< 1	10	7		7			7		
				1.5/13.5	< 1	15	11		11			11		
V_{IL}	Input low voltage			4.5/0.5	< 1	5		1.5			1.5		1.5	V
				9/1	< 1	10		3			3		3	
				13.5/1.5	< 1	15		4			4		4	
V_{OH}	Output drive voltage	G, H types			0	5	4.1		4.1	4.55		4.2		V
					5					4.25				
					10		3.8		3.9	4.10		3.9		
					15					3.95				
					20		3.55		3.4	3.75				
					25		3.4		3.1	3.55				
					0	10	9		9.1	9.55		9.2		V
					5					9.25				
					10		8.85		9	9.15				
					15					9.05				
					20		8.7		8.6	8.9		8.4		
					25		8.6		8.3	8.75				
					0	15	14		14.1	14.55		14.2		V
					5					14.3				
					10		13.9		14	14.2		14.0		
					15					14.1				
					20		13.75		13.7	13.95		13.5		
					25		13.65		13.5	13.8		13.1		
		E, F types			0	5	4.1		4.1	4.57		4.1		V
					5					4.24				
					10		3.6		3.6	4.12		3.3		
					15					3.94				
					20		2.8		2.8	3.75		2.5		
					25					3.54				
					0	10	9.1		9.1	9.58		9.1		V
					5					9.26				
					10		8.75		8.75	9.17		8.45		
					15					9.04				
					20		8.1		8.1	8.9		7.8		
					25					8.75				

STATIC ELECTRICAL CHARACTERISTICS

(over recommended operating conditions)

PARAMETER			TEST CONDITIONS V_I (V)	V_O (V)	$\|I_O\|$ (μA)	V_{DD} (V)	VALUES T^*_{LOW} min.	T^*_{LOW} max.	25°C min.	25°C typ	25°C max.	T^*_{HIGH} min.	T^*_{HIGH} max.	UNIT
		E, F types			0	15	14.1		14.1	14.59		14.1		V
					5					14.27				
					10		13.75		13.75	14.18		13.45		
					15					14.07				
					20		13.1		13.1	13.95		12.8		
					25					13.8				
I_{OL}	Output sink current	G, H types	0/ 5	0.4		5	0.64		0.51	1		0.36		mA
			0/10	0.5		10	1.6		1.3	2.6		0.9		
			0/15	1.5		15	4.2		3.4	6.8		2.4		
		E, F types	0/ 5	0.4		5	0.52		0.44	1		0.36		
			0/10	0.5		10	1.3		1.1	2.6		0.9		
			0/15	1.5		15	3.6		3.0	6.8		2.4		
I_{IH}, I_{IL}	Input leakage current	G, H types	0/18	Any input		18		±0.1		$±10^{-5}$	±0.1		±1	μA
		E, F types	0/15			15		±0.3		$±10^{-5}$	±0.3		±1	
C_I	Input capacitance			Any input						5	7.5			pF

* T_{LOW} = −55°C for G, H devices; −40°C for E, F devices.
* T_{HIGH} = +125°C for G, H devices; +85°C for E, F devices.

The Noise Margin for both "1" and "0" level is:
1 V min. with V_{DD} = 5 V
2 V min. with V_{DD} = 10 V
2.5 V min. with V_{DD} = 15 V

DYNAMIC ELECTRICAL CHARACTERISTICS

(T_A = 25°C, C_L = 50 pF, R_L = 200 kΩ, typical temperature coefficient for all V_{DD} values is 0.3%/°C, all input rise and fall times = 20 ns)

PARAMETER		TEST CONDITIONS V_{DD}(V)	VALUES min.	typ.	max.	UNIT
t_{PHL}	Propagation delay time (Data)	5		520	1040	ns
		10		210	420	
		15		150	300	
t_{PLH}	Propagation delay time (Data)	5		660	1320	ns
		10		260	520	
		15		180	360	
t_{PHL}	Propagation delay time ($\overline{BL}$)	5		350	700	ns
		10		175	350	
		15		150	300	
t_{PLH}	Propagation delay time ($\overline{BL}$)	5		400	800	ns
		10		175	350	
		15		150	300	
t_{PHL}	Propagation delay time ($\overline{LT}$)	5		250	500	ns
		10		125	250	
		15		85	170	
t_{PLH}	Propagation delay time ($\overline{LT}$)	5		150	300	ns
		10		75	150	
		15		50	100	

PARAMETER		TEST CONDITIONS	VALUES			UNIT
		VDD(V)	min.	typ.	max.	
t_{TLH}	Transition time	5		40	80	ns
		10		30	60	
		15		20	50	
t_{THL}	Transition time	5		125	310	ns
		10		75	185	
		15		65	160	
t_{setup}	Setup time	5	150	75		ns
		10	70	35		
		15	40	20		
t_{hold}	Hold time	5	0	-75		ns
		10	0	-35		
		15	0	-20		
t_W	Strobe pulse width	5	400	200		ns
		10	160	80		
		15	100	50		

APPLICATION

Driving common-cathode 7-segment LED displays