

isc Silicon PNP Power Transistors

MJD32C

DESCRIPTION

- DC Current Gain -hFE = 25(Min)@ IC= -1A
- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR) CEO}= -100V(Min)
- Complement to Type MJD31C
- DPAK for Surface Mount Applications
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

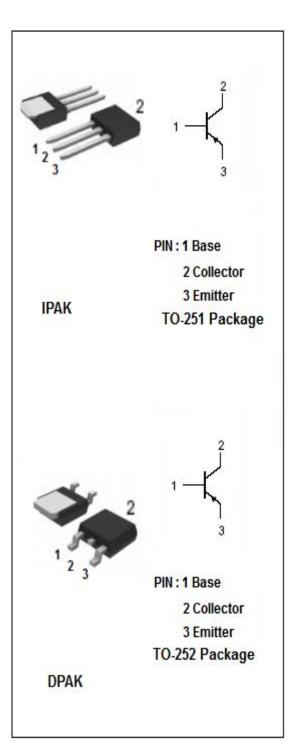
• Designed for use in general purpose amplifier and low speed switching applications.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

| SYMBOL | PARAMETER | VALUE | UNIT | |
|------------------|--|---------|------------|--|
| V _{СВО} | Collector-Base Voltage | -100 | V | |
| V _{CEO} | Collector-Emitter Voltage | -100 | V | |
| V _{EBO} | Emitter-Base Voltage | -5 | V | |
| Ic | Collector Current-Continuous | -3 | Α | |
| I _{CM} | Collector Current-Pulse | -5 | Α | |
| I _B | Base Current | -1 | Α | |
| Pc | Collector Power Dissipation T_c =25°C | 15 | W | |
| | Collector Power Dissipation T _a =25°C | 1.56 | | |
| Tj | Junction Temperature 15 | | $^{\circ}$ | |
| T _{stg} | Storage Ttemperature Range | -65~150 | $^{\circ}$ | |

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | MAX | UNIT |
|---------------------|--|-----|------|
| R _{th j-c} | Thermal Resistance,Junction to Case | 8.3 | °C/W |
| R _{th j-a} | Thermal Resistance,Junction to Ambient | 80 | °C/W |





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ELECTRICAL CHARACTERISTICS

 T_{C} =25°C unless otherwise specified

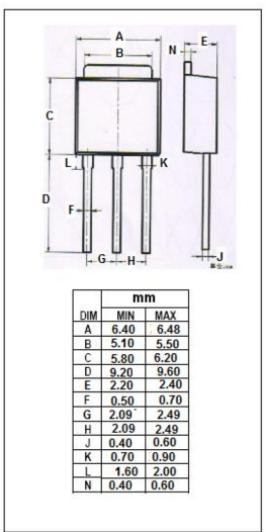
| SYMBOL | PARAMETER | CONDITIONS | MIN | MAX | UNIT |
|-----------------------|--------------------------------------|--|------|------|------|
| V _{(BR)CEO} | Collector-Emitter Breakdown Voltage | I _C = -30mA; I _B = 0 | -100 | | V |
| V _{CE} (sat) | Collector-Emitter Saturation Voltage | I _C = -3A; I _B = -0.375A | | -1.2 | V |
| V _{BE(on)} | Base-Emitter On Voltage | Ic= -3A; V _{CE} = -4V | | -1.8 | V |
| I _{CES} | Collector Cutoff Current | V _{CE} = -100V; V _{EB} = 0 | | -20 | uA |
| I _{CEO} | Collector Cutoff Current | V _{CE} = -60V; I _B = 0 | | -50 | uA |
| I _{EBO} | Emitter Cutoff Current | V _{EB} = -5V; I _C = 0 | | -1.0 | mA |
| h _{FE-1} | DC Current Gain | I _C = -1A; V _{CE} = -4V | 25 | | |
| h _{FE-2} | DC Current Gain | I _C = -3A; V _{CE} = -4V | 10 | 50 | |
| f⊤ | Current-Gain—Bandwidth Product | I _C = -0.5A; V _{CE} = -10V | 3 | | MHz |

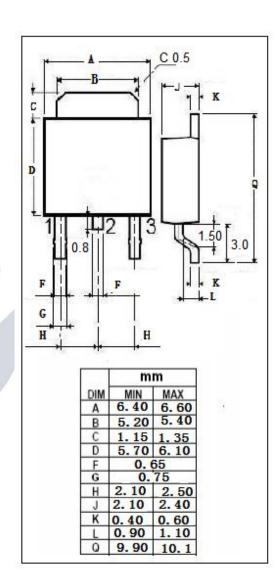


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Outline Drawing





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