

# **isc Silicon NPN Power Transistor**

#### **DESCRIPTION**

- DC Current Gain-
- : h<sub>FE</sub> = 70(Min) @ I<sub>C</sub>= 0.5A
- · Low Collector Saturation Voltage-
  - :  $V_{CE(sat)} = 0.3V(Max.)@I_{C} = 0.5 A$
- Complement to the PNP MJD210
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

#### **APPLICATIONS**

 Designed for low power audio amplifier and low-current, high-speed switching applications.

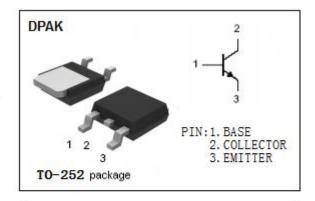
# ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

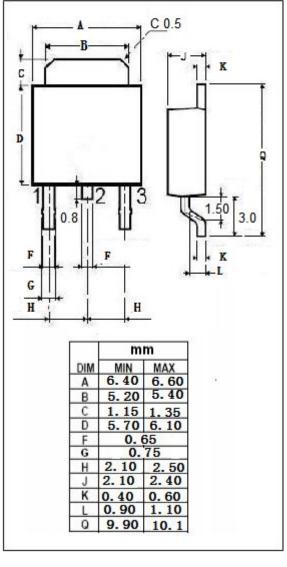
SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	25	V
V <sub>EBO</sub>	Emitter-Base Voltage	8	V
Ic	Collector Current-Continuous	5	Α
I <sub>CM</sub>	Collector Current-Peak	10	Α
I <sub>B</sub>	Base Current	1	А
Pc	Collector Power Dissipation T <sub>a</sub> =25°C	1.4	
	Collector Power Dissipation T <sub>C</sub> =25°C	12.5	W
Ti	Junction Temperature	n Temperature 150	
T <sub>stg</sub>	Storage Temperature Range	-65~150	$^{\circ}$

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal Resistance, Junction to Case	10	°C/W
R <sub>th j-a</sub>	j-a Thermal Resistance,Junction to Ambient		°C/W

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## **isc Silicon NPN Power Transistor**

**MJD200** 

#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub> =25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 10mA; I <sub>B</sub> = 0	25		V
Vce(sat)-1	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 0.5 A ;I <sub>B</sub> = 50mA		0.3	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 2A ;I <sub>B</sub> = 0.2A		0.75	V
V <sub>CE(sat)-3</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A ;I <sub>B</sub> = 1A		1.8	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5A ;I <sub>B</sub> = 1A		2.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 2A; V <sub>CE</sub> = 1V		1.6	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 40V; I <sub>E</sub> = 0 V <sub>CB</sub> = 40V; I <sub>E</sub> = 0;T <sub>C</sub> = 125°C		0.1 0.1	μA mA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 8V; I <sub>C</sub> = 0		0.1	μА
h <sub>FE-1</sub>	DC Current Gain	Ic= 0.5 A; V <sub>CE</sub> = 1V	70		
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = 2A ; V <sub>CE</sub> = 1V	45	180	
h <sub>FE-3</sub>	DC Current Gain	I <sub>C</sub> = 5A ; V <sub>CE</sub> = 2V	10		
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = 0.1 A; V <sub>CE</sub> = 10V; f <sub>test</sub> = 10MHz	65		MHz
Сов	Collector Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = 10V; f <sub>test</sub> = 0.1MHz	60		pF

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