

isc Silicon NPN Power Transistors

DESCRIPTION

- Complement to Type PNP MJ15025
- Excellent Safe Operating Area
- · High DC current Gain
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

 Designed for high power audio, disk head positioners and other linear applications

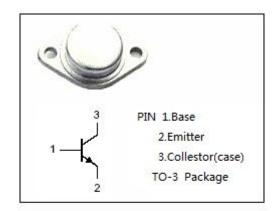
ABSOLUTE MAXIMUM RATINGS(T_C=25℃)

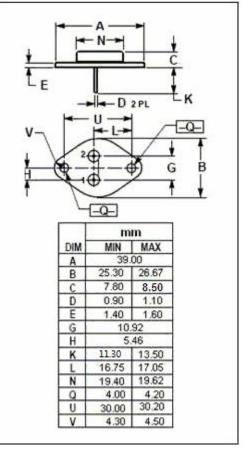
ABOOLOTE MAXIMOM (ATMOS(10 25 0)						
SYMBOL	PARAMETER	VALUE	UNIT			
V _{CBO}	ollector-Base Voltage 400		٧			
V _{CEO}	Collector-Emitter Voltage	250	V			
V _{EBO}	Emitter-Base Voltage 5		V			
Ic	Collector Current-Continuous	16	Α			
I _{CM} (1)	Collector Current-Peak	30	Α			
I _B	Base Current-Continuous	5	Α			
P _D	Total Power Dissipation @Tc=25℃	25℃ 250				
Tj	Junction Temperature	-65~200	$^{\circ}$			
T _{stg}	Storage Temperature	-65~200	$^{\circ}$			

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	0.70	°C/W

⁽¹⁾ Pulse Test: Pulse Width = 5 ms, Duty Cycle< 10%.







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MJ15024

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 50mA ;I _B = 0	250		V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	Ic= 8A; I _B = 0.8A		1.4	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 16A; I _B = 3.2A		4.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 8A ; V _{CE} = 4V		2.2	V
I _{CEO}	Collector Cutoff Current	V _{CE} = 200V; I _B = 0		0.5	mA
I _{CBO}	Collector Cutoff Current	V _{CB} = 250V; I _E = 0		0.25	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C =0		0.5	mA
h _{FE-1}	DC Current Gain	I _C = 8A ; V _{CE} = 4V	15	60	
h _{FE-2}	DC Current Gain	I _C = 16A; V _{CE} = 4V	5		
I _{s/b}	Second Breakdown Collector Current With Base Forward Biased	V _{CE} = 50Vdc,t=0.5 s, Nonrepetitive V _{CE} = 80Vdc,t=0.5 s,Nonrepetitive	5.0 2.0		Α
Сов	Output Capacitance	I _E = 0 ; V _{CB} = 10V; f _{test} = 1.0MHz	300		pF
f⊤	Current-Gain—Bandwidth Product	I _C = 1A ; V _{CE} = 10V; f _{test} = 1.0MHz	4		MHz

⁽¹⁾ Pulse Test: Pulse Width = 300 μ s, Duty Cycle < 2%.

NOTICE:

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