



Type	Silicon MOSFET type Integrated Circuit		
Application	For Switching Power Supply Control		
Structure	CMOS type		
Equivalent Circuit	See Figure 7		
Package	SO8-G2	Marking	MIP530

A. ABSOLUTE MAXIMUM RATINGS (Ta=25°C±3°C)

No.	Item	Symbol	Ratings	Unit	Note
1	DRAIN Voltage	VD	−0.3 ~ 700	V	※1: It is guaranteed within the pulse as below. Leading Edge Blanking Pulse + Current Limit Delay ton(BLK)+td(OCL)
2	VIN Voltage	VIN	−0.3 ~ 650	V	
3	VDD Voltage	VDD	−0.3 ~ 8	V	
4	VDD current	IDD	30	mA	
5	Feedback Voltage	VFB	−0.3 ~ 8	V	
6	Output Peak Current	IDP	0.7(※1)	A	
7	Junction Temperature	Tj	150	°C	
8	Storage Temperature	Tstg	−55 ~ +150	°C	

B. Recommended Operating Conditions

No.	Item	Symbol	Conditions	Unit	Note
1	Junction Temperature	Tj	−40 ~ +125	°C	



C. ELECTRICAL CHARACTERISTICS

Measure condition (Ta=25°C±3°C)

No.	Item	Symbol	Measure Condition (Figure 1)	Typ.	Min.	Max.	Unit
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【CONTROL FUNCTIONS】 * Design Guarantee Item ** Reference Value Item

1	Output Frequency	fosc	※ Figure 6 VDD=VDD(ON), IFB=-20 μA, VD=ILIMIT condition,	125	112	138	kHz
2	Jitter Frequency Deviation	d_fosc	※ Figure 6 VDD=VDD(ON), IFB=-20 μA, VD=ILIMIT condition,	5.5	-	-	kHz
**3	Jitter Frequency Modulation Rate	fM	※ Figure 6 VDD=VDD(ON), IFB=-20 μA, VD=ILIMIT condition,	200	-	-	Hz
4	Maximum On-state Time	MAX(ON)	VDD=VDD(ON), IFB=-20 μA, VD=5 V,	10	7.3	12.7	μs
5	VDD start Voltage	VDD(ON)	VD=5 V, IFB=-20 μA,	5.9	5.4	6.4	V
6	VDD stop Voltage	VDD(UV)	VD=5 V, IFB=-20 μA,	4.9	4.4	5.4	V
7	VDD start/stop Voltage Hysteresis	ΔVDD	VDD(ON) - VDD(UV)	1.0	0.5	1.5	V
8	VDD clamp Voltage	VDD(CLP)	IDD=3 mA	6.2	5.6	6.8	V
9	Delta VDD clamp	D_VDD(CLP)	VDD(CLP)-VDD(ON)	0.3	0.05	0.7	V
10	Feedback Current	IFB_STB	ON→OFF VDD=VDD(ON), VD=ILIMIT condition,	-100	-155	-45	μA
11	Feedback Current Hysteresis	IFB(HYS)	OFF→ON VDD=VDD(ON), VD=ILIMIT condition,	4	-	-	μA
12	FB Pin Voltage	VFB	VDD=VDD(ON), IFB=-20 μA, VD=ILIMIT condition,	2	1.65	2.35	V
13	FB Pin Voltage at light load	VFB_STB	VDD=VDD(ON), IFB=IFB_STB, VD=ILIMIT condition,	1.75	1.4	2.1	V
14	FB Pin Grounded Current	IFB_GND	VDD=VDD(ON), VFB=0 V, VD=ILIMIT condition,	-360	-500	-240	μA
15	Pre-start Consuming Current	IDD(SB)	VDD=VDD(ON)-0.3 V, IFB=-20 μA, VD=5 V,	0.22	0.16	0.28	mA
16	Operating Circuit Consuming Current	IDD	VDD=VDD(ON), IFB=-20 μA, VD=ILIMIT condition,	0.38	0.24	0.52	mA
17	Operating Circuit Consuming Current at light load	IDD(OFF)	VDD=VDD(ON), IFB=IFB_STB, VD=ILIMIT condition,	0.38	0.20	0.56	mA
18	VDD Charging Current	Ich1 Ich2	VDD=0 V, VIN=40 V, VDD=5 V, VIN=40 V,	-11 -8	-16.5 -12	-5.5 -4	mA mA



No.	Item	Symbol	Measure Condition (Figure 1)	Typ.	Min.	Max.	Unit
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【CIRCUIT PROTECTIONS】 * Design Guarantee Item ** Reference Value Item

19	Self Protection Current Limit	ILIMIT	※Figure 4 $t_{on}=30\%$ duty cycle, VDD=VDD(ON), VFB=3 V, VD=adjust,	0.2	0.18	0.22	A
**20	When OCP Detected Oscillation Off-state Time	Tdet(OC)	VDD=VDD(ON), VFB=3 V, VD=adjusted,	1	–	–	μ s
**21	Light-load Output Current	ID(OFF)	※Figure 4 $t_{on}=30\%$ duty cycle, VDD=VDD(ON), IFB=IFB_STB+5 μ A, VD=adjust,	80	–	–	mA
22	FB Pin Over Load Charging Current	IFBch	VDD=VDD(ON), VFB=3 V, VD=ILIMIT condition,	–8	–11	–5	μ A
23	FB Pin Over Load Protection Voltage	VFB(OLP)	VDD=VDD(ON), VD=ILIMIT condition,	4.3	3.7	4.8	V
24	VFB Hysteresis	Δ VFB	VFB(OLP)–VFB	2.3	1.45	3.15	V
25	OLP VDD Oscillation Count	OLP_CNT	※Figure 3 VDD=VDD(ON) \leftrightarrow VDD(UV), VD=ILIMIT condition, FB=Open,	8			
*26	Leading Edge Blanking Delay	ton(BLK)		330	260	400	ns
*27	Current Limit Delay	td(OCL)		100	65	135	ns
28	VDD current at latch stop	IDD(OV)	ON \rightarrow OFF IFB=–20 μ A, VD=5 V,	14	9	21	mA
*29	Thermal Shutdown Temperature	TOTP		140	130	150	$^{\circ}$ C
	Thermal Shutdown Temperature Hysteresis	Δ TOTP		70	–	–	$^{\circ}$ C
30	Power-up Reset Threshold Voltage	VDDreset		2.4	1.5	3.3	V



No.	Item	Symbol	Measure Condition (Figure 1)	Typ.	Min.	Max.	Unit
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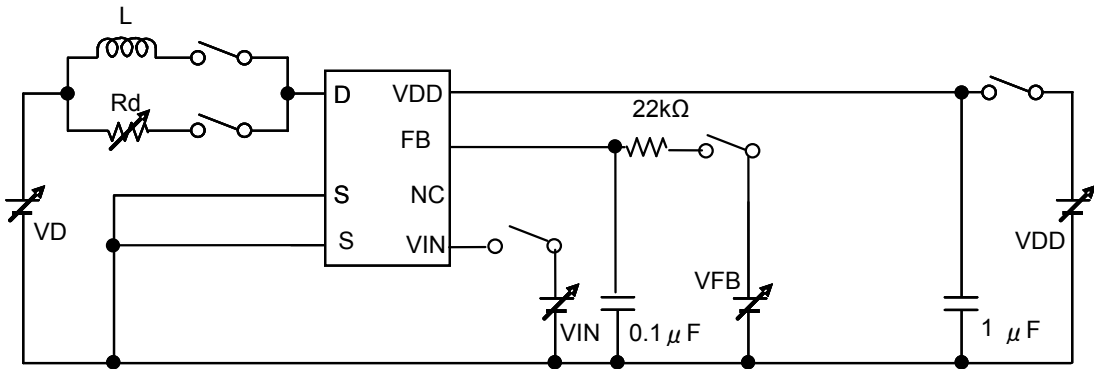
【High Voltage Input】 * Design Guarantee Item ** Reference Value Item

31	Off-state VIN Pin Leakage Current	IIN(LEAK)	VIN=600 V, IDD=IDD(OV)	10	–	20	μ A
32	VIN Pin Voltage	BVVIN	IIN=100 μ A, IDD=IDD(OV)	–	650	–	V
33	Minimum VIN Voltage	VIN(MIN)	IFB=–20 μ A, VD=5 V,	–	50	–	V

【Output】 * Design Guarantee Item ** Reference Value Item

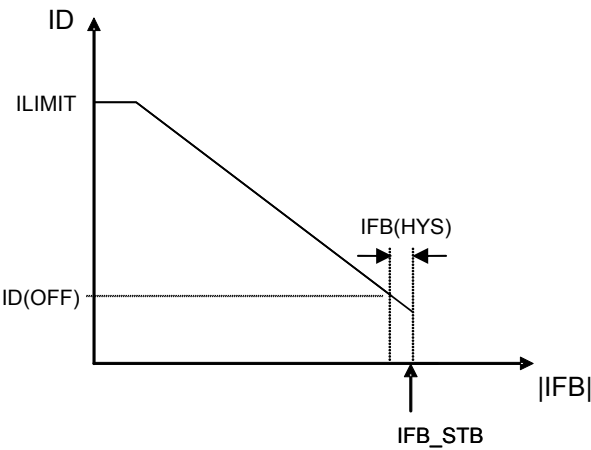
34	ON-State Resistance	RDS(ON)	VDD=VDD(ON), IFB=–20 μ A, IDS=100 mA,	27	–	37	Ω
35	OFF-State Current	IDSS	IDD=IDD(OV), IFB=–20 μ A, VD=650 V,	2	–	20	μ A
36	Breakdown Voltage	VDSS	IDD=IDD(OV), IFB=–20 μ A, ID=100 μ A,	–	700	–	V
**37	Rise Time	tr	※Figure 5 VDD=VDD(ON), IFB=–20 μ A, VD=5 V,	50	–	–	ns
**38	Fall Time	tf	※Figure 5 VDD=VDD(ON), IFB=–20 μ A, VD=5 V,	50	–	–	ns

【Figure 1 : Measure Circuit】

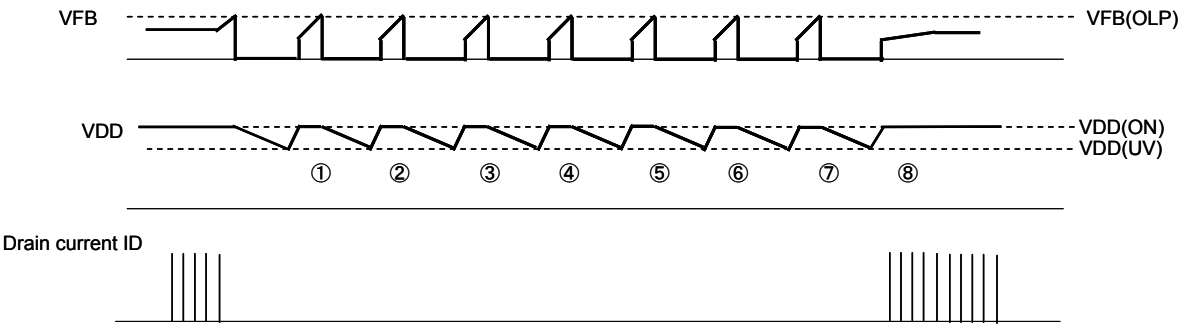




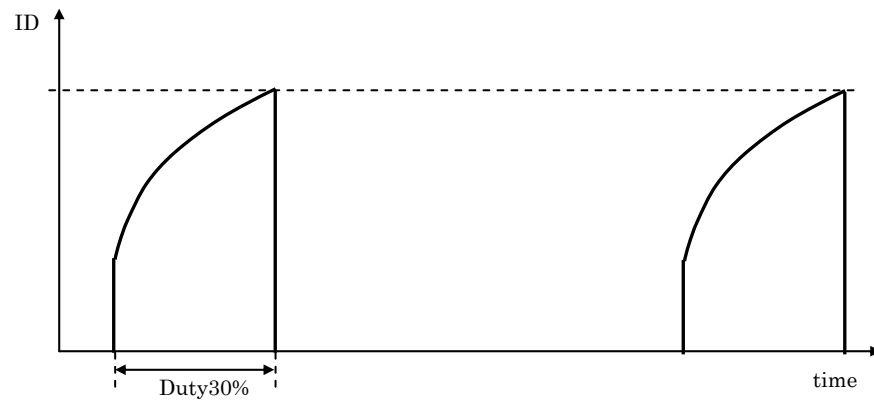
【Figure 2 : ID vs IFB Measurement】



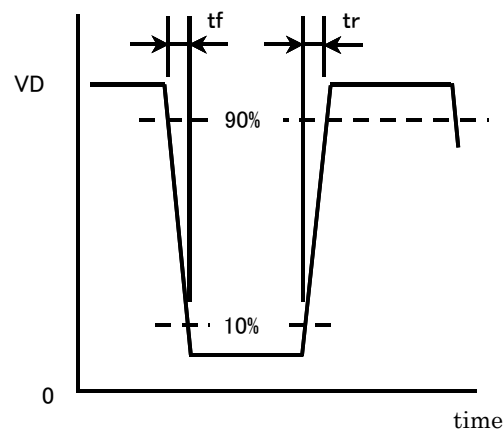
【Figure 3 : Over-Load Detected Measurement】



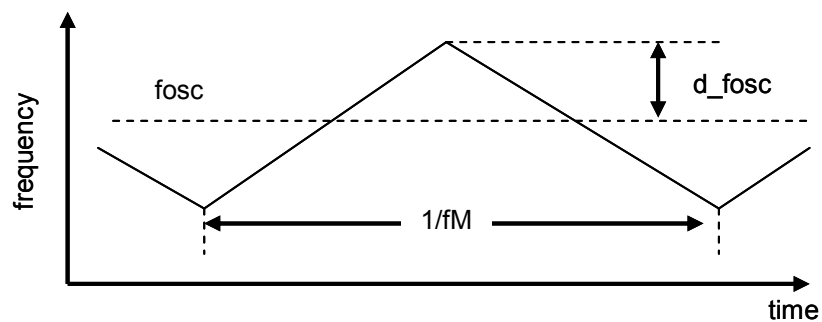
【Figure 4 : I_{LIMIT} , $I_D(OFF)$ Measurement】



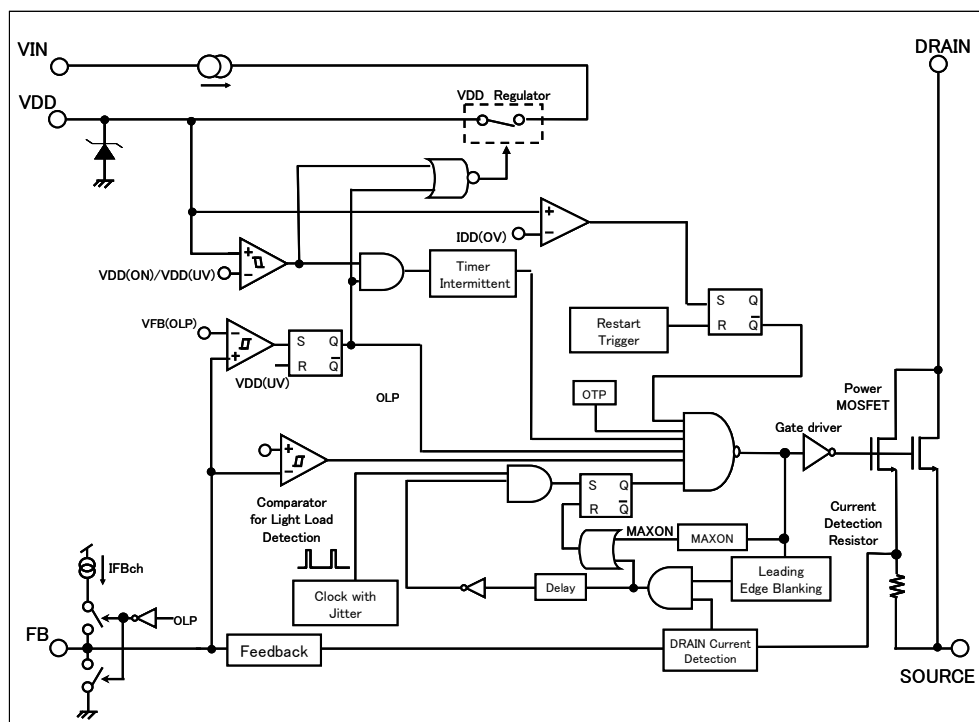
【Figure 5 : t_r , t_f Measurement】



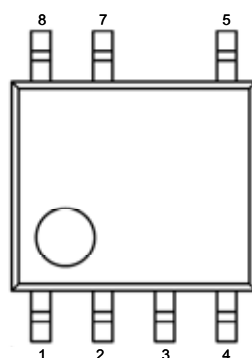
【Figure 6 : d_{fosc} , f_M Measurement】



【Figure 7 : Block Diagram】



【Figure 8 : Pin Layout】



Pin No.	Terminal Name
1	VIN
2	NC
3	FB
4	VDD
5	DRAIN
6	—
7	SOURCE
8	SOURCE



【Precautions for Use 1】

Connect a Ceramic Capacitor (over 1 μ F) between VDD Pin and SOURCE.

【Precautions for Use 2】

The IPD has risks for break-down or burst or giving off smoke in following conditions. Avoid the following use.
Fuse should be added at the input side or connect zener diode between control pin and GND, etc as a countermeasure to pass regulatory Safety Standard. Concrete countermeasure could be provided individually. However, customer should make the final judgment.

- (1) DRAIN Pin and VIN Pin reversely connect into power board.
- (2) DRAIN Pin and VIN Pin short circuit.
- (3) DRAIN Pin and FB Pin short circuit.
- (4) DRAIN Pin and VDD Pin short circuit.
- (5) VIN Pin and FB Pin short circuit.
- (6) VIN Pin and VDD Pin short circuit.

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Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.

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- 1) The sale and/or the export of IPD products to customers located in certain countries is restricted by the Agreement made and executed by and between Power Integrations, Inc. and Panasonic Corporation. For details, refer to the following Attached table "IPD availability by customer."
- 2) IPD products purchased from our company, or its authorized agents, hereinafter referred to as our company, shall be used only for production purposes by those parties who have duly purchased IPD products. Those who have purchased IPD products shall not use such IPD products in unmodified form for re-sale, loan, or sample shipment for evaluation purposes to any other parties.
- 3) If a party who has duly purchased IPD products subcontracts its production to any other parties, including its subsidiaries or any other third parties inside and/or out of Japan, and the IPD products are consigned to such subcontracting parties thereat, such party is obligated to monitor and control the quantity of IPD products to prevent any of the aforementioned re-sale, loan or sample shipments from taking place.
- 4) In the event that any actual or threatened breach or violation of any of the above mentioned 2) or 3) has occurred or is about to occur, our company will hold all shipments of IPD products and may request the customer to disclose necessary documentation describing the status of our end-users and/or distribution channels.

Note) The products of MIP50**, MIP51**, and MIP7** are excluded from above-mentioned precautions, 1) to 3).

Attached table "IPD availability by customer"

Parts No.			Companies/areas to which products can be sold	Companies/areas to which products cannot be sold	Application
MIP01** MIP2** MIP9A**	MIP02** MIP3** MIP9L**	MIP1** MIP4**	<ul style="list-style-type: none"> · Japanese companies in Japan · Japanese companies in Asia (50% or more owned) 	<ul style="list-style-type: none"> · Companies in European and American countries · Asian companies in Asia · Other local companies 	<ul style="list-style-type: none"> · For power supply · For DC-DC converter
MIP00** MIP55** MIP803/804	MIP52** MIP56** MIP816/826	MIP53** MIP5S** MIP9E**	<ul style="list-style-type: none"> · Japanese companies in Japan · Japanese companies in Asia (50% or more owned) · Asian companies in Asia 	<ul style="list-style-type: none"> · Companies in European and American countries · Other local companies 	<ul style="list-style-type: none"> · For power supply · For EL driver · For LED lighting driver
MIP50**	MIP51**	MIP7**	<ul style="list-style-type: none"> · No restrictions in terms of contract 	<ul style="list-style-type: none"> · No restrictions in terms of contract 	<ul style="list-style-type: none"> · For lamp driver/ car electronics accessories

Note) For details, contact our sales division.