MG6403WZ

650V 80A Insulated Gate Bipolar Transistor

Datasheet

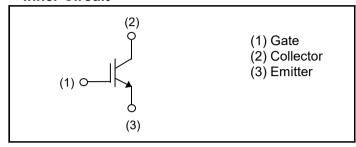
V_{CES}	650V
I _{C (Nominal)}	80A
V _{CE(sat) (Typ.)}	1.5V
Max. Possible Chips per Wafer	444pcs

● Outline Wafer

Features

- 1) Trench Light Punch Through Type
- 2) Low Collector Emitter Saturation Voltage
- 3) High Speed Switching & Low Switching Loss
- 4) Short Circuit Withstand Time 2µs

●Inner Circuit



Application

Solar Inverter

UPS

Welding

ΙH

PFC

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

	<u> </u>		
Parameter	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CES}	650	V
Gate - Emitter Voltage	V_{GES}	±30	V
Collector Current	I _C *1	*1)	Α
Pulsed Collector Current	I _{CP} *2	320	Α
Operating Junction Temperature	T _j	-40 to +175	°C

^{*1} Depending on thermal properties of assembly

^{*2} Pulse width limited by $T_{jmax.}$

●Design Assurance

Parameter Symbol Conditions		Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
		$V_{CC} \le 360V$,				
Short Circuit Withstand Time	t _{sc} *3	$V_{CC} \le 360V$, $V_{GE} = 15V$, $T_i = 25^{\circ}C$	2	-	-	μs
		T _j = 25°C				
		$I_C = 320A, V_{CC} = 520V,$				
Reverse Bias Safe Operating Area	RBSOA*3	$I_C = 320A$, $V_{CC} = 520V$, $V_P = 650V$, $V_{GE} = 15V$, $R_G = 100\Omega$, $T_j = 175^{\circ}C$	FULL SQUARE		-	
		$R_G = 100\Omega, T_j = 175^{\circ}C$				

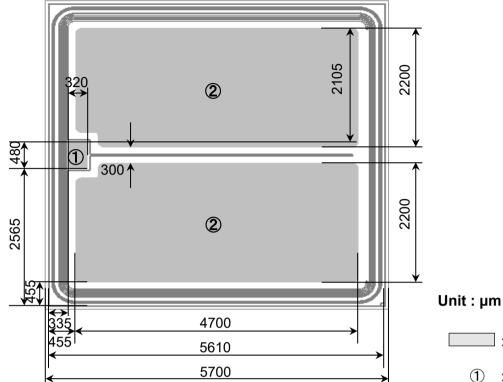
^{*3} Design assurance without measurement

●Electrical Characteristics (at T_j = 25°C unless otherwise specified, in case of TO-247N package)

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Parameter	Symbol Conditions		Values			Unit
5,		Min.	Тур.	Max.		
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	>
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	-	-	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V$, $V_{CE} = 0V$	-	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 57.1 \text{mA}$	5.0	6.0	7.0	V
		$I_C = 80A, V_{GE} = 15V,$				
Collector - Emitter Saturation Voltage	V _{CE(sat)} *3	T _j = 25°C	-	1.5	1.9	V
Voltago		T _j = 175°C	-	1.85	-	
Input Capacitance	C _{ies}	V _{CE} = 30V,	-	4810	-	
Output Capacitance	C _{oes}	V _{GE} = 0V,	-	184	-	pF
Reverse transfer Capacitance	C_{res}	f = 1MHz	-	79	-	*
Total Gate Charge	Q_g	V _{CE} = 400V,	-	171	-	
Gate - Emitter Charge	Q_ge	I _C = 80A,	-	33	-	nC
Gate - Collector Charge	Q_gc	V _{GE} = 15V	-	59	-	

^{*3} Design assurance without measurement

●Chip Information

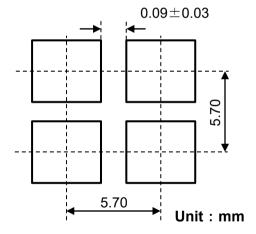


: Pad Area

① : Gate Bonding Pad

② : Emitter Bonding Pad

Backside : Collector



Wafer Size	150mm	
Wafer Thickness	0.07±0.01mm	
Chip Size	5.70mm×5.70mm	
Cut Line Width	0.09±0.03mm	
Top Side Metallization	AlSiCu:4.4µm	
Back Side Metallization	Ti/Ni:0.4μm/Au:0.05μm	
Passivation	Polyimide	

•Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

This chip data sheet refers to the device data sheet	RGTVX6TS65
This chip data sheet refers to the device data sheet	KG1VX01303

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSIII	CLASS II b	CLASSIII
CLASSIV		CLASSⅢ	CLASSIII

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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