

# MDIS2N65B

## N-Channel MOSFET 650V, 1.95A, 4.5Ω

### General Description

The MDIS2N65B uses advanced MagnaChip's MOSFET technology, which provides low on-state resistance, high switching performance and excellent quality.

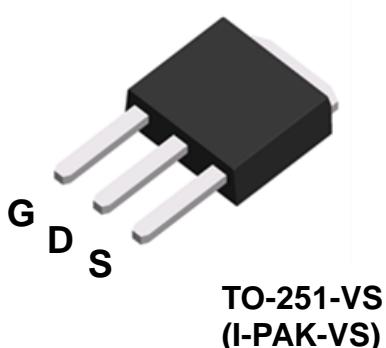
MDIS2N65B is suitable device for SMPS, compact ballast, battery charger and general purpose applications.

### Features

- $V_{DS} = 650V$
- $I_D = 1.95A$  @  $V_{GS} = 10V$
- $R_{DS(ON)} \leq 4.5\Omega$  @  $V_{GS} = 10V$

### Applications

- Power supply
- Battery charger
- Ballast



### Absolute Maximum Ratings ( $T_a = 25^\circ C$ )

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DSS}$	650	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	1.95	A
$T_c=25^\circ C$		1.23	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	7.8	A
Power Dissipation	$P_D$	44	W
$T_c=25^\circ C$		0.353	W/ $^\circ C$
Repetitive Avalanche Energy <sup>(1)</sup>	$E_{AR}$	4.4	mJ
Peak Diode Recovery $dv/dt$ <sup>(3)</sup>	$dV/dt$	4.5	V/ns
Single Pulse Avalanche Energy <sup>(4)</sup>	$E_{AS}$	115	mJ
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~150	$^\circ C$

### Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	62.5	$^\circ C/W$
Thermal Resistance, Junction-to-Case <sup>(1)</sup>	$R_{\theta JC}$	2.83	

## Ordering Information

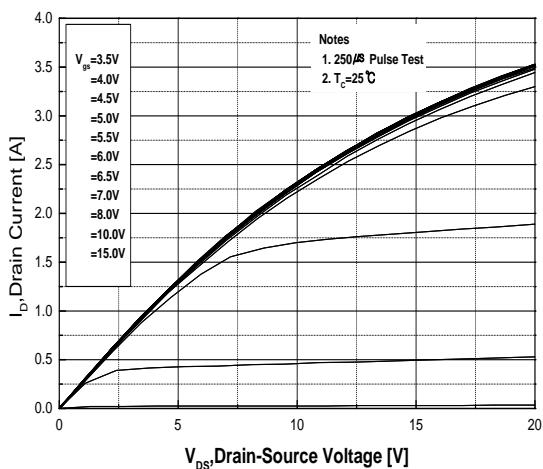
Part Number	Marking	Temp. Range	Package	Packing	RoHS Status
MDIS2N65BTH	MDI2N65B	-55~150°C	TO-251-VS (I-PAK-VS)	Tube	Halogen Free

## Electrical Characteristics (Ta = 25°C)

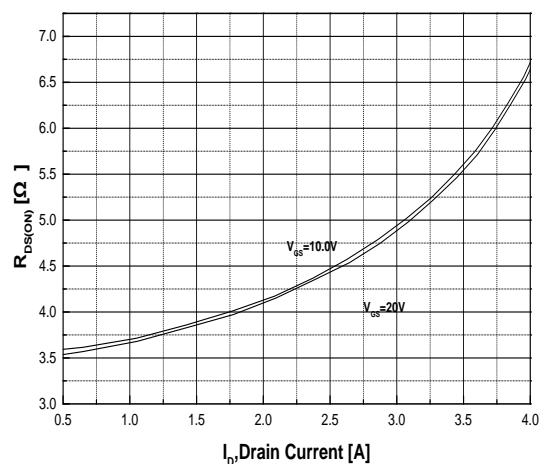
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	650	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V			1	μA
		V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V	-	-	10	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V	-	-	100	nA
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.975A		3.6	4.5	Ω
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = 30V, I <sub>D</sub> = 0.975A	-	1.3	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 520V, I <sub>D</sub> = 2.0A, V <sub>GS</sub> = 10V	-	7.3		nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.4		
Gate-Drain Charge	Q <sub>gd</sub>		-	3.1		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	296		pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	2		
Output Capacitance	C <sub>oss</sub>		-	34.7		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 325V, I <sub>D</sub> = 2.0A, R <sub>G</sub> = 25Ω	-	6.2		ns
Rise Time	t <sub>r</sub>		-	24.2		
Turn-Off Delay Time	t <sub>d(off)</sub>		-	82		
Fall Time	t <sub>f</sub>		-	50		
<b>Drain-Source Body Diode Characteristics</b>						
Maximum Continuous Drain to Source Diode Forward Current	I <sub>S</sub>	I <sub>S</sub> = 1.95 A, V <sub>GS</sub> = 0V	-	-	1.95	A
Source-Drain Diode Forward Voltage	V <sub>SD</sub>		-		1.4	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	210		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.86		μC

Note :

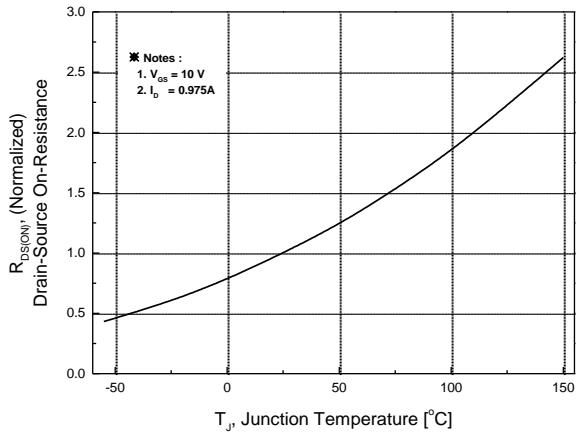
1. Pulse width is based on R<sub>θJC</sub> & R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
3. I<sub>SD</sub> ≤2.0A, di/dt≤200A/us, V<sub>DD</sub>≤BV<sub>DSS</sub>, Starting T<sub>J</sub>=25°C
4. L=53.1mH, I<sub>AS</sub>=2.0A, V<sub>DD</sub>=50V, R<sub>g</sub>=25Ω, Starting T<sub>J</sub>=25°C



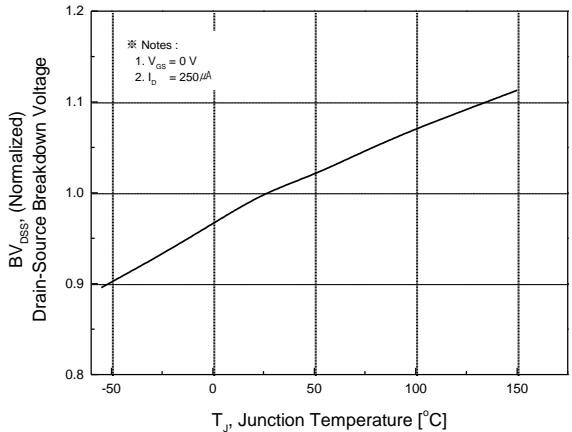
**Fig.1 On-Region Characteristics**



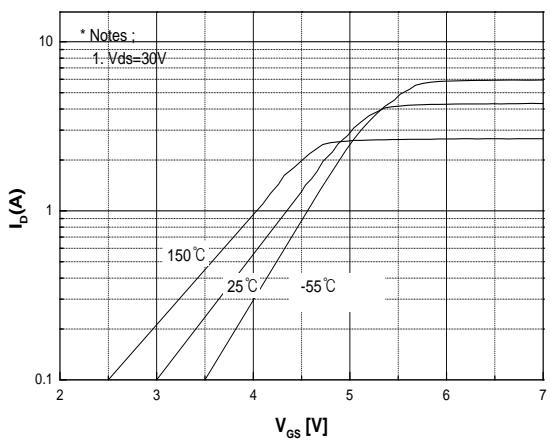
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



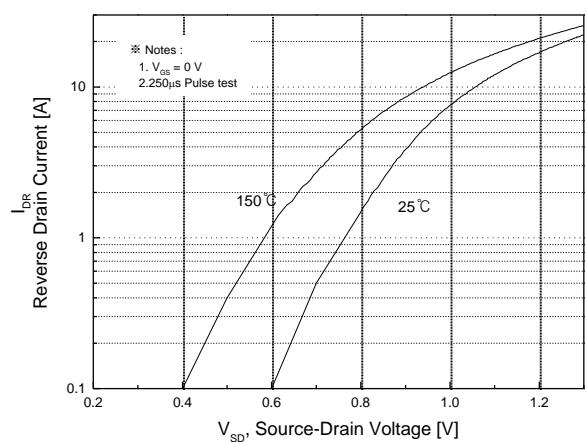
**Fig.3 On-Resistance Variation with Temperature**



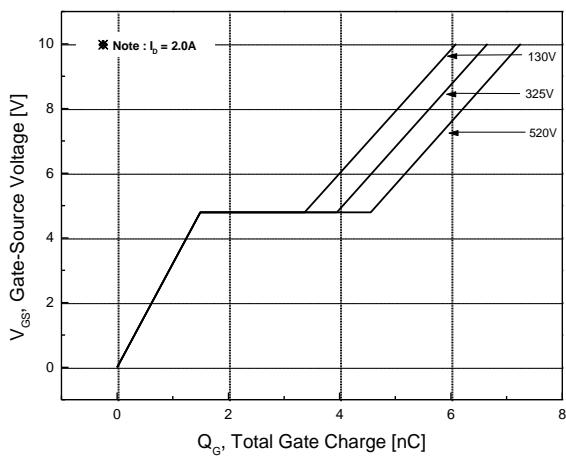
**Fig.4 Breakdown Voltage Variation vs. Temperature**



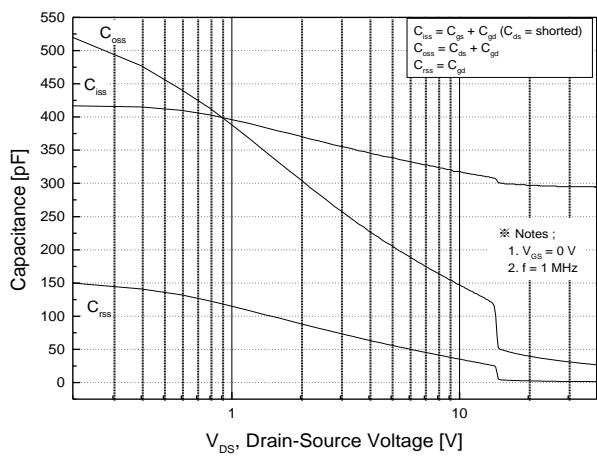
**Fig.5 Transfer Characteristics**



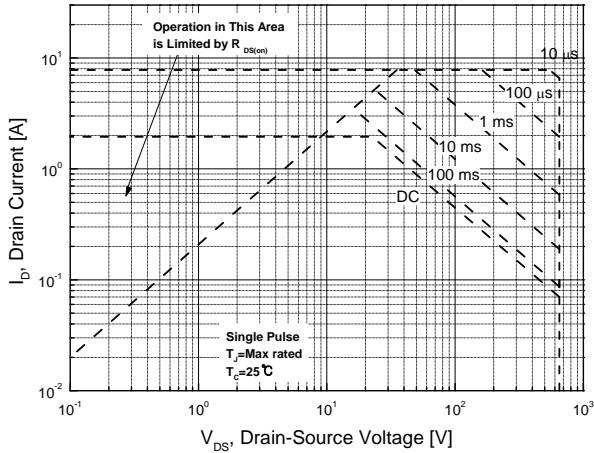
**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**



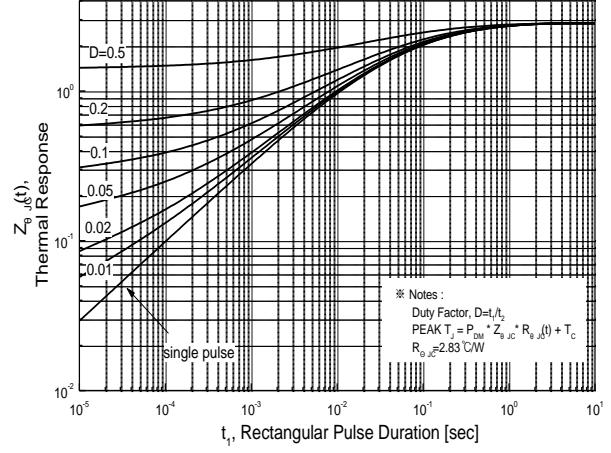
**Fig.7 Gate Charge Characteristics**



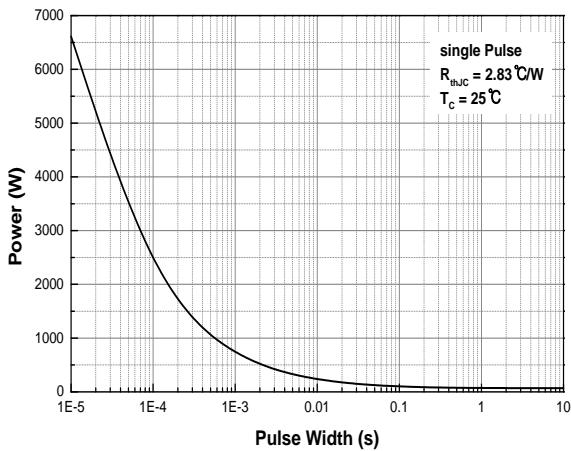
**Fig.8 Capacitance Characteristics**



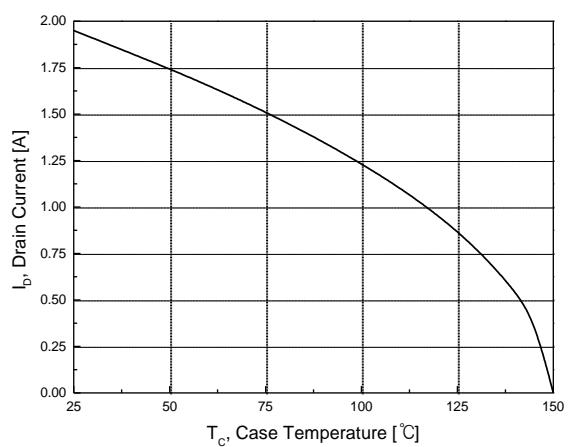
**Fig.9 Maximum Safe Operating Area**



**Fig.10 Transient Thermal Response Curve**



**Fig.11 Single Pulse Maximum Power Dissipation**

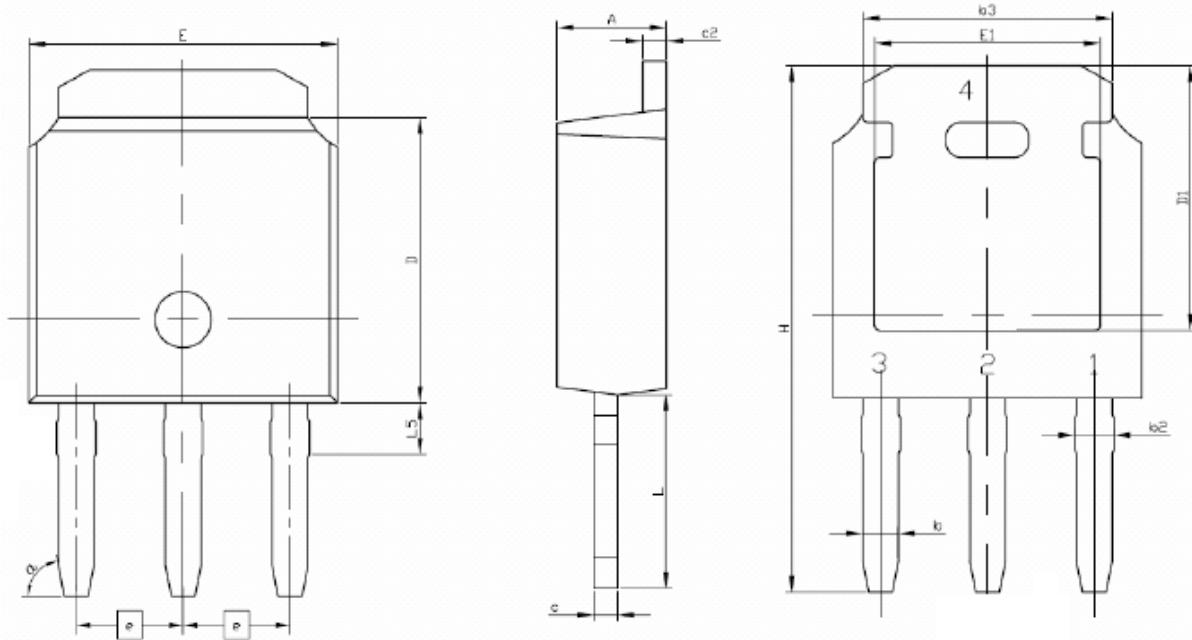


**Fig.12 Maximum Drain Current vs. Case Temperature**

## Physical Dimensions

### TO-251-VS (IPAK-VS), 3L

Dimensions are in millimeters, unless otherwise specified



Symbol	MILLIMETERS	
	Minimum	Maximum
A	2.18	2.39
b	0.64	0.89
b2	0.76	1.14
b3	4.95	5.46
c	0.40	0.61
c2	0.40	0.61
D	5.97	6.223
D1	5.10	-
e	2.286 BSC	
E	6.35	6.73
E1	4.32	-
H	10.26	11.45
L	3.98	4.28
L5	-	1.23

**DISCLAIMER:**

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