



# MD1803DFP

High voltage NPN Power transistor for standard definition CRT display

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## Features

- State-of-the-art technology:
  - Diffused collector “enhanced generation”
- Stable performance versus operating temperature variation
- Low base drive requirement
- Tight  $h_{FE}$  range at operating collector current
- Fully insulated power package UL compliant
- Integrated free wheeling diode

## Applications

- Horizontal deflection output for TV

## Description

The MD1803DFP is manufactured using diffused collector in planar technology adopting new and enhanced high voltage structure. The new MD product series show improved silicon efficiency bringing updated performance to the horizontal deflection stage.

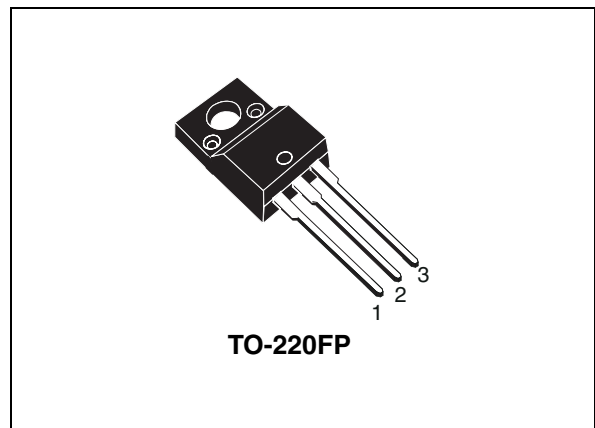


Figure 1. Internal schematic diagram

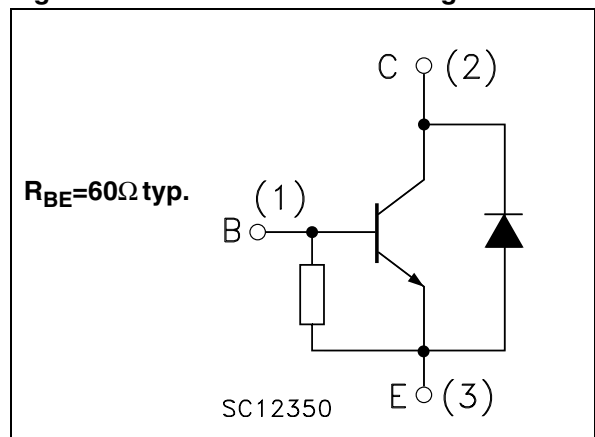


Table 1. Device summary

Order code	Marking	Package	Packing
MD1803DFP	MD1803DFP	TO-220FP	Tube

# 1 Electrical ratings

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**Table 2. Absolute maximum rating**

Symbol	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage ( $V_{BE} = 0$ )	1500	V
$V_{CEO}$	Collector-emitter voltage ( $I_B = 0$ )	700	V
$V_{EBO}$	Emitter-base voltage ( $I_C = 0$ )	10	V
$I_C$	Collector current	10	A
$I_{CM}$	Collector peak current ( $t_P < 5\text{ms}$ )	15	A
$I_B$	Base current	5	A
$P_{TOT}$	Total dissipation at $T_c = 25^\circ\text{C}$	40	W
$V_{isol}$	Insulation withstand voltage (rms) from all three leads to external heatsink	1500	V
$T_{stg}$	Storage temperature	-65 to 150	$^\circ\text{C}$
$T_J$	Max. operating junction temperature	150	$^\circ\text{C}$

**Table 3. Thermal data**

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case max	3.125	$^\circ\text{C/W}$

## 2 Electrical characteristics

( $T_{CASE} = 25^{\circ}\text{C}$ ; unless otherwise specified)

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**Table 4. Electrical characteristics**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$I_{CES}$	Collector cut-off current ( $V_{BE} = 0$ )	$V_{CE} = 1500\text{ V}$ $V_{CE} = 1500\text{ V}$ $T_C = 125^{\circ}\text{C}$			0.2 2	mA mA
$I_{EBO}$	Emitter cut-off current ( $I_C = 0$ )	$V_{EB} = 5\text{ V}$	40		120	mA
$V_{(BR)EBO}$	Emitter-base breakdown voltage ( $I_C = 0$ )	$I_E = 700\text{ mA}$	10			V
$V_{CE(sat)}^{(1)}$	Collector-emitter saturation voltage	$I_C = 5\text{ A}$ $I_B = 1.25\text{ A}$			2	V
$V_{BE(sat)}^{(1)}$	Base-emitter saturation voltage	$I_C = 5\text{ A}$ $I_B = 1.25\text{ A}$			1.2	V
$h_{FE}^{(1)}$	DC current gain	$I_C = 1\text{ A}$ $V_{CE} = 5\text{ V}$ $I_C = 5\text{ A}$ $V_{CE} = 1\text{ V}$ $I_C = 5\text{ A}$ $V_{CE} = 5\text{ V}$	5.5	18 5	7.5	
$V_F$	Diode forward voltage	$I_F = 5\text{ A}$			1.6	V
$t_s$ $t_f$	Inductive load Storage time Fall time	$I_C = 4\text{ A}$ $f_h = 16\text{ KHz}$ $I_{B(on)} = 0.6\text{ A}$ $V_{BE(off)} = -2.7\text{ V}$ $L_{BB(off)} = 4.5\text{ }\mu\text{H}$		2.5 0.3	3 0.6	$\mu\text{s}$ $\mu\text{s}$

1. Pulsed duration = 300 ms, duty cycle  $\leq 1.5\%$ .

2.1 Electrical characteristics (curve)

Figure 2. Safe operating area

Figure 3. Derating curve

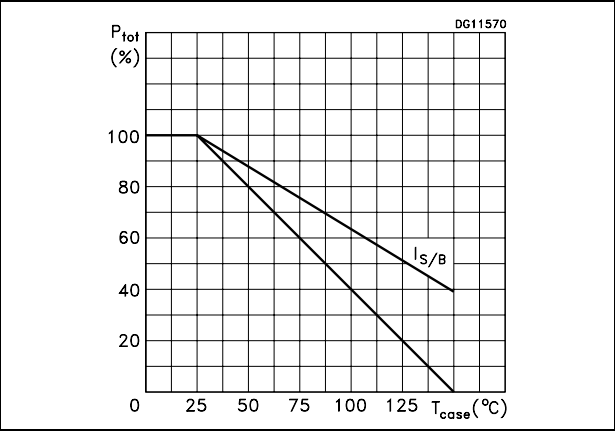
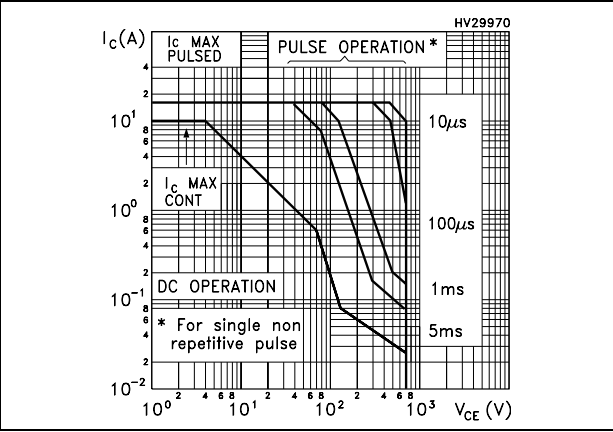


Figure 4. Output characteristics

Figure 5. Reverse biased SOA

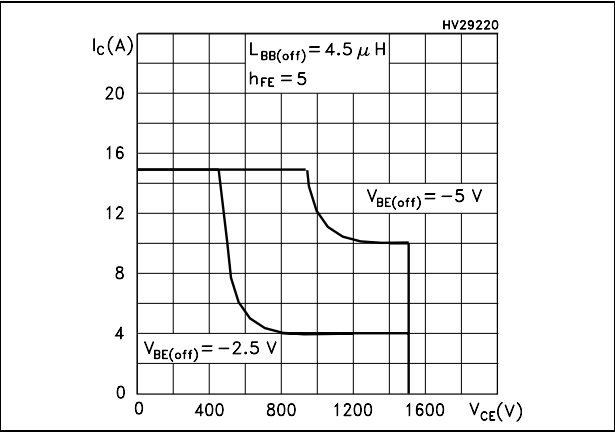
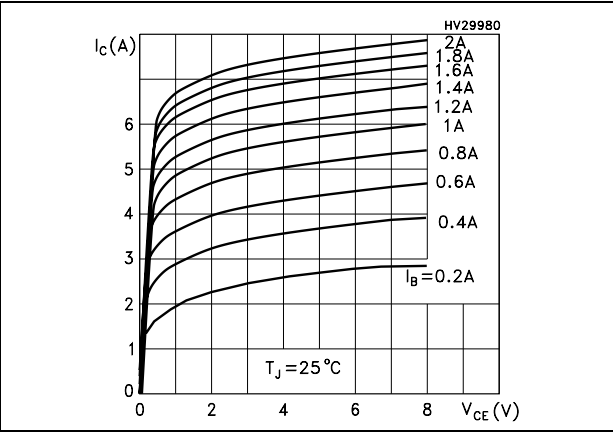


Figure 6. DC current gain

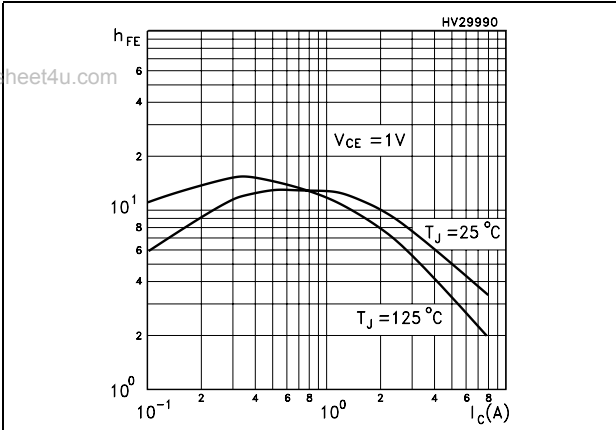


Figure 7. DC current gain

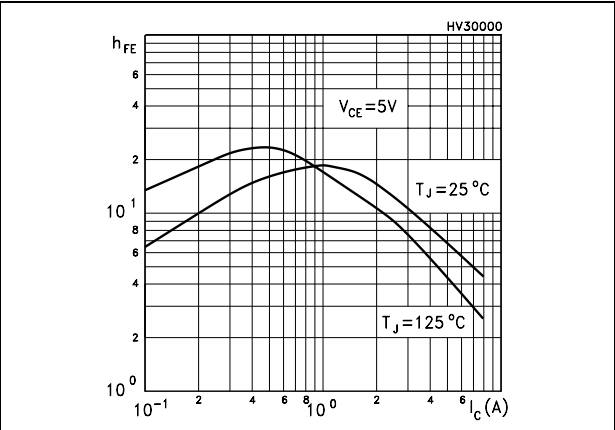


Figure 8. Collector-emitter saturation voltage

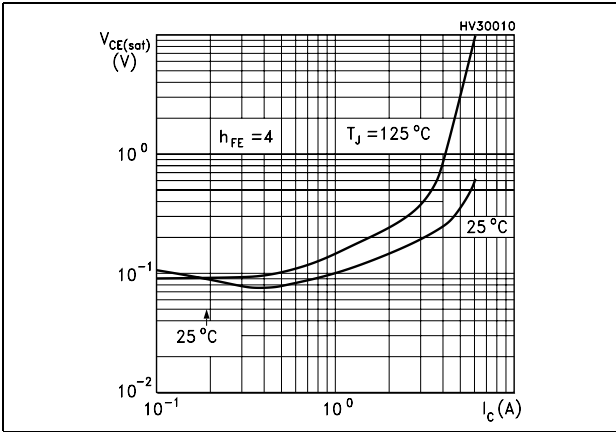


Figure 9. Base-emitter saturation voltage

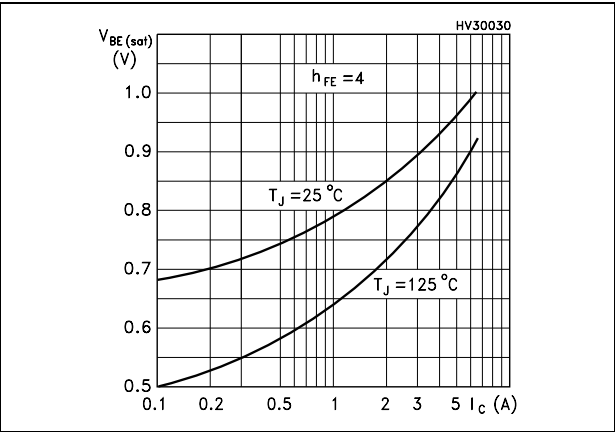


Figure 10. Power losses

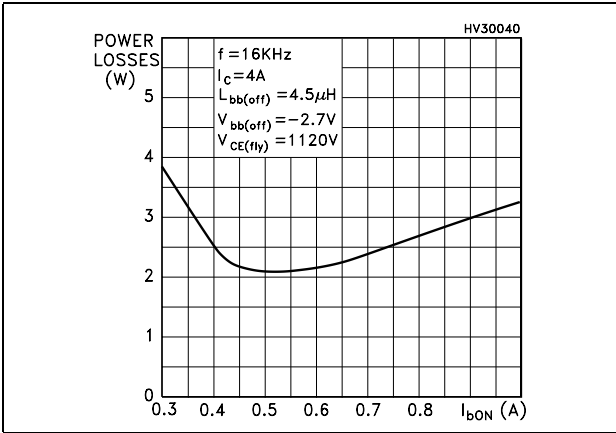
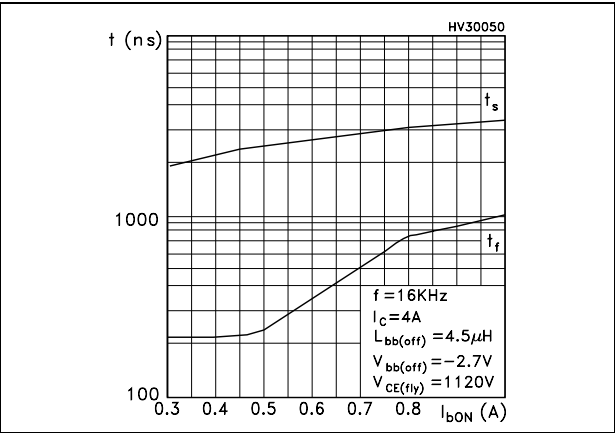
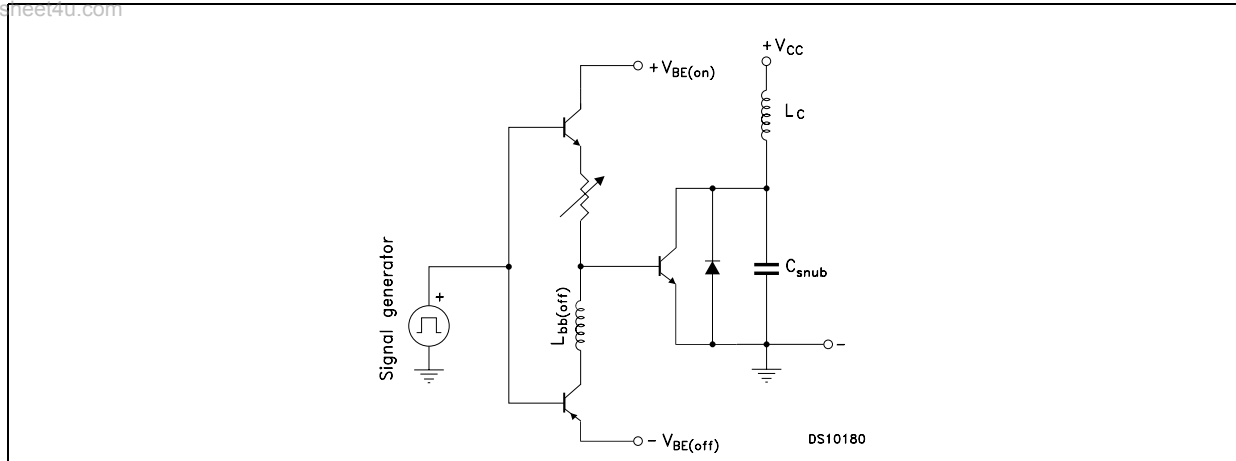


Figure 11. Inductive load switching time

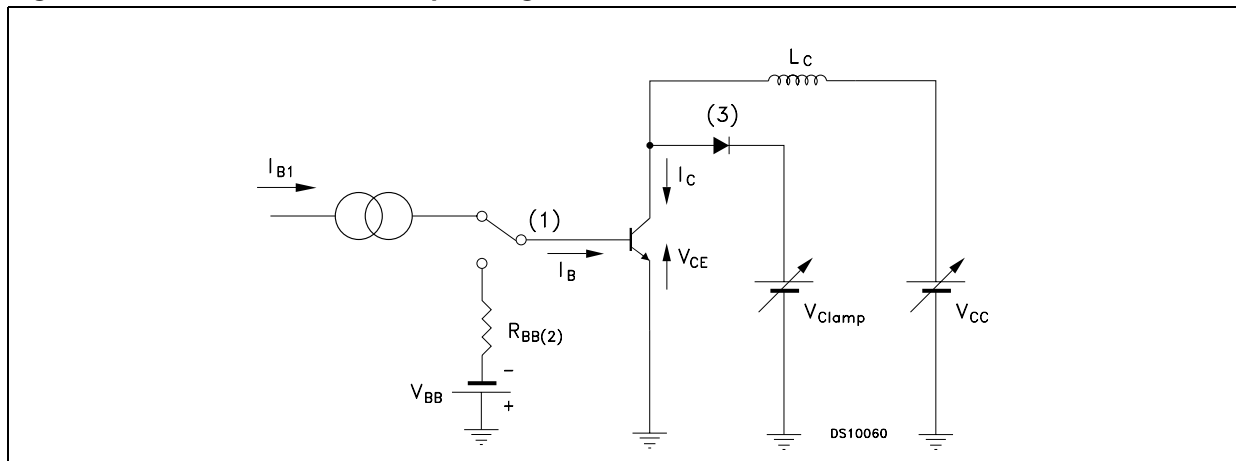


### 3 Test circuit

**Figure 12. Power losses and inductive load switching test circuit**



**Figure 13. Reverse biased safe operating area test circuit**



## 4 Package mechanical data

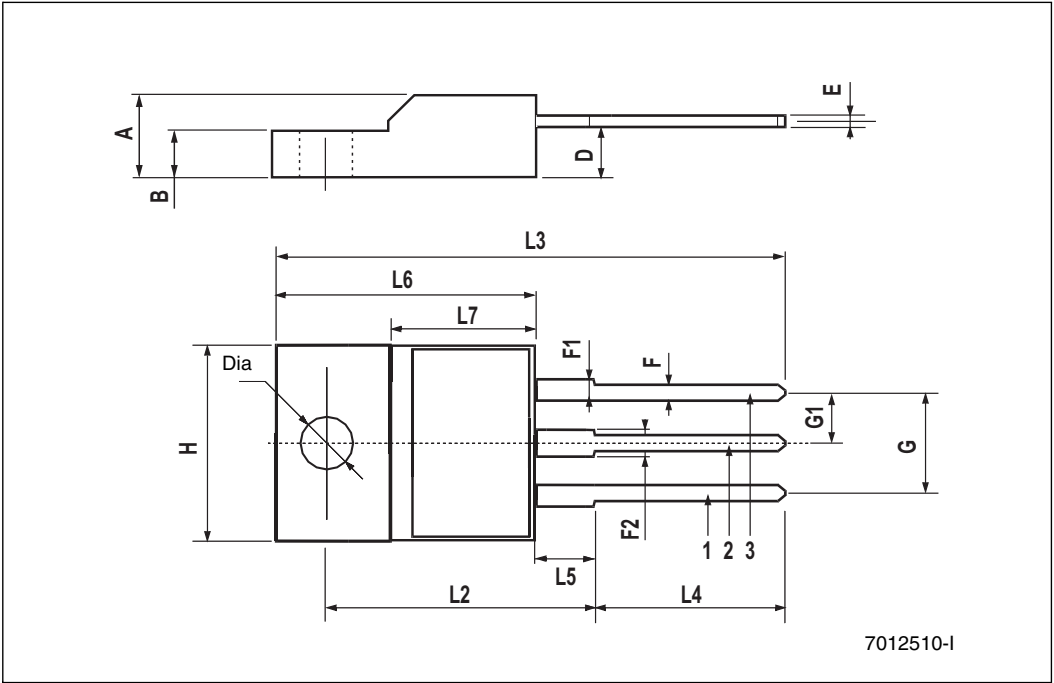
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TO-220FP mechanical data

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Dim.	mm.			inch		
	Min.	Typ	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.70	0.017		0.027
F	0.75		1.00	0.030		0.039
F1	1.15		1.50	0.045		0.067
F2	1.15		1.50	0.045		0.067
G	4.95		5.20	0.195		0.204
G1	2.40		2.70	0.094		0.106
H	10		10.40	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.80		10.60	0.385		0.417
L5	2.9		3.6	0.114		0.141
L6	15.90		16.40	0.626		0.645
L7	9		9.30	0.354		0.366
Dia	3		3.2	0.118		0.126



## 5 Revision history

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**Table 5. Document revision history**

Date	Revision	Changes
27-May-2008	1	First release

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