

## MCSO6E family package 3.5×2.2mm T range up to 210°C



**DIMENSIONS** Package: **Recommended Solder Pad:** Bottom side 3,5 1,3 1,3 1,3 1,1 2,2 3 2 pin 1 ED pin 2 GND pin 3 Fout pin 4 Vdd All dimensions in mm typical

# SMT Clock oscillator in ceramic package Fundamental quartz mode frequency High shock and vibration resistance Wide temperature range Low aging

Ultra low MSL
Very fast start-up
Excellent solderability
Swiss made quality
Customer specification on request

ELECTRICAL
CHARACTERISTICS AT +25°C

## **DESCRIPTION:**

This SMD oscillator in ceramic package has been specially designed for surface mount using infrared, vapor phase or epoxy techniques.

### **APPLICATIONS:**

- Downhole and Well drilling equipments
- Avionics
- Airbone equipments
- Geothermal equipments

The MCSO6's are supplied on trays (208 pcs / tray)
For pick-and-place equipment, the parts are available in 12mm tapes
with 250 parts min
1000 parts min

Over temperature range C = -55 to +125°C (see ordering info) Including 2)*  Frequency stability Over temperature range E = -55 to +150°C (see ordering info) Including 2)*  Frequency stability Over temperature range D = -55 to +175°C (see ordering info) Including 2)*  Frequency stability Over temperature range D = -55 to +175°C (see ordering info) Including 2)*  Frequency stability Over temperature range G = -55 to +210°C (see ordering info) Including 2)*  Supply voltage $\pm$ 5% 1)*  Vdd 2.5 / 3.3 / 5 V  Input current Idd see table 1  Output signal HC-MOS compatible  Symmetry at Vdd/2  Rise & fall time For F=32.768 kHz rise & fall time $\leq$ 150ns (load 15pf 20% to 80%)  Level "0" & "1" $<$ 0.4>Vdd-0.5 V  Start-up time	Frequency stability			
Frequency stabilityOver temperature range E = -55 to +150°C (see ordering info) $\Delta F/F$ ≤ ± 150ppmFrequency stability Over temperature range D = -55 to +175°C (see ordering info) $\Delta F/F$ ≤ ± 300ppmFrequency stability Over temperature range G = -55 to +210°C (see ordering info) $\Delta F/F$ ≤ ± 400ppmSupply voltage ± 5%1)*Vdd2.5 / 3.3 / 5VInput currentIddsee table 1Output signalHC-MOS compatibleSymmetry at Vdd/240 / 60%Rise & fall timeFor F=32.768 kHz rise & fall time ≤ 150ns (load 15pf 20% to 80%)≤7nsLevel "0" & "1"<0.4>Vdd-0.5V		ΔF/F	≤ ± 100	ppm
Over temperature range E = -55 to +150°C (see ordering info) Including 2)*  Frequency stability Over temperature range D = -55 to +175°C (see ordering info) Including 2)*  Frequency stability Over temperature range G = -55 to +210°C (see ordering info) Including 2)*  Frequency stability Over temperature range G = -55 to +210°C (see ordering info) Including 2)*  Supply voltage $\pm$ 5% 1)*  Vdd 2.5 / 3.3 / 5 V  Input current Idd see table 1  Output signal HC-MOS compatible  Symmetry at Vdd/2 40 / 60 %  Rise & fall time For F=32.768 kHz rise & fall time $\leq$ 150ns (load 15pf 20% to 80%)  Level "0" & "1" $<$ 0.4>Vdd-0.5 V				
(see ordering info) Including 2)*  Frequency stability Over temperature range D = -55 to +175°C (see ordering info) Including 2)*  Frequency stability Over temperature range G = -55 to +210°C (see ordering info) Including 2)*  Supply voltage $\pm$ 5% 1)*  Vdd 2.5 / 3.3 / 5 V  Input current Idd see table 1  Output signal HC-MOS compatible  Symmetry at Vdd/2 40 / 60 %  Rise & fall time For F=32.768 kHz rise & fall time $\leq$ 150ns (load 15pf 20% to 80%)  Level "0" & "1" $<$ 0.4>Vdd-0.5 V				
Over temperature range D = -55 to +175°C (see ordering info) Including 2)*  Frequency stability Over temperature range G = -55 to +210°C (see ordering info) Including 2)*  Supply voltage $\pm$ 5% 1)* Vdd 2.5 / 3.3 / 5 V  Input current Idd see table 1  Output signal HC-MOS compatible  Symmetry at Vdd/2 40 / 60 %  Rise & fall time For F=32.768 kHz rise & fall time $\leq$ 150ns (load 15pf 20% to 80%)  Level "0" & "1" $<$ 0.4>Vdd-0.5 V		ΔF/F	≤ ± 150	ppm
	Frequency stability			
Over temperature range G = -55 to +210 °C (see ordering info) Including 2)* $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		ΔF/F	≤ ± 300	ppm
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Frequency stability			
Input current         Idd         see table 1           Output signal         HC-MOS compatible           Symmetry at Vdd/2         40 / 60         %           Rise & fall time         For F=32.768 kHz rise & fall time ≤ 150ns (load 15pf 20% to 80%)         ≤7         ns           Level "0" & "1"         <0.4>Vdd-0.5         V		ΔF/F	≤ ± 400	ppm
Output signal       HC-MOS compatible         Symmetry at Vdd/2       40 / 60       %         Rise & fall time       50       50       10         For F=32.768 kHz rise & fall time ≤ 150ns (load 15pf 20% to 80%)       50       10	Supply voltage ± 5% 1)*	Vdd	2.5 / 3.3 / 5	V
Symmetry at Vdd/2       40 / 60       %         Rise & fall time       50       50       10	Input current	ldd	see table 1	
Rise & fall time For F=32.768 kHz rise & fall time ≤ 150ns (load 15pf 20% to 80%)  Level "0" & "1"  <0.4>Vdd-0.5  V	Output signal		HC-MOS compatible	
For F=32.768 kHz rise & fall time ≤ 150ns (load 15pf 20% to 80%)  Level "0" & "1" <0.4>Vdd-0.5 V	Symmetry at Vdd/2		40 / 60	%
(load 15pf 20% to 80%) Level "0" & "1"  <0.4>Vdd-0.5  V	Rise & fall time			
Level "0" & "1" <0.4>Vdd-0.5 V			≤7	ns
Start-up time t <5 ms	Level "0" & "1"		<0.4>Vdd-0.5	V
	Start-up time	t	<5	ms
Load min / max 3/47 pF	Load min / max		3/47	pF

<sup>\* 1)</sup> C = 47nF ceramic must be connected between GND & Vdd Operable over 2.3 to 5.5V

<sup>\* 2)</sup> adjustment at +25°C, long term aging 1 year over supply voltage ±5% and over load min to max

**TABLE 1: Idd** (Without load)

	Frequency	F= 32 kHz	F=< 10MHz	≤ 20MHz	> 20MHz
W	=Vdd = 2.5V	< 300µA	< 2mA	< 3mA	< 15mA
V	=Vdd = 3.3V	< 1mA	< 4mA	< 5mA	< 20mA
bla	nk=Vdd = 5V	< 2mA	< 6mA	< 7mA	< 30mA

### **STANDARD FREQUENCIES:**

Frequency «MHz»						
4	8	10	12	16	20	24
40	50	60				
Other frequencies from 10 kHz MHz to 60 MHz on request						

**ENVIRONMENTAL CHARACTERISTICS:** 

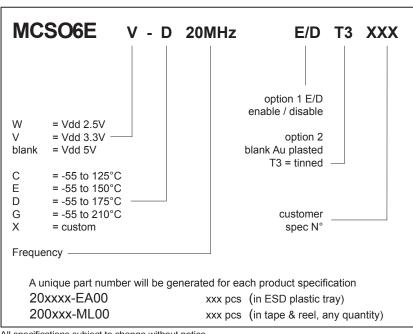
Storage temp. range	-65 to +125°C
Vibration resistance	10 to 2000Hz / 40g
Shocks resistance	10000g / 0.3ms / ½ sine

**TERMINATIONS AND** PROCESSING:

Reflow soldering	+260°C / 10s max	
Package	Ceramic 3.5 x 2.2 x 1.2mm	
Lids	Ceramic	
Terminations option T3 on request (not available on G temperature range)	with tinned Ag/Cu/Zn	
E/D option 1 on request Reaction time < 1µs	Pin 1 open → Pin 3 Clock H → Clock L → Low	

- No power E/D function (pin 1) before Vdd is setting on
- E/D option not available for F < 500 kHz
- E/D option on request (very low consumption in disable mode).

### PRODUCT DESCRIPTION AND **ORDERING INFORMATION:**



All specifications subject to change without notice.



Micro Crystal AG Mühlestrasse 14 CH-2540 Grenchen Switzerland

+41 32 655 82 82 Tel. +41 32 655 82 83 Fax sales@microcrystal.com www.microcrystal.com