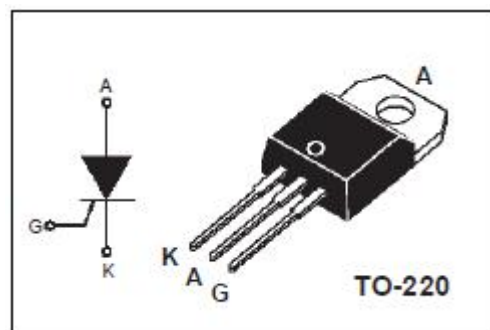


isc Thyristors

MCR310-10G

DESCRIPTION

- With TO-220 packaging
- High heat dissipation and durability
- Thermowatt construction for low thermal
- Glass passivated junctions and center gate fire for greater parameter uniformity and stability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

- Switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	800	V
V_{RRM}	Repetitive peak reverse voltage	800	V
$I_{\text{T(RMS)}}$	RMS on-state current $T_c=70^\circ\text{C}$	10	A
I_{TSM}	Surge non-repetitive on-state current (1/2 cycle,sine wave;60HZ; $T_c=125^\circ\text{C}$)	100	A
$P_{\text{G(AV)}}$	Average gate power dissipation $T_p=8.3\text{ms}; T_c=70^\circ\text{C}$	0.75	W
T_j	Operating junction temperature	-40~110	$^\circ\text{C}$
T_{stg}	Storage temperature	-40~150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_{\text{RM}}=V_{\text{RRM}}$ $V_{\text{DM}}=V_{\text{DRM}}$ $T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0.01	mA
I_{DRM}	Repetitive peak off-state current			5.0	
V_{TM}	On-state voltage	$I_{\text{TM}}=20\text{A}$		2.2	V
I_{GT}	Gate-trigger current	$V_D=12\text{V}; R_L=100\Omega$		200	mA
V_{GT}	Gate-trigger voltage	$V_D=12\text{V}; R_L=100\Omega$		1.5	V
$R_{\text{th(j-c)}}$	Thermal resistance	Junction to case		2.2	$^\circ\text{C/W}$

NOTICE:

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