

Features

- Fully Automotive Qualified to AEC-Q101
- · Trench LV MOSFET Technology
- High Density Cell Design For Ultra Low RDS(on)
- · Moisture Sensitivity Level 1
- Halogen Free."Green"Device(Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

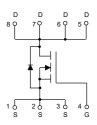
- Operating Junction Temperature Range: -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 55°C/W Junction to Ambient^(Note2)
- Thermal Resistance: 1.8°C/W Junction to Case

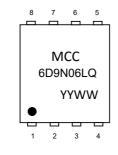
Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Volltage		V _{GS}	±20	V	
Continuous Drain Current	T _C =25°C		75	Α	
	T _C =100°C	I _D	53		
Pulsed Drain Current (Note3)		I _{DM}	300	Α	
Total Power Dissipation ^(Note4)		P _D	83	W	
Single Pulsed Avalanche Energy ^(Note 5)		E _{AS}	156	mJ	

Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-case thermal resistance.
- 5. TJ=25 °C, V_{DD}=50V, V_G=10V, L=0.5mH.

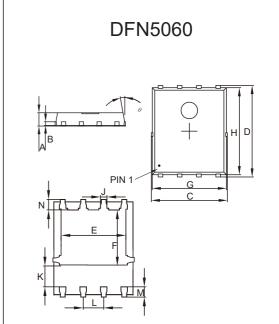
Internal Structure and Marking Code





4 codes in total AWWA YY is the year AWWA WW is the `^^\

N-CHANNEL MOSFET



	DIMENSIONS					
DIM -	INCHES		MM		NOTE	
	MIN	MAX	MIN	MAX	NOTE	
Α	0.031	0.047	0.80	1.20		
В	0.010		0.254		TYP.	
С	0.193	0.219	4.90	5.55		
D	0.232	0.250	5.90	6.35		
Е	0.148	0.167	3.75	4.25		
F	0.126	0.154	3.20	3.92		
G	0.189	0.213	4.80	5.40		
Н	0.222	0.239	5.65	6.06		
K	0.045	0.059	1.15	1.50		
J	0.012	0.020	0.30	0.50		
L	0.046	0.054	1.17	1.37		
M	0.012	0.028	0.30	0.71		
N	0.016	0.028	0.40	0.71		



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics	1		· ·	1	1	1	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1.3	1.8	2.3	V	
Drain-Source On-Resistance	D	V _{GS} =10V, I _D =20A		5.3	6.9	mΩ	
	R _{DS(on)}	V _{GS} =4.5V, I _D =10A		7	9.5		
Gate Resistance	R_{G}	f=1MHz, Open drain		1.6		Ω	
Diode Characteristics			<u>'</u>				
Continuous Body Diode Current	Is				75	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V	
Reverse Recovery Time	t _{rr}	I _F =20A,di/dt=100A/μs		23.3		ns	
Reverse Recovery Charge	Q _{rr}	1 F-20A, αι/αι-100A/μS		17.3		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			1736			
Output Capacitance	C _{oss}	V _{DS} =30V,V _{GS} =0V,f=1MHz		344		pF	
Reverse Transfer Capacitance	C _{rss}			3.5			
Total Gate Charge	Qg			27			
Gate-Source Charge	Q _{gs}	V _{DS} =30V,V _{GS} =10V,I _D =20A		5.6		nC	
Gate-Drain Charge	Q_{gd}			3.9			
Turn-On Delay Time	t _{d(on)}			12			
Turn-On Rise Time	t _r	V _{DD} =30V, V _{GS} =10V,		58			
Turn-Off Delay Time	t _{d(off)}	$R_G=2.7\Omega$, $I_D=20A$		27		ns	
Turn-Off Fall Time	t _f			5.8			



Curve Characteristics

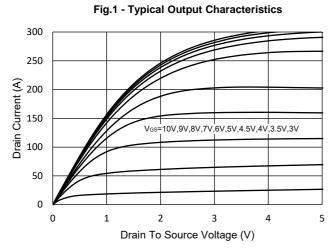


Fig.2 - Transfer Characteristic

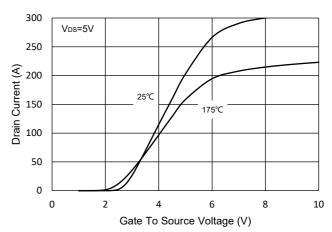


Fig.3 - R_{DS(ON)} - V_{GS}

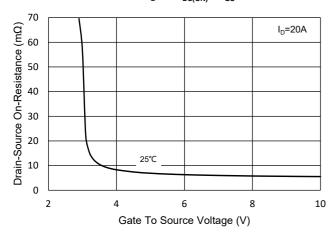


Fig.4 - R_{DS(ON)} - I_D

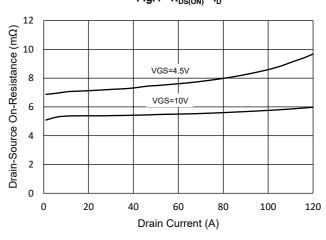


Fig.5 - Capacitance Characteristics

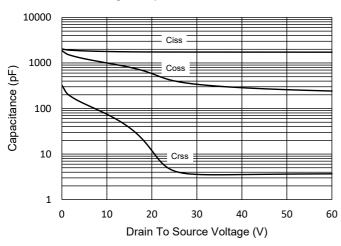
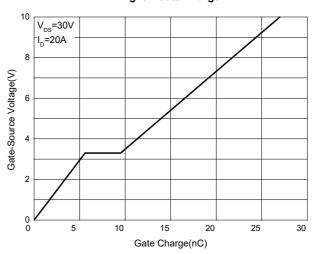
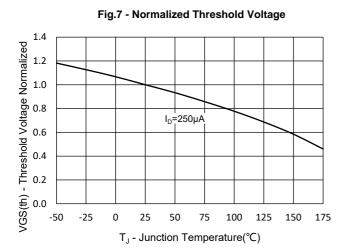


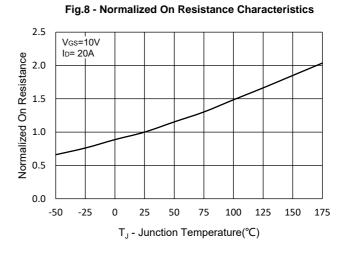
Fig. 6 - Gate Charge

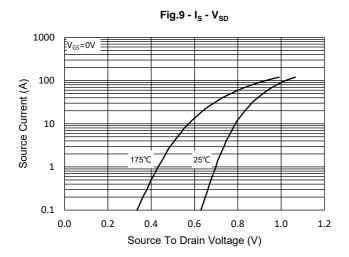


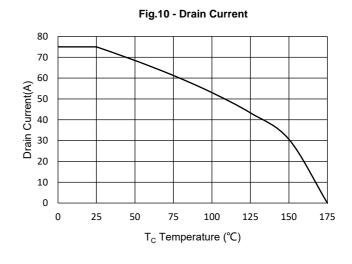


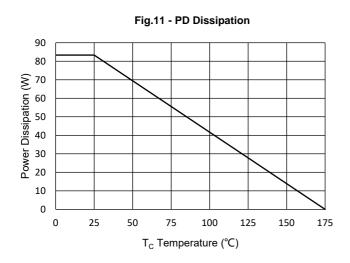
Curve Characteristics





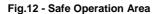








Curve Characteristics



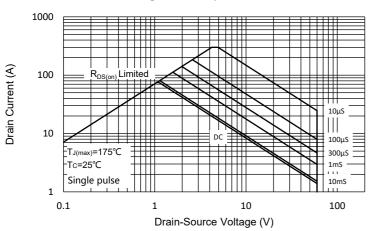
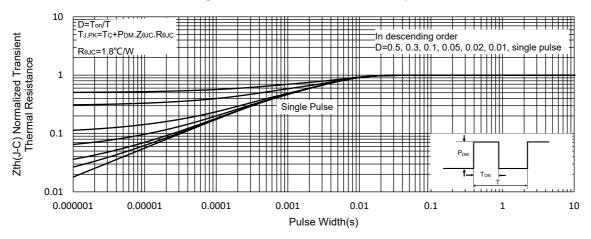


Fig.13 - Normalized Transient Thermal Impedance



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Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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